

Place C10 as close as possible to pin 4.
Place C11 as close as possible to pin 48.
Place C12 as close as possible to pin 1.
Place C13 as close as possible to pin 2.

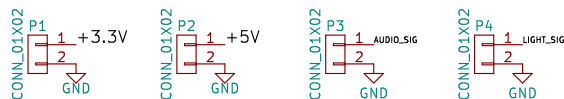
Text contrast controlled
with potentiometer.

LP_GPIO_3, 4, 5, 22, 23 configured
with internal pull-up resistors.
All other GPIOs configured
with internal pull-down resistors.

LCD brightness controlled
through PWM.

Place C21, C22, and C23
as close as possible
to pin 1, in that order.

915MHz antenna connects
through on-module SMA.



Sheet: /
File: base.sch

Title: Base

Size: A4 Date: May 26, 2017

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