# Cpr E 281 LAB08 ELECTRICAL AND COMPUTER ENGINEERING IOWA STATE UNIVERSITY

### Lab 8 Answer Sheet

Name and Std ID:	Lab Section:				
Date:					
PRELAB:					
<b>Q1</b> . Read section 2.2 and write the Verilog code for a 4-to-1 multiple	lever				

**Q2.** Design a 4-bit shifter as described in Section 3.1 of the lab description. Sketch the block diagram for your design showing the multiplexers, the inputs, outputs and the selectors to each multiplexer.

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**Q3.** Refer to Section 3.1 of the lab description and complete the following table before you come to the lab:

Х3	_	X1				X-3		S0	F3			F0
1	0	0	1	1	1	0	0	1	0	0	1	1
1	1	1	0	0	0	0	1	1				
1	1	1	0	0	1	1	0	0				
1	1	0	1	0	1	0	1	0				
1	0	1	1	0	1	1	0	1				

	TΑ	Initials:	
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#### LAB:

- **2.1** Hardware results demonstrate a good circuit. TA Initials: \_\_\_\_\_\_
- **2.2** Hardware results demonstrate correct code. TA Initials: \_\_\_\_\_
- **3.1** Hardware results demonstrate correct code. TA Initials: \_\_\_\_\_