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**Cpr E 281 LAB2**
ELECTRICAL AND COMPUTER
ENGINEERING
IOWA STATE UNIVERSITY

## Lab 2 Answer Sheet

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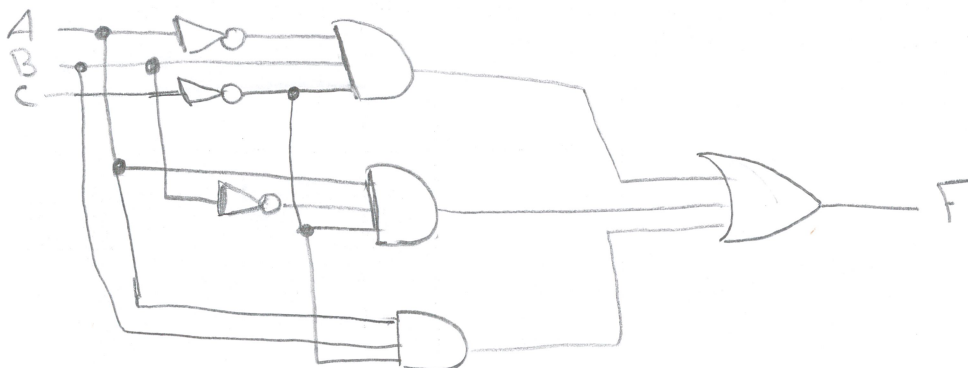
**Date:** Sept 6, 2016

## PRELAB:

**Q1.** Read section 3.0 and fill in the truth table below for *lab2step1*. Then use it to construct the SOP expression and draw the resulting circuit using logic gates.

| A | B | C | F |
|---|---|---|---|
| 0 | 0 | 0 | 0 |
| 0 | 0 | 1 | 0 |
| 0 | 1 | 0 | 1 |
| 0 | 1 | 1 | 0 |
| 1 | 0 | 0 | 1 |
| 1 | 0 | 1 | 0 |
| 1 | 1 | 0 | 1 |
| 1 | 1 | 1 | 0 |

**SOP Logic Expression:** $(\bar{A}B\bar{C}) + (A\bar{B}\bar{C}) + (AB\bar{C}) = F$

**Circuit Diagram:**



**TA Initials:** MS

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