## Cpr E 281 LAB2 ELECTRICAL AND COMPUTER ENGINEERING IOWA STATE UNIVERSITY

## Lab 2 Answer Sheet

d Student I	D:		Lab Section:_	
:				
В	С	F		
0	0			
0	1			
1	0			
1	1			
0	0			
0	1			
1	0			
1	1			
•	າ:			
	B 0 0 1 1 0 0 1	Section 3.0 and fill in the SOP expression and state   SOP expression and	I section 3.0 and fill in the truth table the SOP expression and draw the research BC F  BC F  0 0 0 1 1 0 1 1 0 0 0 1 1 1 1 0 1 1 1 0 1 1 1 0 1 1 1 0 1 1 1 0 1 1 1 0 1 1 1 0 1 1 1 0 1 1 1 1	

TA Initials: \_\_\_\_\_

## Cpr E 281 LAB2 ELECTRICAL AND COMPUTER ENGINEERING IOWA STATE UNIVERSITY

## Lab 2 Answer Sheet

**Q2.** Read section 4.0 and fill in the truth table below for *lab2step2*. Then use it to construct the SOP expression and draw the resulting circuit using logic gates.

Cabbage	Goat	Wolf	Alarm
0	0	0	
0	0	1	
0	1	0	
0	1	1	
1	0	0	
1	0	1	
1	1	0	
1	1	1	

Circuit Diagram:
TA Initials:
LAB:
<b>3.0</b> Hardware results demonstrate a correct circuit. TA Initials:
<b>4.0</b> Hardware results demonstrate a correct circuit. TA Initials:

SOP Logic Expression: