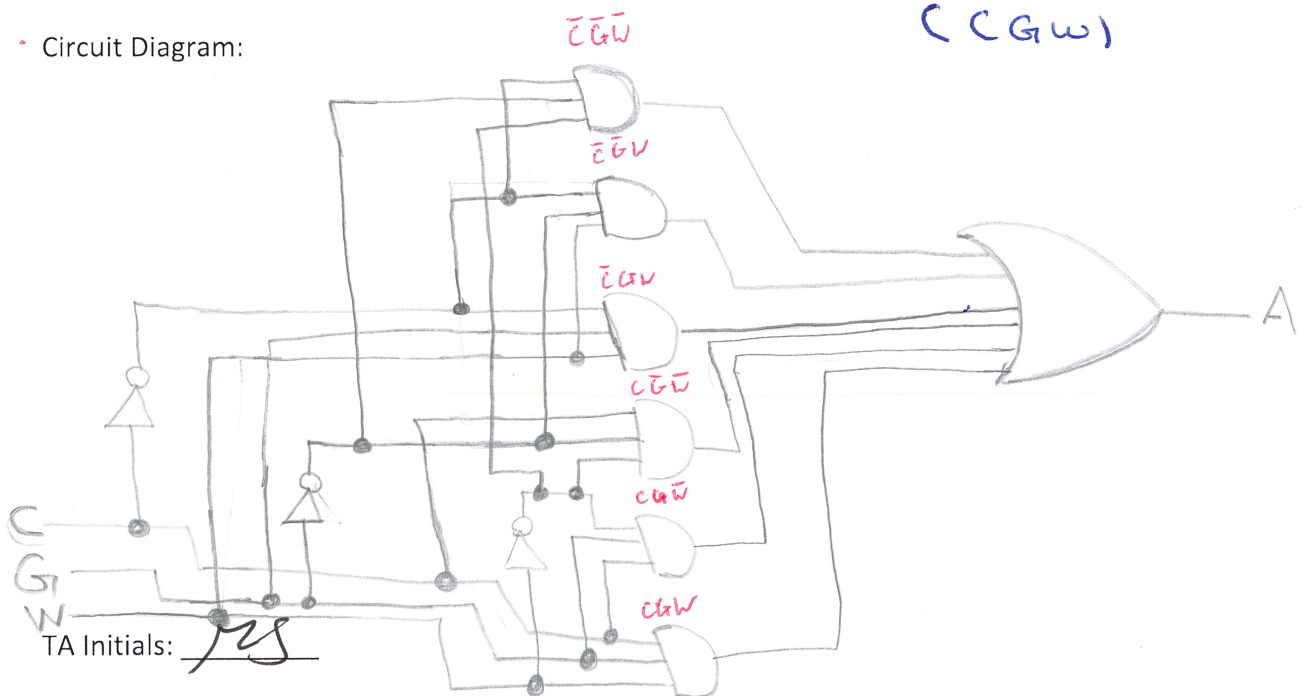Q2. Read section 4.0 and fill in the truth table below for *lab2step2*. Then use it to construct the SOP expression and draw the resulting circuit using logic gates.

Cabbage	Goat	Wolf	Alarm
0	0	0	1
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	1
1	0	1	0
1	1	0	1
1	1	1	1

- SOP Logic Expression: $A = (\bar{C}\,\bar{G}\,\bar{W}) + (\bar{C}\,\bar{G}\,W) + (\bar{C}\,G\,W) + (C\,\bar{G}\,\bar{W}) + (C\,G\,\bar{W}) + (C\,G\,W)$

- Circuit Diagram:

TA Initials: _____

LAB:

3.0 Hardware results demonstrate a correct circuit. TA Initials: __AW__

4.0 Hardware results demonstrate a correct circuit. TA Initials: __AW__

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