

Ch-1 Lec-1Operating system

- ↳ नियंत्रित करने वाला प्रणाली है जो कंप्यूटर को नियंत्रित करता है
- ↳ Computer user and hardware के बीच interface/interaction का नियंत्रित करता है
- ↳ main purpose: CPU - Processor
- Memory 20%

Operating system functions:

" " " services:

* " " " components:

1. User

2. Application softwares → compiler (C, C++, Fortran), built-in/interpreter (Python, system)

3. System

4. Hardware

↳ कंप्यूटर को run करने वाला output का दर्ता

OS Examples:

1. Process Management

↳ single unit processor

2. I/O Device management

3. File Management

4. Network "

5. Main Memory " → Primary memory

6. Secondary "

7. Security "

8. Command interpreter system

Lesson 1-1

* Why do learn OS?

⇒

System software → Operating system
Application

* OS component: 8 bit

File Management

↳ Creation & deletion

Lists, stacks, queues

Random access memory

U/V/X

Memory

User interface

* Cache memory (to faster file access or organize open files) Recent files

1. Linux → Dual boot

Windows → gpt or mbr ← Disk partition type

Pendrive + iso file ubuntu boot

Memory

Random access memory

Windows

Random access memory

Lecture-2* Types of Operating System:

↳ Service A is 290. Depend on Network OS.

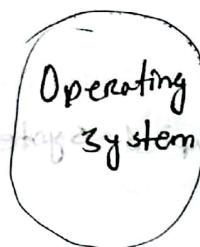
Distributed Network → works on different Computer, across

→ acts as the manager of all the resources

1. Batch OS:

↳ similar type of task to perform together

→ " jobs → same requirement and group

Adv, Disadv:

↳ processor know how long the job will be completed

↳ Multiple users

↳ idle time very less

↳ easy to manage Large work repeatedly

Disadv.

↳ best experienced

↳ hard to debug

↳ costly

↳ wait for an unknown time if any job fails

↳ idle time refer

2. Time-sharing OS/Multitasking system: function of CPU to control the processing task
- ↳ time to share CPU OS after 2ms
 - ↳ time wise CPU is shared
 - One by one sequentially

Adv.

- ↳ idle time can be reduced

Disadv.

- ↳ security and integrity of programs and data ensure that no one

Ex → Unix

3. Distributed OS/Loosely coupled system:

↳ refer used

↳ different computer to inter connect with each other

↳ processors differ in size and function

Adv.

↳ All systems are independent

↳ resources being shared, computation highly fast and durable

↳ Load

↳ Delay data processing reduced

Disadv.

↳ data goes to central resources as central flow of

data with added

4. Network OS (loosely coupled system)

↳ Linux server

↳ Network OS provides env. for OS.

Adv.

↳ Highly stable

Hardware change over Windows effect

Ex: Linux, Mac OS, UNIX

Cloud computing

5. Real-time OS:

↳ Robot - high precision and high time accuracy

↳ Process and response time

↳ small

Ex: like missile systems

Adv.

Disadv.

↳ Complex algorithm

OS services:

1. Program execution

↳ Load main memory → a program

↳ Execute program

↳ Handle ..

↳ Process synchronization

↳ .. " communication

↳ deadlock handling

2. I/O operation:

3. File system manipulation

* Communication

↳ 2 methods implemented without inter agents communication

↳ 1. Shared Memory

2. Message Passing

* Error handling:

↳ error detection and correction

* Resource Management:

↳ Allocate →

→ Deallocate system and memory

* Protection:

↳ file system protection

↳ memory protection: memory protection

↳ file

most common is a process where logical and

physical file protection and memory protection

↳ memory

→ block

↳ file

access mode for a process

↳ memory

not recommended

↳ file

process based access

↳ memory

process based access

* OS Properties:

1. Batch processing ↳ sequence of commands, programs and data
2. Multitasking ↳ number of jobs in memory and executes
↳ order of submission
3. Multiprogramming ↳ output spool
4. Interactivity ↳ user printing after their job
↳ executes complete job
5. Real Time system ↳ process synchronization
- 6.
- 7.

Adv. disadv.

2. Multitasking: multiple programs executed simultaneously

- Time-sharing system
- ↳ context switching
↳ switching between processes for switching
- ↳ CPU scheduling
↳ CPU utilization

3. Multiprogramming:

* process in time → program

- ↳ sharing the processor
↳ 2 or more programs → concurrency

Multithread → one process or task into part/unit → faster execution
→ one task into multiple threads

Multiprocessor → core i7 → 7 or more processes executes simultaneously
→ faster execution

↳ time save

Round-Robin algorithm: time-share job

4. Interactivity:

↳ user to interaction

5. Real Time System:

↳ hard/software

↳ embedded system

→ Robotics

→ ensure correct performance

6. Distributed Environment:

↳ multiple independent CPUs and processors

↳ CPU Buffer

* Diff. 32bit & 64-bit OS → H.W.

BIOS & Record

↳ Boot

↳ Kernel

→ booting flow (1) memory flow

→ BIOS → boot flow

→ memory flow (2) memory flow

→ BIOS flow

→ memory flow (3) memory flow

→ memory flow (4) memory flow

→ memory flow (5) memory flow

→ memory flow (6) memory flow

* Microkernel in OS: at একে মাইক্রোকোর্নেল বলা হয়।

↳ Software and hardware গুরুত্বের বিপরীতে।



Kernel

↳ specific কার্য করার জন্য একটি

↳ Create new file

read file

write "

Figure:

kernel process → internal

user process / execution → user mode "
 user process execution → 2525 কোর্স করে যাবে → user address space

kernel " / mode → internal 312 OS 25 278725 → Kernel " "

Primary memory



Secondary memory



Harddisk

↓
Swap
mem

* Virtual Memory Scheduling

↑
secondary memory

Virtual, Virtual, Global

* System call OS: যেসব কাজের জন্য একটি প্রক্রিয়া।

↳ Interface between a process and OS.

user mode → user process Executing → S.C → result
kernel mode

↳ Interprocess communication between two threads; for ex. -

kernel mode → one thread has to open a file → other thread has to read it out

Excute call

Open

Read

Out

* Why do we need System Calls in OS?

↳ Reading and writing to memory

↳ Creating

↳ Deleting files from disk

↳ Writing to disk

↳ Reading from disk

↳ Other

Types:

1.

2.

3.

4. \leftarrow The user can be free & advances freedom much

↳ A user can't do the following \leftarrow above is illegal

* Bus time: \leftarrow after complete 280 microseconds \rightarrow 280 microseconds

wait()

\hookrightarrow after process (280 microseconds) completed \rightarrow wait execute

msr

fork(), kill(), exit()

↳ after 280 microseconds

↳ child process runs concurrently

* Windows, Linux \rightarrow categories

↳ 1. child

↳ -ve: Child not created process unsuccessful

→ 0: New child created, returned value is 0 or Process ID

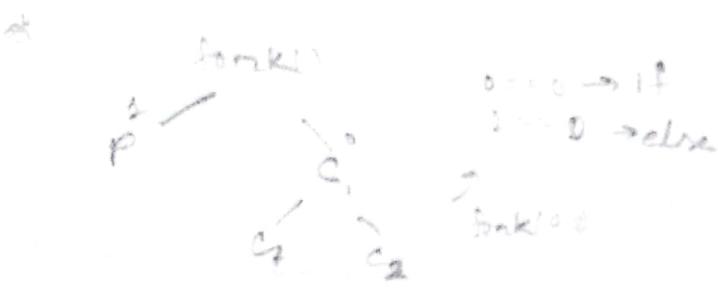
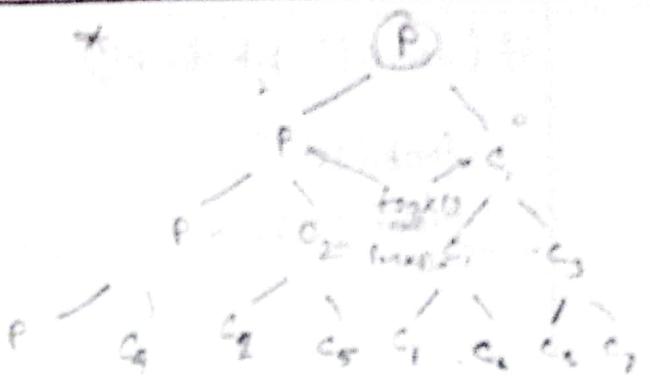
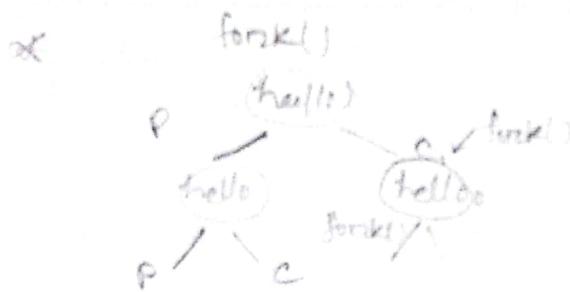
↳ +ve: Parent (280 microseconds)

\hookrightarrow -ve 280 microseconds child 280 microseconds created

\hookrightarrow +ve in Parent in in in

• Foskel (interdo sample free and with haploid mei

2/29/87 process
one - Parent child
4 do call 285



$$2^8 = 8 \times 2^7$$

$$2^5 - 1 = 8 - 1$$

3 → fork → 3 copy
call

n = system call

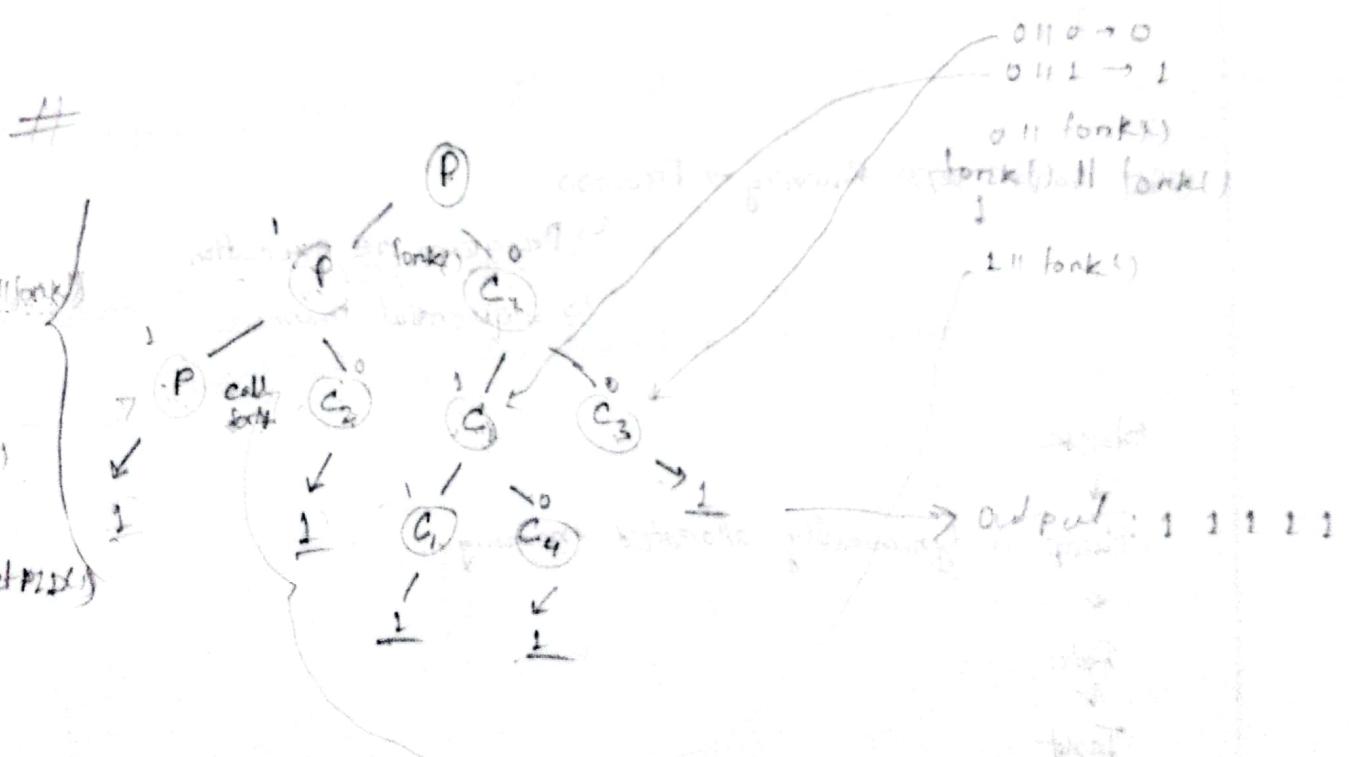
task 10 11 task 11

fork () || fork()

① left

110

10



H.W. fork() & fork()

if (fork() == 0)

fork();

printf ("1");



0 3 6 9 12 15
1 4 7 10 13 16

2 5 8 11 14 17

3 6 9 12 15 18

4 7 10 13 16 19

5 8 11 14 17 20

6 9 12 15 18 21
7 10 13 16 19 22
8 11 14 17 20 23
9 12 15 18 21 24

After 2nd fork running of process

↳ Program A is execution

↳ sequential manner.

Stack



Heap → dynamically allocated memory



Data



Text

Ready State:

* Process Life Cycle:

1. New state \rightarrow Secondary memory page has been paged in
2. Ready \rightarrow Primary
3. Run \rightarrow Primary
4. Terminate \rightarrow Secondary

* Degree of Multiprogramming

* Long term scheduler (LTS) \rightarrow $\text{LTS} \rightarrow \text{Ready state} \rightarrow \text{Run}$

* PCB \rightarrow Process Control Block \rightarrow PID 27/28

\uparrow process \rightarrow 1st PCB 27/28

\downarrow process 2nd 28th PCB 3 27/28

Time quantum: Parallel execution between processes, starvation of processes

* Suspend Ready \rightarrow VIP (or) wait 27/28

\downarrow Wait \rightarrow I/O completion 28/25 (51625) suspend wait

\downarrow Ready State 28/25 51625

Context switch

Context switch

1. Long term or Job scheduler:

↳ careful selection \rightarrow I/O and CPU bound Process

↳ Secondary and main memory \rightarrow MFCW

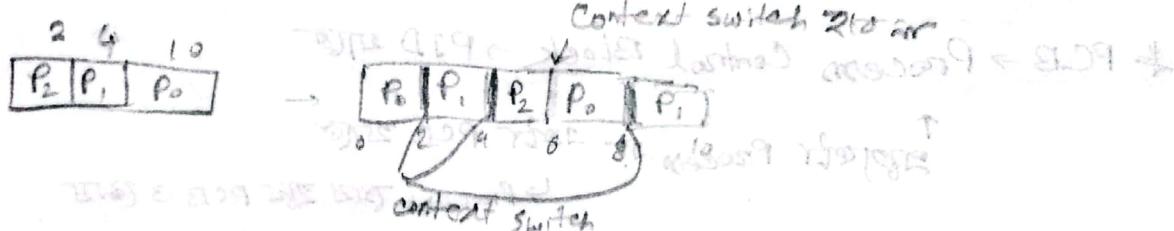
2. CPU scheduler or

↳ ready to run state

↳ dispatcher

* Context switching \rightarrow कार्ज Complete तरीके से परिवर्तन करते हैं।

switch करते हैं।

3. Medium term scheduler:

↳ suspend, wait state

↳ swaping \rightarrow main memory to disk or vice versa

High Priority

↳ CPU scheduler

1. I/O scheduler: I/O operations

2. Real-time : Critical task

* Thread in OS:

Thread \rightarrow ~~light weight process~~ \rightarrow ~~process~~ \rightarrow ~~OS~~

\hookrightarrow light weight process

\hookrightarrow small memory

Program counter (PC):

Ex \rightarrow formatting text

Processing unit

* Need of Thread:

Process vs Thread

Thread \rightarrow \hookrightarrow same memory space \rightarrow interdependent \rightarrow system \hookrightarrow ~~call~~ \rightarrow ~~exit~~ \rightarrow ~~exit~~

Process \rightarrow different \rightarrow ~~Mobile~~ \rightarrow independent

\hookrightarrow Thread \rightarrow ~~start~~

process \rightarrow ~~start~~ \rightarrow ~~exit~~ \rightarrow ~~start~~ \rightarrow ~~exit~~ \rightarrow ~~start~~ \rightarrow ~~exit~~

Types of Thread:

1. Kernel level thread

2. User " " \rightarrow it is user defined

\hookrightarrow OS not recognize

\hookrightarrow user level thread blocking operation \rightarrow whole process blocked

\hookrightarrow Kernel \rightarrow ~~user~~ \rightarrow ~~process~~ \rightarrow ~~user~~ but multiple thread \rightarrow ~~user~~

2. Kernel

↳ 3rd Multiple thread is 2nd process 275 285.

2nd thread kernel is first block 285 out 285
thread is 3rd 285 context effect 285 285

* Component of Threads

* Benefit

29-2-24

Lec-7

Multithreading Models & Hyperthreading

3 ways -

1. Many to one Model → context switch system block (or call 285

2. One to one → 285 285 or Automatic Block 285 285

3. Many to Many → Multiprocessor 285 285 problem 285,

↓

↳ 285 285 user and

Kernel thread.

↳ " " specific

285 285,

↳ Multiprocessor parallel
execution 285

↳ 2nd user thread is 2nd 285 285 kernel thread

285 285,

↳ problem → overhead

↳ performance degrade 285

* Hyperthreading / Simultaneous Multithreading (SMT)

Number of Cores \rightarrow physical

Logical Processors \rightarrow logical & double 2:1 \rightarrow 12

Suppose there are 3 cores
and 2 threads per core

CT \rightarrow 12

Types of threads: soft and hard threads (SMT)

CPU Scheduling

Why do we need CPU scheduling?

\Rightarrow

Two cores (2:1 SMT) have 12 threads waiting for CPU \rightarrow Pre-emption

Pre-emption \rightarrow Pre-emption

CPU scheduling Algorithm

1. Pre-emption Algorithm \rightarrow shortest Round Robin
2. Non \rightarrow shortest, longest

\rightarrow shortest remaining Time First \rightarrow Preemptive SJF

\rightarrow longest \rightarrow L-JF

Non-preemptive SJF \rightarrow Non-preemptive SJF

PL of 0.1 \rightarrow wait \rightarrow wait turned

wait \rightarrow wait completed

wait \rightarrow PL \rightarrow wait turned

Arrival time

CPU Scheduling Terminologies

Arrival time: ready queue (O) waiting Process

↳ no time a process

↳ 2nd 5th time a ready queue (O) waiting

Arrival time - 5ms

CPU Idle time: 1st 5ms time → Arrival of user

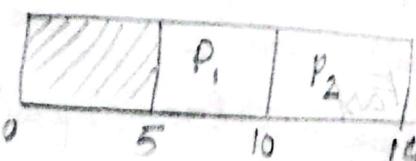
Burst time/Execution time: execution (O) no time (O) waiting

* Arrival time same (O) PID (O) burst time (O) waiting

Completion time: end of process CPU (O) wait time (O) 20 25

Premption, Non-preemption

↳ first in First out



↳ completion time

P₂ → Arrival time → 6ms

→ 6ms a waiting

Burst time → 4ms → 10 to 14

Completion time → 14ms

Turn Around time → 14 - 6 = 8ms

Response time: $10 \text{ ms} - 6 \text{ ms} = 4 \text{ ms}$ \leftarrow 1st step \rightarrow

2nd step \downarrow \rightarrow
 completion time \downarrow \rightarrow
 \downarrow \rightarrow
 Step 2nd \downarrow \rightarrow
 Arr. time

Waiting time: = Turnaround time - burst time

$$16 - 10 = 4 \text{ ms}$$

First Come First Serve (FCFS): \rightarrow Non-preemption

\rightarrow Arrival time \leftarrow Criteria

Process	Arrival time	Burst time
P ₁	0	4
P ₂	1	3
P ₃	2	1
P ₄	3	2
P ₅	4	5

Avg turnaround time \leftarrow

Avg Waiting time

T

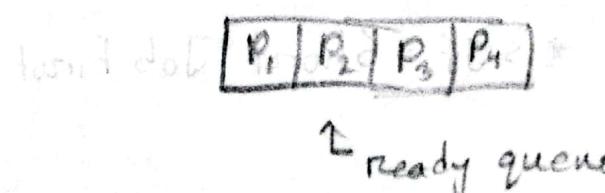
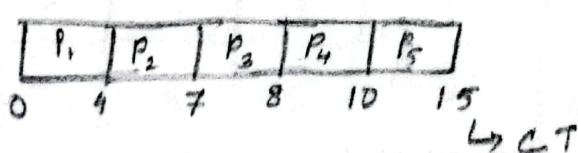
Algorithm FCFS \leftarrow

मालार

\rightarrow Avg TAT = 6.8, Avg waiting time

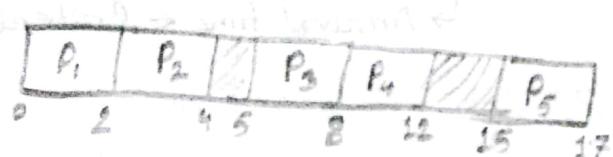
Grant Chart: Bar chart \rightarrow 75% version

= 3.8



Process	AT	BT	CT	TAT	WT	RT
P ₁	0	2	2	2	0	0
P ₂	1	2	4	3	1	1
P ₃	5	3	8	3	0	0
P ₄	6	4	12	6	2	2
P ₅	15	2	17	2	0	0

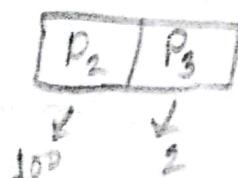
Grant chart: scheduling job by SJF (Short Job First) and Gantt chart



Idle time = 4 - 5 = 1 ms \leftarrow Idle time

wait pattern

Disadvantage: convoy effect



TAT \rightarrow 102 265 472

~~* SJF \rightarrow Short Job First~~

* SJF = Short Job First

Disadvantages \rightarrow Convoy effect

SJF \rightarrow नारे Arrival time रखे तो यहाँ जो बड़े होंगे

\hookrightarrow shortest Job First

\hookrightarrow 1. Promptive SJF

2. Non-Promptive \rightarrow Shortest Remaining Time First

\hookrightarrow Context switching नहीं होता

Disadv. \rightarrow Shorter Process $\&$ Burst time बड़े होंगे तो इसे नहीं होता
 \rightarrow Starvation होता

Avg. Waiting time $\&$ Avg. Turnaround time रखे तो यहाँ जो बड़े होंगे

SJF \Rightarrow

Criteria \rightarrow Burst time

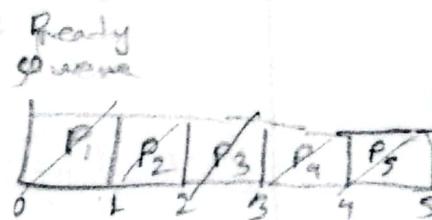
Non-preemptive SJF : \rightarrow WT, RT same रखा जाता है Always

PID	AT	BT	CT	TAT	WT	RT
P ₁	0	4	4	4	0	0
P ₂	1	3	10	9	6	$\frac{7-1}{2} = 3$
P ₃	2	1	5	3	2	$\frac{4-2}{2} = 1$
P ₄	3	2	7	4	2	$\frac{5-3}{2} = 1$
P ₅	4	5	15	11	6	$\frac{10-4}{2} = 3$

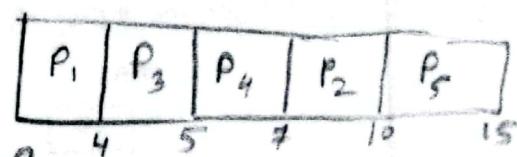
P₁ (2021.5) \Rightarrow Burst time check

\hookrightarrow short ones

and complete 2nd Ready queue after



Grand chart



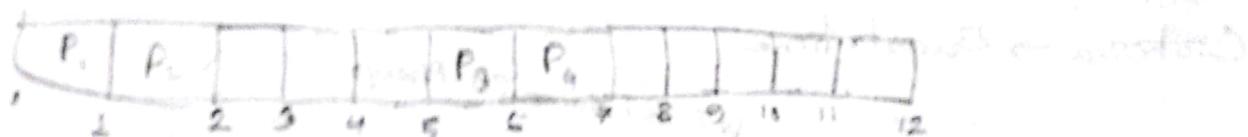
\rightarrow ms

Grand chart at time 15 ms AT 15 ms

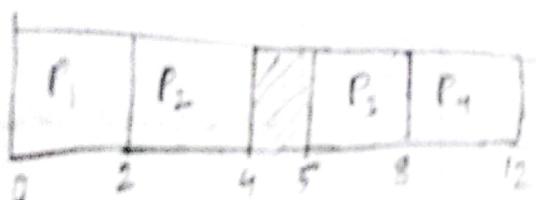
PID	AT	BT	CT	TAT	WT	RT
P ₁	0	2	2	2	0	0
P ₂	1	2	4	3	2	2-1
P ₃	5	3	8	3	0	5-6
P ₄	6	4	22	6	2	8-6
				avg-3.5	avg-0.75	-2

→ Burst time sorting (P₁, P₂, P₃, P₄)

Ready Queue:



Graph chart:



→ CPU time instance
ms

Premptive SJF (SRTF): \rightarrow WT, RT same 27.3 27.3 ms

Time quantum = 1 ms

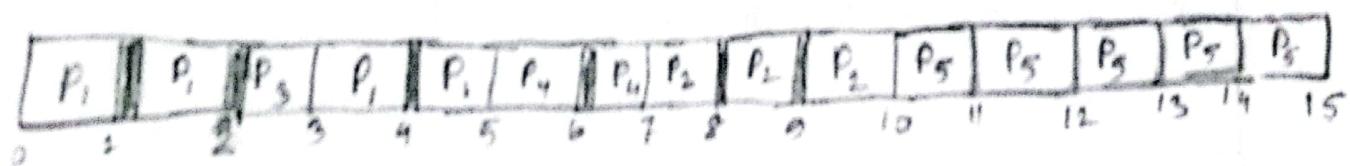
PID	AT	BT	CT	TAT	WT	RT
P ₁	0	4	5	5	1	0
P ₂	1	3	10	9	6	$\frac{7-1}{2} = 6$
P ₃	2	1	3	2	0	$\frac{9-2}{2} = 3.5$
P ₄	3	2	7	9	2	$\frac{5-3}{2} = 1$
P ₅	4	5	15	11	6	$\frac{10-4}{2} = 3$

avg = 6 Avg = 3

Ready Queue:

P ₁	P ₂	P ₁	P ₃	P ₁	P ₄	P ₅	P ₁	P ₄	P ₂	P ₁	P ₅	P ₅	P ₄	P ₃
4	3	3	2	2	2	5	2	2	2	1	4	3	2	1

Gantt Chart:



P₅

10 15

* Compare w/ other switching algo. Load or Bus alg.
* QF RT

PID	AT	BT	CT	TAT	WT	RT
P ₁	0	2	2	2	0	0
P ₂	1	2	4	3	1	2
P ₃	5	3	8	3	0	3
P ₄	6	4	12	6	2	6

Ready Queue:

P ₁	P ₂	P ₁	P ₂	P ₃	P ₄	P ₃	P ₅	P ₄	P ₆	P ₄
1 2	2 1	2 3	2 4	3 5	4 6	2 7	2 8	3 9	2 10	1 11

Gantt Chart:

P ₁	P ₂	P ₂	P ₂	P ₃	P ₃	P ₃	P ₄	P ₄	P ₄	P ₄
0 1	1 2	2 3	3 4	4 5	5 6	6 7	7 8	8 9	9 10	10 11

* SJF/NR Algorithm Adv. Disadv. diff/ Comparison

Largest Job First \rightarrow Preemptive
 \hookrightarrow Non - II

Non-preemptive:

PID	AT	BT	CT	TAT	WT	RT
P ₁	0	4	4	4	0	0
P ₂	1	3	12	11	8	7-1 = 8
P ₃	2	1	15	13	12	14-2 = 12
P ₄	3	2	14	11	9	12-3 = 9
P ₅	4	5	9	5	0	4-4 = 0
		Avg: 8.8		Avg: 5.0		

Ready queue

P ₁	P ₅	P ₃	P ₄	P ₂
0	4	9	14	15

Gantt chart

P ₁	P ₅	P ₃	P ₄	P ₂
0	4	9	14	15

1. Preemptive Scheduling

2. Non-preemptive Scheduling

3. Shortest Job First

4. Shortest Remaining Time First

5. Round Robin Scheduling

Non-preemptive LJP:

PID	AT	BT	CT	TAT	WT	RT	Completion =
P ₁	0	2	2	2	0	0	Completion =
P ₂	1	2	4	3	1	1	Completion =
P ₃	5	3	8	3	0	0	Completion =
P ₄	6	9	12	6	2	2	Completion =

Ready queue:

P ₁	P ₂	P ₃	P ₄
2	2		

Grant chart:

P ₁	P ₂	P ₃	P ₄
0	2	45	8

Premptive LJP

PID	AT	BT	CT	TAT	NT	RT
P ₁	0	4	12	11	7	0 = 0 - 0
P ₂	1	3	12	11	8	2 - 1 = 1
P ₃	2	1	13	11	10	12 - 2 = 10
P ₄	3	2	14	12	9	8 - 3 = 5
P ₅	4	5	15	12	6	4 - 4 = 0

Ready queue:

P ₁	P ₂	P ₁	P ₃	P ₁	P ₄	P ₅	P ₅	P ₂	P ₅	P ₃	P ₂	P ₄	P ₅	
4	3	3	1	2	2	2	1	5	4	3	2	1	1	1

Grant chart:

P ₁	P ₁	P ₂	P ₁	P ₅	P ₅	P ₅	P ₂	P ₄	P ₅	P ₁	P ₂	P ₃	P ₄	P ₅	
0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15

* Convoy effect

↳ Waiting time $1362 - 2785$

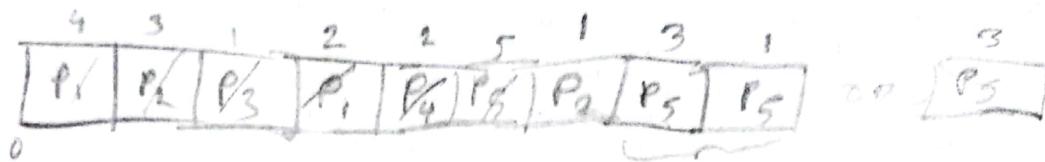
→ context switching ≥ 10 CPU degrade ≥ 5

Ready queue is PID chronologically sorted
Serial & Parallel Chrono PID Order

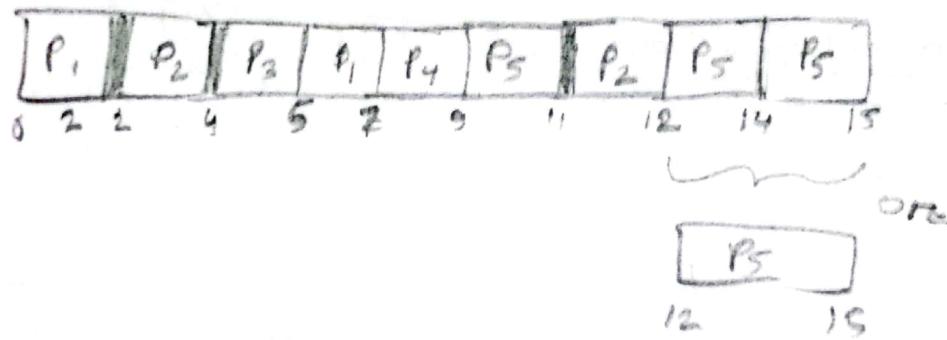
preemptive for priority \rightarrow A.C. \rightarrow CPU performance efficient, \downarrow waiting time
Round robin \rightarrow Context switching cost \downarrow \rightarrow Quantum
 time slices use \rightarrow but fixed time
 \rightarrow CPU program process for first time.

Time slice/Quantum = 1ms \leftarrow Maximum 26ms

Ready queue:



Gantt \rightarrow CPU time:



PID	AT	BT	CT	TAT	WT	RT
P ₁	0	4	7	7	3	0-0 =0
P ₂	1	3	12	11	8	2-1 =1
P ₃	2	1	5	3	2	4-2 =2
P ₄	3	2	9	9	6	7-3 =4
P ₅	4	5	15	11	6	9-9 =5

PID	AT	BT	CT	TAT	WT	RT
P ₁	0	2	2	2	0	0-0 =0
P ₂	1	2	4	3	1	2-1 =1
P ₃	3	3	10	5	2	5-5 =0
P ₄	6	4	12	6	2	7-6 =1
				Avg = 4	Avg = 1.25	

Time slice = 2ms

Ready queue:

2	2		3	4	2	2
P ₁	P ₂		P ₃	P ₄	P ₃	P ₄
0	1	2	3	4	5	6

Grant:

P ₁	P ₂	P ₃	P ₄	P ₃	P ₄
6	2	4	5	7	9

Priority:

1. Lowest value Highest Priority

→ Priority column P₃ extra

2. Highest = Lowest =

* Priority A time slice 20ms

20ms for priority Fair queue

Priority Based Scheduling Algorithm

LVHP

PID	AT	BT	Priority	CT	TAT	WT	RT
P ₁	0	2	2	4	4	2	0-0=0
P ₂	1	2	1	3	2	6	2-1=1
P ₃	5	3	4	12	7	4	5-5=0
P ₄	6	4	3	10	4	0	6-6=0
				4.25	1.3		

preemptive

Ready queue

2	2	9
P ₁	P ₂	P ₁

Grant:

P ₁	P ₂	P ₁	P ₁	P ₃	P ₄	P ₄	P ₄	P ₃	P ₃
0	2	2	3	4	5	6	7	8	10

Min Burst time (Criteria - Priority)

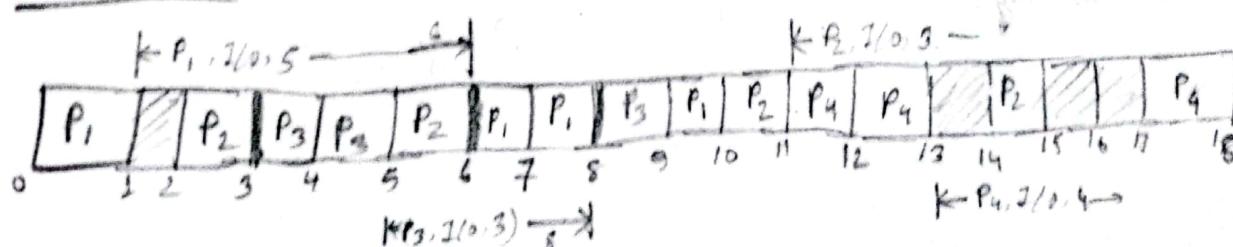
Mode - Preemptive, Lowest No - Highest Priority

PID	AT	Priority	CPU	I/O	CPU	CT	TAT	WT	RT
P ₁	0	2	10	5	3	10	10	10-10=0	0-0=0
P ₂	2	3	3	2	1	15	13	13-13=0	2-2=0
P ₃	3	1	2	3	1	9	6	6-2=4	3-3=0
P ₄	3	4	2	4	2	18	15	15-(2+4)=8	11-3=8
									18-3=15

Ready queue:

2	3	2	2	2	3	1	1	1	1
P ₁	P ₂	P ₃	P ₄	P ₂	P ₁	P ₂	P ₃	P ₁	P ₂

Gantt chart:



Multilevel Queue Scheduling
 VIP processes \rightarrow System process
 user \rightarrow interactive

Similar type \rightarrow Batch \rightarrow Background

Priority \rightarrow High \rightarrow System
 Medium \rightarrow Interactive
 Low \rightarrow Batch

VIP \rightarrow Multilevel Queue
 Interactive \rightarrow Multilevel Queue user Q
 Batch \rightarrow Multilevel Queue Algorithm follows
 Shortest Job First \rightarrow SJF

FCFS, Round Robin, Priority, Preemptive Round Robin

1. Fixed Priority preemptive scheduling Algorithm

\rightarrow Priority time \rightarrow 20% of time \rightarrow 20% of time \rightarrow 20% of time

2. Time Slicing:

Process synchronization:

- ↳ **Process** synchronization

2 वर्ष वाले 2 वर्ष वाले Allow करते

↳ Multiple version वाले run वाले फिर भी

* How process synchronization in OS works?

↳ ~~first~~ time a process Read ~~the~~ ^{from} ~~Waffer~~ ^{Memory} &

Elements/Section in a program:

1. Entry section
2. Critical " → main " → allow only one process
3. Exit " → ~~multiple~~ process द्वारा द्वयोरपि एवं विवर
process shared करते हैं एवं यह एक वर्ष
4. Remainder " → Code वाले विवर विवर विवर section वाले
or विवर section वाले

Race Condition:

Multiple process ^{run} द्वारा करते हैं तो → risk

→ ~~जैसे~~ Data access वाले ^{result} Execution ~~जैसे~~ depend वाले Execution
जैसे order वाले depend वाले

* Multiple thread execution विवर,

Solution: (solution of problem with the help of semaphore and locks)

Critical section \rightarrow यह एक जगह है जहाँ process तो Allow होता है

* Critical section problem:

wait() function

\hookrightarrow handle एक वाहन जहाँ process run होता है

* Requirements of Synchronization:

1. Mutual exclusion: एक process running होता है तभी तभी critical section \rightarrow

one process (or Allow होते हैं) \rightarrow दूसरे process नहीं होती हैं mutually exclusion तभी process तभी execute होता है

2.

2. Progress: Remaining section \rightarrow process तभी decide वहाँ से next

process तभी enter होता है execution critical section \rightarrow subsequence

3. No Starvation/Bounded Waiting:

\hookrightarrow तभी-तभी wait तो critical section \rightarrow 2785 (fixed time)

starvation \rightarrow infinite time wait

Solution of Critical Section Problem:

* Critical section execution तभी-तभी Remaining part execution तभी-तभी

problem

lock method = Smith

\rightarrow Remaining \rightarrow Write to enter file (Grosser Read/Temporary entry)

$$n \times (t + b) = t \times b$$

Critical section 2 execution of 325 Remaining process 225
execution 325

* A Boolean flag:

True: Process accessing

False: Process not

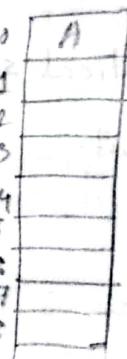
Initial → False after

→ False (or true) Access to

Procedure Consumer Problem

Consumer Code

```
int count;  
void consumer(void)  
{ int itemC  
    while(1)  
        if(itemC == 0)  
            while(count == 0);  
        itemC = buffer[out];  
        out = (out + 1) % n;  
        count = count + 1  
    }
```



Procedure Code

```
int count = 0;  
void procedure(void)  
{ int itemP  
    while(1)  
        if(produce_item(itemP))  
            if(itemP == A)  
                itemC = buffer[out];  
                out = (out + 1) % n;  
                count = count + 1  
    }
```

initially
IN [0]
count [0]

IN → division
Method
Circularly
step 1
step 2

count = count + 1

count = count - 1

count = 0
1 2 3 4 5 6 7 8 9
m[0] 0 1 2 3 4 5 6 7 8 9

1. Load R_c , m[count]

2. Decr. R_c \rightarrow context switching

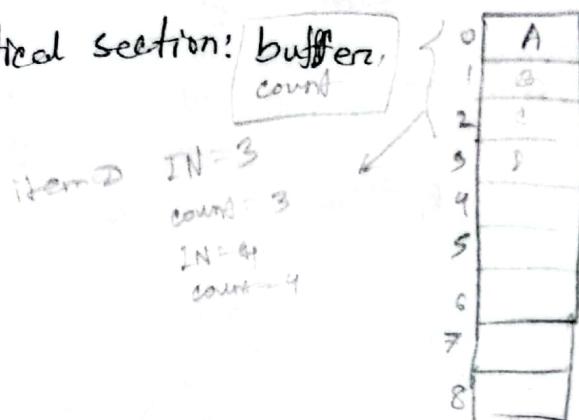
3. Store, m[count], R_c

1. Load R_p , m[count]

2. Incr. R_p \rightarrow context switching

3. Store, m[count], R_p

Critical section:



initially IN = 0, count = 0, item = 0

IN = 1, item = 1, count = 1

False
while (count != 0),
count = 1

count = 0
while () \rightarrow buffer full \rightarrow condition true \rightarrow infinite loop \rightarrow Z65 270
while () \rightarrow buffer empty \rightarrow condition true \rightarrow

Registers



* Synchronization problem

Printer Spooler Problem!

1. Load $R_i, m[IN]$
 2. Store $SD[R_i]$, "File-Name"
 3. Increment R_i
 4. Store $m[IN], R_i$

IN
0
↑
initially

$$R_i \leftarrow m[0]$$

$$R_i = 1$$

IN = 1

$P_1 \rightarrow I_1, I_2, I_3$ | P_2, I_1, I_2, I_3, I_4 | P_3, I_4
Context 7
Switching Start pointer

IN **3**

1. $R_2 = m[3]$
 2. Stone $m[3] \rightarrow \text{file 5}$
 3. $R_2 = 4$
 4. Stone $R_4 = 4 \rightarrow m[4] \rightarrow \text{TNT 9}$

The diagram illustrates a pointer variable. On the left, a rectangular box is labeled "file I", representing memory. To its right is a vertical curly brace, with the word "Pointer" written below it. An arrow points from the brace to the right, indicating that the pointer variable points to the memory location of the file structure.

$$p_i \rightarrow t/k \pm 1$$

Best care

IN [3]	Switching
0	file1
1	file2
2	file3
3	file4
4	
5	

INC

from the machine
error for
 $P_1 = \text{file 4}$
 $P_2 = \text{file 5}$

1. $R_i = \text{load } m[3]$
 2. Store $m[3]$ after
 3. $R_i = 4$

Race condition \rightarrow यदि कोई thread ने सेमाफोर को उत्तरीकृत किया है तो उसके बाद दूसरे thread ने उसी सेमाफोर पर उत्तरीकृत करने की कोशिश करता है।

\hookrightarrow Race condition के लिए同步 synchronization की आवश्यकता है।

\hookrightarrow Semaphore

Semaphore

\hookrightarrow integer value \rightarrow concurrent process (के लिए Race avoidance)

\rightarrow +ve value के लिए condition के लिए example

255, -ve values

255 तक

Binary, counting

\swarrow \downarrow

(0 and 1) (-∞ to +∞)

Program \rightarrow 4 भिन्न sections:

1. Entry \rightarrow P(), down, wait

2. Critical \rightarrow किसी program की critical section \rightarrow P() तक

3. Exit \rightarrow V(), up, signal, post, release

4. Remainder

but

S[2]

S[1] → P₄ → Entry 02205

Ready queue 1

P₁ P₂

Entry 1 → 02205 → S value 0 → P₁ P₂ P₃ P₄ Ready queue 1

2 2715 RT, semaphore S, value 1 → 225 Entry 02405

S[0] → P₅ → Entry 02205

→ 02205 → S value 0 → CS

S[1]

→ 02205 → 2

P ₁
P ₂
P ₃
P ₄

most efficient to do

can do and do

→ 02205 → S value 0 → CS

Semaphore → +ve → make process success 02205 RT
-ve → suspend → make process 02205

0 → memory process 02205 Block list & CS 1

* S[3]

6P

3V

when will be the value of S after these operations?

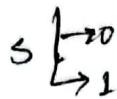
$$9 \rightarrow 9 - 6 = 3 + 3 = 6$$

process

2715 → 02205 → S value 0 → CS

2715 → 02205 → S value 0 → CS

Binary Semaphore



Offered when value is 1 or process wait value initially 0.

Down (Semaphore s)

```

if (s.value == 1)
  {
    s.value = 0
    else
      block process, sleep();
  }
  
```

Up (Semaphore s)

```

if (suspend list empty)
  {
    s.value = 1;           value 0 to 1
    always 1
    else
      select a process from
      block list, wake up();
    Up ready queue
  }
  
```

$s \boxed{1} 0$
 $\text{Down}(s) \leftarrow$
 $\boxed{0} \leftarrow$
 $\text{Up}(s) \rightarrow$

P_1
 $\text{Down} \rightarrow$
 \boxed{CS}
 $\text{Up} \rightarrow$

P_2
 $\text{Down} \rightarrow$
 \boxed{CS}
 $\text{Up} \rightarrow$

$\rightarrow s \boxed{0}$

P_1 access \boxed{CS} \rightarrow \times
 $P_2 \rightarrow \text{Block}$

$CS[P_1]$
 $\text{OK } \boxed{P_1, P_2} CS$

Condition: P_1 and P_2 are to be run in the same order

Binary Semaphore $s \boxed{0}$ First a request, if process block list is empty, carefully select one to run

Down / select one

CS \rightarrow Shared \rightarrow Synchronization problem 305 013

Down, Up \rightarrow Entry

* Mutual exclusion
2nd process syn.

S[1]

\searrow Down
 $P_1 \rightarrow S[0]$ and free

CS \rightarrow 162 2785

Up signal $S[1]$ and free

* 2nd process block list \rightarrow free

013 \rightarrow CS \rightarrow 013

Mutual exclusion 013

P_1 UPL	P_2 DownL
CS	Up
DownL	UPL

S[2]

$(P_1 - P_2)$
 $P(s)$
 CS
 $\vee(s) \rightarrow$ But

S[1]

P_1

Entry $\leftarrow \vee(s)$

CS

$\leftarrow \vee(s) \rightarrow P(s)$

\hookrightarrow Max \rightarrow 3rd process

\rightarrow Max \rightarrow 3rd

\hookrightarrow Max \rightarrow 2nd process

Min \rightarrow 1 or Always

P_1
P_2
CS
$P(s)$

\rightarrow Block list 2nd

* Maximum \rightarrow 10⁶
process CS \rightarrow 270K ms?

S[2]

10⁶ process, 20 ms

$P_1 \rightarrow S[0]$

$\downarrow S[0]$

CS [P₁, P₂] \rightarrow 013 2nd process turns or CS 2, P₂ - P₁ \rightarrow block list \rightarrow 625 ms

$P_{10} \rightarrow S[1]$

S \rightarrow 1, 0, 1, 0, 1, 0,

CS [P₁, P₁₀]

CS [P₁, P₁₀, P₂, P₃, P₁₀, P₄, P₁₀, P₅]

P₂ \rightarrow S[0]

CS [P₁, P₁₀, P₂]

[P₁, P₁₀, P₂]

\downarrow
S[1]

∴ a. $\theta \rightarrow 10^\circ$ having max. μ

$\boxed{P_1, P_{10}, P_2} \rightarrow \text{Maximum} \rightarrow 30^\circ$
→ minimum height