

# Assembly Language for Intel-Based Computers, 5<sup>th</sup> Edition

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## Chapter 1: Basic Concepts

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# Objectives

- Understand common applications of assembly language
- Understand what an assembler does
- Understand hardware and software requirements for the book
- Know the basic history of PC Assemblers
- Differentiate between protected mode and real-address mode
- Learn basic principles of computer architecture as applied to the Intel IA-32 processor family

# Objectives (continue)

- Learn how to recognize and convert boolean and hexadecimal integers
- Perform binary addition and subtraction
- Understand basic boolean operations
- Differentiate between signed and unsigned binary integers
- Understand ASCII character representation

# Chapter Overview

- **Welcome to Assembly Language**
- Virtual Machine Concept
- Data Representation
- Boolean Operations

# Welcome to Assembly Language

- Some Good Questions to Ask
- Assembly Language Applications

# Questions to Ask

- Why am I learning Assembly Language?
- What background should I have?
- What is an assembler?
- What hardware/software do I need?
- What types of programs will I create?
- What do I get with this book?
- What will I learn?

# Welcome to Assembly Language *(cont)*

- How does assembly language (AL) relate to machine language?
- How do C++ and Java relate to AL?
- Is AL portable?
- Why learn AL?

# Assembly Language Applications

- Some representative types of applications:
  - Business application for single platform
  - Hardware device driver
  - Business application for multiple platforms
  - Embedded systems & computer games

(see next panel)



## Comparing ASM to High Level Languages

Type of Application	High-Level Languages	Assembly Language
Business application software, written for single platform, medium to large size.	Formal structures make it easy to organize and maintain large sections of code.	Minimal formal structure, so one must be imposed by programmers who have varying levels of experience. This leads to difficulties maintaining existing code.
Hardware device driver.	Language may not provide for direct hardware access. Even if it does, awkward coding techniques must often be used, resulting in maintenance difficulties.	Hardware access is straightforward and simple. Easy to maintain when programs are short and well documented.
Business application written for multiple platforms (different operating systems).	Usually very portable. The source code can be recompiled on each target operating system with minimal changes.	Must be recoded separately for each platform, often using an assembler with a different syntax. Difficult to maintain.
Embedded systems and computer games requiring direct hardware access.	Produces too much executable code, and may not run efficiently.	Ideal, because the executable code is small and runs quickly.

# What's Next

- Welcome to Assembly Language
- **Virtual Machine Concept**
- Data Representation
- Boolean Operations

# Virtual Machine Concept

- Virtual Machines
- Specific Machine Levels

# Virtual Machines

- Tanenbaum: **Virtual machine concept**
- Programming Language analogy:
  - Each computer has a native machine language (language L0) that runs directly on its hardware
  - A more human-friendly language is usually constructed above machine language, called Language L1
- Programs written in L1 can run two different ways:
  - **Interpretation** – L0 program interprets and executes L1 instructions one by one
  - **Translation** – L1 program is completely translated into an L0 program, which then runs on the computer hardware

# Translating Languages

**English:** Display the sum of A times B plus C.

**C++:** `cout << (A * B + C);`

**Assembly Language:**

```
mov eax,A  
mul B  
add eax,C  
call WriteInt
```

**Intel Machine Language:**

```
A1 00000000  
F7 25 00000004  
03 05 00000008  
E8 00500000
```

# Specific Machine Levels

(descriptions of individual levels  
follow . . . )

# High-Level Language

- Level 5
- Application-oriented languages
  - C++, Java, Pascal, Visual Basic . . .
- Programs compile into assembly language (Level 4)

# Assembly Language

- Level 4
- Instruction mnemonics that have a one-to-one correspondence to machine language
- Calls functions written at the operating system level (Level 3)
- Programs are translated into machine language (Level 2)



# Operating System

- Level 3
- Provides services to Level 4 programs
- Translated and run at the instruction set architecture level (Level 2)

# Instruction Set Architecture

- Level 2
- Also known as **conventional machine language**
- Executed by Level 1 (microarchitecture) program

# Microarchitecture

- Level 1
- Interprets conventional machine instructions (Level 2)
- Executed by digital hardware (Level 0)

# Digital Logic

- Level 0
- CPU, constructed from digital logic gates
- System bus
- Memory
- Implemented using bipolar transistors

next: Data Representation

# What's Next

- Welcome to Assembly Language
- Virtual Machine Concept
- **Data Representation**
- Boolean Operations

# Data Representation

- Binary Numbers
  - Translating between binary and decimal
- Binary Addition
- Integer Storage Sizes
- Hexadecimal Integers
  - Translating between decimal and hexadecimal
  - Hexadecimal subtraction
- Signed Integers
  - Binary subtraction
- Character Storage

# Binary Numbers

- Digits are 1 and 0
  - 1 = true
  - 0 = false
- MSB – most significant bit
- LSB – least significant bit

- Bit numbering:

1	0	1	1	0	0	1	0	1	0	0	1	1	1	0	0
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---

MSB

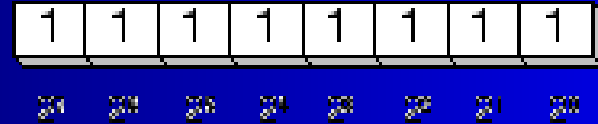
LSB

15

0

# Binary Numbers

- Each digit (bit) is either 1 or 0
- Each bit represents a power of 2:



Every binary number is a sum of powers of 2

**Table 1-3** Binary Bit Position Values.

$2^n$	Decimal Value	$2^n$	Decimal Value
$2^0$	1	$2^8$	256
$2^1$	2	$2^9$	512
$2^2$	4	$2^{10}$	1024
$2^3$	8	$2^{11}$	2048
$2^4$	16	$2^{12}$	4096
$2^5$	32	$2^{13}$	8192
$2^6$	64	$2^{14}$	16384
$2^7$	128	$2^{15}$	32768



# Translating Binary to Decimal

Weighted positional notation shows how to calculate the decimal value of each binary bit:

$$dec = (D_{n-1} \times 2^{n-1}) + (D_{n-2} \times 2^{n-2}) + \dots + (D_1 \times 2^1) + (D_0 \times 2^0)$$

D = binary digit

binary 00001001 = decimal 9:

$$(1 \times 2^3) + (1 \times 2^0) = 9$$

# Translating Unsigned Decimal to Binary

- Repeatedly divide the decimal integer by 2. Each remainder is a binary digit in the translated value:

Division	Quotient	Remainder
37 / 2	18	1
18 / 2	9	0
9 / 2	4	1
4 / 2	2	0
2 / 2	1	0
1 / 2	0	1

$$37 = 100101$$

# Binary Addition

- Starting with the LSB, add each pair of digits, include the carry if present.

$$\begin{array}{r}
 \text{Carry: } 1 \\
 \begin{array}{cccccccc}
 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\
 + & 0 & 0 & 0 & 0 & 0 & 1 & 1 & 1 \\
 \hline
 0 & 0 & 0 & 0 & 1 & 0 & 1 & 1 \\
 \end{array}
 \end{array}
 \begin{array}{l}
 (4) \\
 (7) \\
 (11)
 \end{array}$$

16 15 14 13 12 11 10 9 8  
 0 0 0 0 1 0 1 1 0

# Integer Storage Sizes

Standard sizes:

**Table 1-4** Ranges of Unsigned Integers.

Storage Type	Range (low–high)	Powers of 2
Unsigned byte	0 to 255	0 to $(2^8 - 1)$
Unsigned word	0 to 65,535	0 to $(2^{16} - 1)$
Unsigned doubleword	0 to 4,294,967,295	0 to $(2^{32} - 1)$
Unsigned quadword	0 to 18,446,744,073,709,551,615	0 to $(2^{64} - 1)$

What is the largest unsigned integer that may be stored in 20 bits?

# Hexadecimal Integers

Binary values are represented in hexadecimal.

**Table 1-5** Binary, Decimal, and Hexadecimal Equivalents.

Binary	Decimal	Hexadecimal	Binary	Decimal	Hexadecimal
0000	0	0	1000	8	8
0001	1	1	1001	9	9
0010	2	2	1010	10	A
0011	3	3	1011	11	B
0100	4	4	1100	12	C
0101	5	5	1101	13	D
0110	6	6	1110	14	E
0111	7	7	1111	15	F

# Translating Binary to Hexadecimal

- Each hexadecimal digit corresponds to 4 binary bits.
- Example: Translate the binary integer 000101101010011110010100 to hexadecimal:

1	6	A	7	9	4
0001	0110	1010	0111	1001	0100

Binary 000101101010011110010100<sub>(2)</sub> equals hex 16A794<sub>(16)</sub>

# Converting Hexadecimal to Decimal

- Multiply each digit by its corresponding power of 16:

$$\text{dec} = (D_3 \times 16^3) + (D_2 \times 16^2) + (D_1 \times 16^1) + (D_0 \times 16^0)$$

- Hex  $1234_{(16)}$  equals  $(1 \times 16^3) + (2 \times 16^2) + (3 \times 16^1) + (4 \times 16^0)$ , or

decimal  $4,660_{(10)}$

- Hex  $3BA4_{(16)}$  equals  $(3 \times 16^3) + (11 * 16^2) + (10 \times 16^1) + (4 \times 16^0)$ ,

or decimal  $15,268_{(10)}$

# Powers of 16

Used when calculating hexadecimal values up to 8 digits long:

$16^n$	Decimal Value	$16^n$	Decimal Value
$16^0$	1	$16^4$	65,536
$16^1$	16	$16^5$	1,048,576
$16^2$	256	$16^6$	16,777,216
$16^3$	4096	$16^7$	268,435,456



# Converting Decimal to Hexadecimal

Division	Quotient	Remainder
422 / 16	26	6
26 / 16	1	A
1 / 16	0	1

decimal 422<sub>(10)</sub> = 1A6<sub>(16)</sub> hexadecimal

# Hexadecimal Addition

- Divide the sum of two digits by the number base (16). The quotient becomes the carry value, and the remainder is the sum digit.

36	28	<sup>1</sup> 28	<sup>1</sup> 6A
42	45	58	4B
<hr/>			
78	6D	80	B5

21 / 16 = 1, rem 5

Important skill: Programmers frequently add and subtract the addresses of variables and instructions.

# Hexadecimal Subtraction

- When a borrow is required from the digit to the left, add 16 (decimal) to the current digit's value:

16 + 5 = 21

↓

C6  
A2  

---

24

-1 ↓

75  
47  

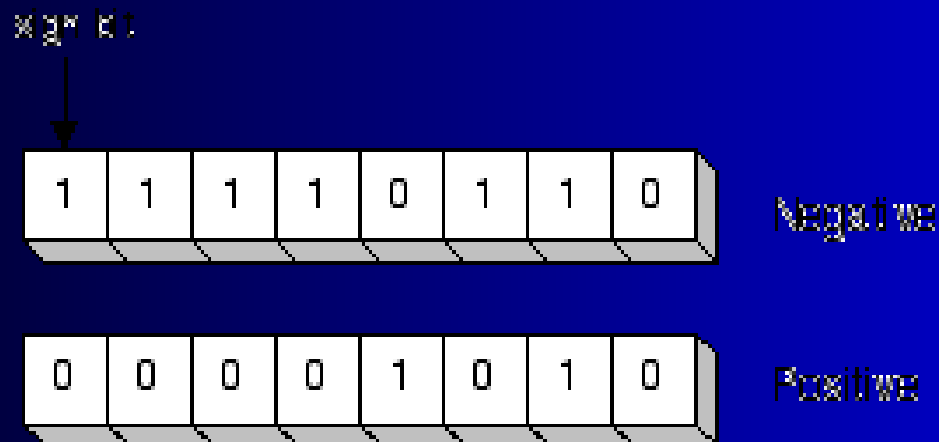
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2E

Practice: The address of **var1** is 00400020. The address of the next variable after var1 is 0040006A. How many bytes are used by var1?

# Signed Integers

The highest bit indicates the sign. 1 = negative, 0 = positive



If the highest digit of a hexadecimal integer is  $> 7$ , the value is negative. Examples: 8A, C5, A2, 9D

# Forming the Two's Complement

- Negative numbers are stored in two's complement notation
- Represents the **additive Inverse**

Starting value	00000001
Step 1: reverse the bits	11111110
Step 2: add 1 to the value from Step 1	11111110 +00000001
Sum: two's complement representation	11111111

Note that  $00000001 + 11111111 = 00000000$

# Binary Subtraction

- When subtracting  $A - B$ , convert  $B$  to its two's complement
- Add  $A$  to  $(-B)$

$$\begin{array}{r} 00001100 \\ - 00000011 \\ \hline \end{array} \longrightarrow \begin{array}{r} 00001100 \\ 11111101 \\ \hline 00001001 \end{array}$$

Practice: Subtract 0101 from 1001.

# Form the two's complement of a hexadecimal integer

- Reverse all bits and add 1
- Easy way: subtract the digits from 15 and add 1  
 $6A3D(16) \rightarrow 95C2(16) \rightarrow 95C3$

# Convert signed binary to decimal

- If the highest bit is a 1, the number is in two's complement
  - Form two's complement
  - Convert decimal number and attach a minus sign to the beginning of the decimal integer

<b>Starting value</b>	<b>11110000<sub>(2)</sub></b>
-----------------------	-------------------------------

**Step 1: Reverse the bits**      **00001111**

**Step 2: Add 1 to the value  
from step 1**

**Step 3: Form the two's complement**      **00010000**

**Step 4: Covert to decimal and attach sign**  $-16_{(10)}$

- If the highest bit is a 0, convert it to decimal integer



# Convert signed decimal to binary

- Convert the absolute value into binary
- If the original decimal number is negative, form the two's complement of the binary number

**Starting value**  **$-16_{(10)}$**

**Step 1: Convert the absolute value into binary**  **$00010000_{(2)}$**

**Step 2: Reverse the bits**  **$11101111_{(2)}$**

**Step 3: Form the two's complement**  **$11110000_{(2)}$**

# Convert signed decimal to hexadecimal

- Convert the absolute value into hexadecimal
- If the original decimal number is negative, form the two's complement of the hexadecimal number

**Starting value**  $-43_{(10)}$

**Step 1: Convert the absolute value into hexadecimal**  $2B_{(16)}$

**Step 2: Form the two's complement**  $D5_{(16)}$

# Convert signed hexadecimal to decimal

- If the hexadecimal integer is negative, form it's two's complement otherwise retain the integer as is
- Convert it to decimal number. If the original value is negative, attach a minus sign to the beginning of the decimal integer

Starting value

**D5**<sub>(16)</sub>

Step 1: Form the  
two's complement

**2B**<sub>(16)</sub>

Step 4: Covert to  
decimal and  
attach sign

**- 43**<sub>(10)</sub>

# Ranges of Signed Integers

The highest bit is reserved for the sign. This limits the range:

Storage Type	Range (low–high)	Powers of 2
Signed byte	–128 to +127	$-2^7$ to $(2^7 - 1)$
Signed word	–32,768 to +32,767	$-2^{15}$ to $(2^{15} - 1)$
Signed doubleword	–2,147,483,648 to 2,147,483,647	$-2^{31}$ to $(2^{31} - 1)$
Signed quadword	–9,223,372,036,854,775,808 to +9,223,372,036,854,775,807	$-2^{63}$ to $(2^{63} - 1)$

Practice: What is the largest positive value that may be stored in 20 bits?

# Character Storage

- Character sets
  - Standard ASCII (0 – 127)
  - Extended ASCII (0 – 255)
  - ANSI (0 – 255)
  - Unicode (0 – 65,535)
- Null-terminated String
  - Array of characters followed by a *null byte*
- Using the ASCII table
  - back inside cover of book

# Numeric Data Representation

- pure binary
  - can be calculated directly
- ASCII binary
  - string of digits: "01010101"
- ASCII decimal
  - string of digits: "65"
- ASCII hexadecimal
  - string of digits: "9C"

next: Boolean Operations

# What's Next

- Welcome to Assembly Language
- Virtual Machine Concept
- Data Representation
- **Boolean Operations**

# Boolean Operations

- NOT
- AND
- OR
- Operator Precedence
- Truth Tables



# Boolean Algebra

- Based on **symbolic logic**, designed by George Boole
- Boolean expressions created from:
  - NOT, AND, OR

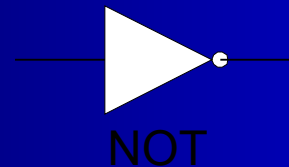
Expression	Description
$\neg X$	NOT X
$X \wedge Y$	X AND Y
$X \vee Y$	X OR Y
$\neg X \vee Y$	( NOT X ) OR Y
$\neg (X \wedge Y)$	NOT ( X AND Y )
$X \wedge \neg Y$	X AND ( NOT Y )

# NOT

- Inverts (reverses) a boolean value
- Truth table for Boolean NOT operator:

X	$\neg X$
F	T
T	F

Digital gate diagram for NOT:

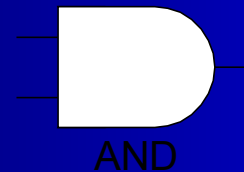


# AND

- Truth table for Boolean AND operator:

X	Y	$X \wedge Y$
F	F	F
F	T	F
T	F	F
T	T	T

Digital gate diagram for AND:

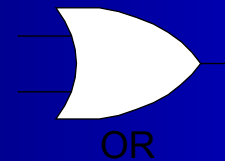


# OR

- Truth table for Boolean OR operator:

X	Y	$X \vee Y$
F	F	F
F	T	T
T	F	T
T	T	T

Digital gate diagram for OR:



# Operator Precedence

- Examples showing the order of operations:

Expression	Order of Operations
$\neg X \vee Y$	NOT, then OR
$\neg(X \vee Y)$	OR, then NOT
$X \vee (Y \wedge Z)$	AND, then OR

# Truth Tables (1 of 3)

- A **Boolean function** has one or more Boolean inputs, and returns a single Boolean output.
- A **truth table** shows all the inputs and outputs of a Boolean function

Example:  $\neg X \vee Y$

X	$\neg X$	Y	$\neg X \vee Y$
F	T	F	T
F	T	T	T
T	F	F	F
T	F	T	T

## Truth Tables (2 of 3)

- Example:  $X \wedge \neg Y$

X	Y	$\neg Y$	$X \wedge \neg Y$
F	F	T	F
F	T	F	F
T	F	T	T
T	T	F	F

# Truth Tables (3 of 3)

- Example:  $(Y \wedge S) \vee (X \wedge \neg S)$

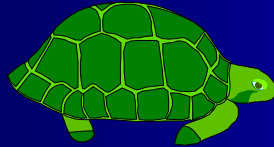
X	Y	S	$Y \wedge S$	$\neg S$	$X \wedge \neg S$	$(Y \wedge S) \vee (X \wedge \neg S)$
F	F	F	F	T	F	F
F	T	F	F	T	F	F
T	F	F	F	T	T	T
T	T	F	F	T	T	T
F	F	T	F	F	F	F
F	T	T	T	F	F	T
T	F	T	F	F	F	F
T	T	T	T	F	F	T

Two-input multiplexer



# Summary

- Assembly language helps you learn how software is constructed at the lowest levels
- Assembly language has a one-to-one relationship with machine language
- Each layer in a computer's architecture is an abstraction of a machine
  - layers can be hardware or software
- Boolean expressions are essential to the design of computer hardware and software



54 68 65 20 45 6E 64

What do these numbers represent?