

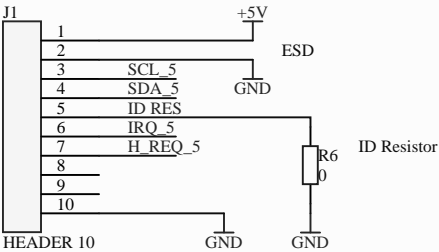
1

2

3

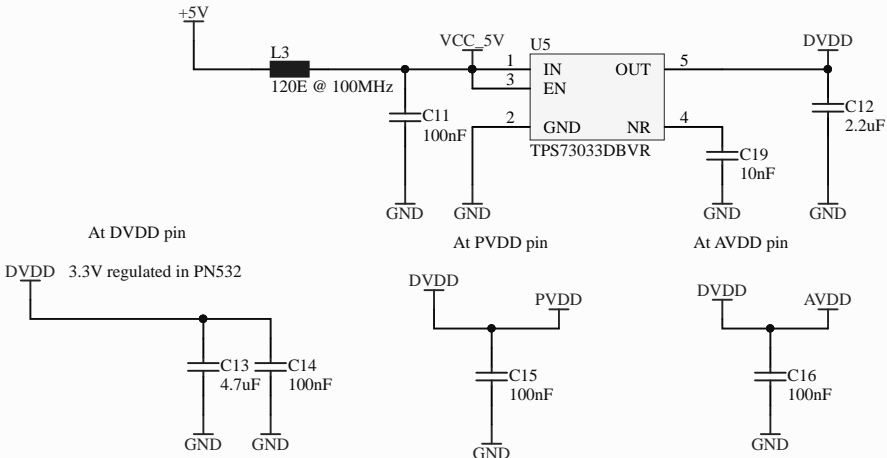
4

Interface connector

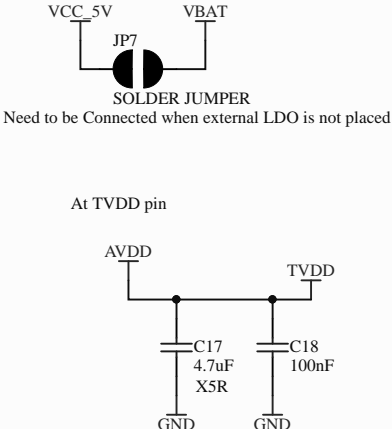


Power supply

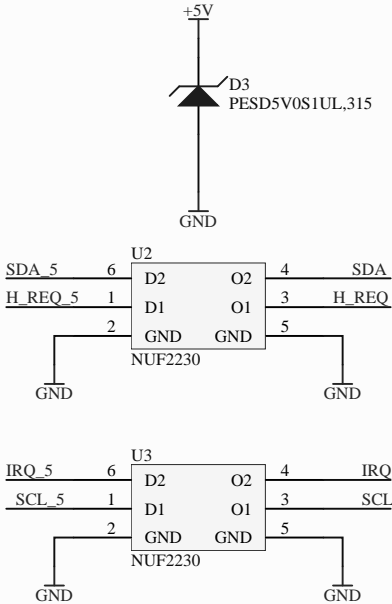
+5VDC > LDO > (3.3V) > PVDD > AVDD > TVDD



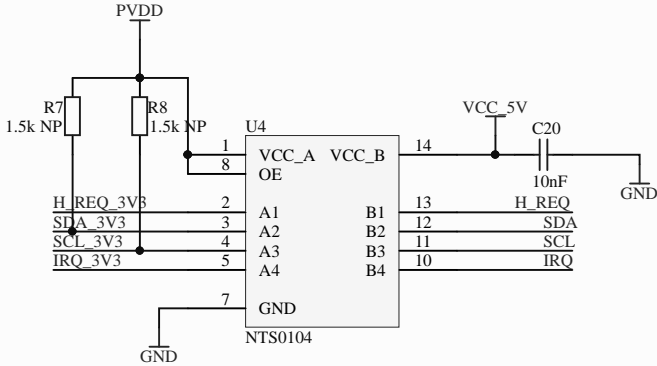
Power supply Overrule



ESD Protection Interface
place close to connector



level shifters 5V bus to 3.3V



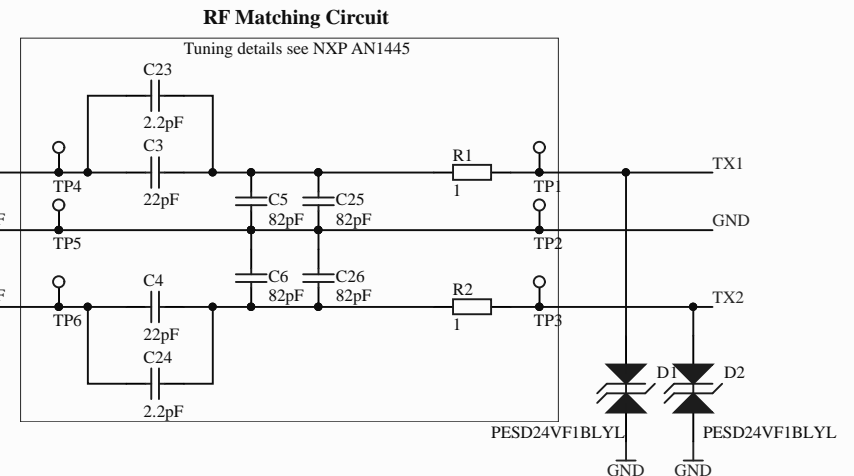
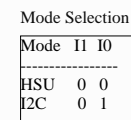
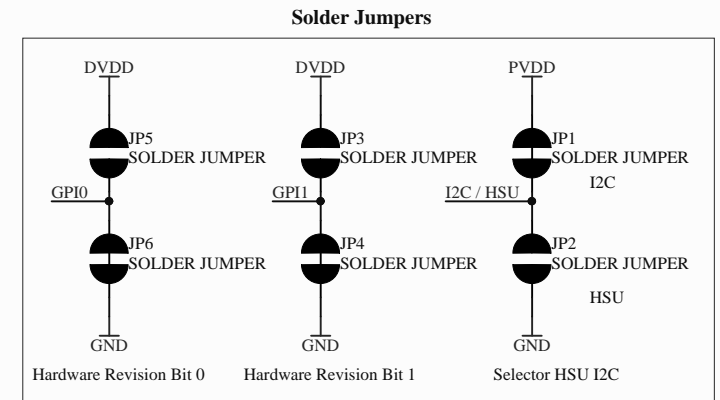
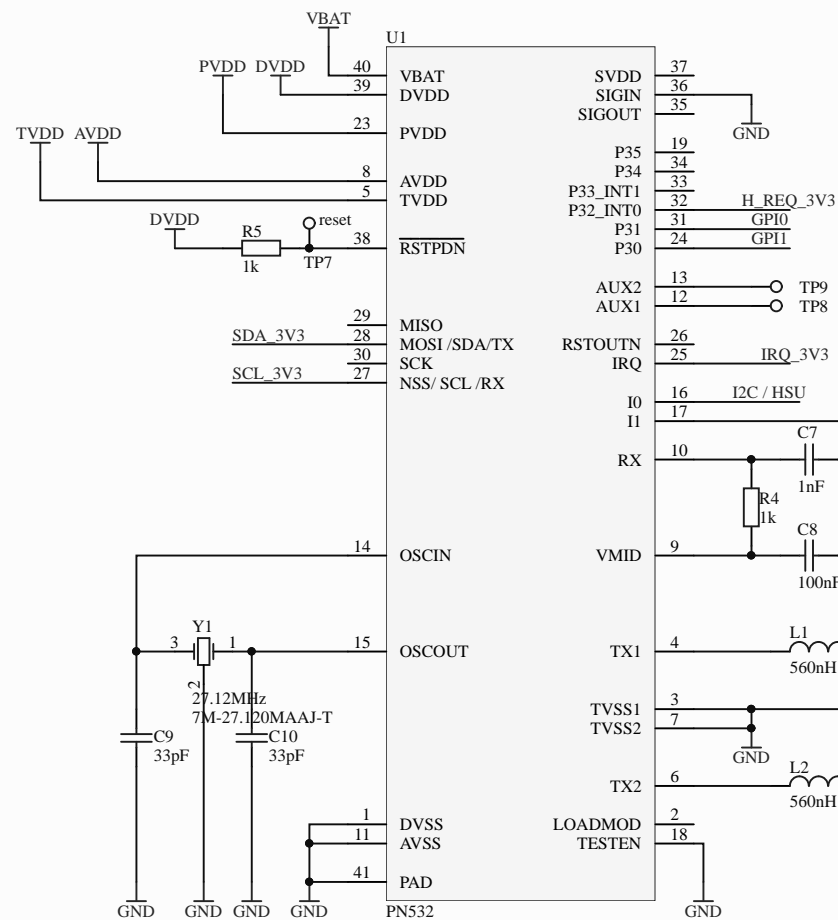
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RFID_PCB_Antenna.PrjPcb		
Size	Number	Revision
A4	*	1726-B
Date:	4-1-2017	Sheet of *
File:	C:\Users\...\RFID_PCB_Antenna_Interface\...	Drawn By: Dirk-Jan Vethaak

1

2

3

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Title			RFID_PCB_Antenna.PrjPcb		
Size	Number			Revision	
A4	*			1726-B	
Date:	4-1-2017		Sheet	of *	
File:	C:\Users\...RFID_PCB_Antenna_Main.cdr		Driss Sciflo	Dirk-Jan Vethaak	

	1	2	3	4																									
A	<div>1726-B:</div> <div>DJV 4-1-2017 BOM variant on 1726-A pcb with the pull up resistors R7 and R8 removed, for better I2C low levels.</div> <div>1726-A-03:</div> <div>DJV: PCB is the same. Matching componets are added to the assembly and BOM. C3,C4 = 22pF. 1% 50V C23,C24 = 2.2 pF. 1% 50V C5,C25 ,C6, C26 = 82pF 1% 50V R1,R2 = 1R</div> <div>1726-A-02:</div> <div>DJV PCB edited after feedback from Faber.</div> <div>1726-A :</div> <div>DJV Initial PCBA. The matching componets are not placed yet because of machting of the antenna.</div>				A																								
B					B																								
C					C																								
D	<table><tr><td colspan="4">Title</td></tr><tr><td colspan="4">RFID_PCB_Antenna.PrjPcb</td></tr><tr><td>Size</td><td colspan="2">Number</td><td>Revision</td></tr><tr><td>A4</td><td colspan="2"></td><td>1726-B</td></tr><tr><td>Date:</td><td colspan="2">4-1-2017</td><td>Sheet of</td></tr><tr><td>File:</td><td colspan="2">C:\Users\...\RFID_PCB_Antenna_Revision03.dwg</td><td>Drawn by:</td></tr></table>				Title				RFID_PCB_Antenna.PrjPcb				Size	Number		Revision	A4			1726-B	Date:	4-1-2017		Sheet of	File:	C:\Users\...\RFID_PCB_Antenna_Revision03.dwg		Drawn by:	D
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