CPE301 – SPRING 2019

Design Assignment 3B

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Directory: DA3B

1. COMPONENTS LIST AND CONNECTION BLOCK DIAGRAM w/ PINS

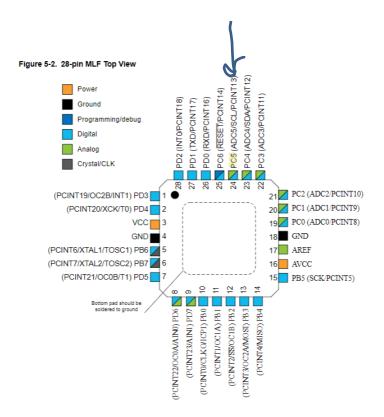
List of Components used

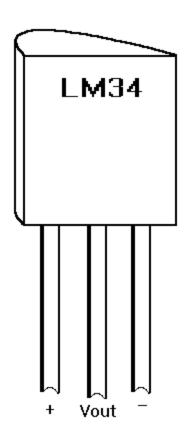
- Xplained mini
 - Breadboard
 - FTDI
 - Micro USB cable
 - Female and Male wires
 - LM34
 - Atmel Studio 7

Block diagram with pins used in the Atmega328P

Pin-out Figure 5-1. 28-pin PDIP 28 PC5 (ADC5/SCL/PCINT13) (PCINT14/RESET) PC6 (PCINT16/RXD) PD0 27 PC4 (ADC4/SDA/PCINT12) (PCINT17/TXD) PD1 26 PC3 (ADC3/PCINT11) (PCINT18/INT0) PD2 25 PC2 (ADC2/PCINT10) (PCINT19/OC2B/INT1) PD3 24 PC1 (ADC1/PCINT9) (PCINT20/XCK/T0) PD4 23 PC0 (ADC0/PCINT8) 22 GND 21 AREF (PCINT6/XTAL1/TOSC1) PB6 9 20 AVCC (PCINT7/XTAL2/TOSC2) PB7 10 19 PB5 (SCK/PCINT5) 18 PB4 (MISO/PCINT4) (PCINT21/OC0B/T1) PD5 11 (PCINT22/OC0A/AIN0) PD6 12 17 PB3 (MOSI/OC2A/PCINT3) (PCINT23/AIN1) PD7 13 16 PB2 (\$\overline{SS}/OC1B/PCINT2) (PCINTO/CLKO/ICP1) PB0 15 PB1 (OC1A/PCINT1)







2. DEVELOPED CODE OF TASK 1

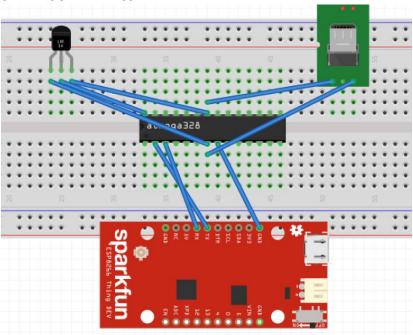
```
#define F CPU 16000000UL
#define BAUD_RATE 9600
#include <avr/io.h>
#include <util/delay.h>
#include <avr/interrupt.h>
void usart_init();
void usart_send(unsigned char ch);
int main(void)
{
       usart_init();
       /** Setup and enable ADC **/
       ADMUX = (0 << REFS1)
       (1<<REFS0) | //AVcc - external cap at AREF
       (0<<ADLAR) //ADC Left Adjust Result
       (1<<MUX2) //Analog Channel Selection Bits
       (0<<MUX1)| //ADC5(PC5 PIN28)
       (1<<MUX0);
       ADCSRA =(1<<ADEN) | //ADC Enable
       (0<<ADSC)| //ADC Start Conversion
(1<<ADATE)| //ADC Auto Trigger Enable</pre>
       (0<<ADIF) | //ADC Interrupt Flag
       (0<<ADIE) | //ADC Interrupt Enable
       (1<<ADPS2) | //ADC Prescaler Select Bits
```

```
(0<<ADPS1)
       (1<<ADPS0);
       TCCR1B =
                     5;
                           //setting
                                          the
                                                 prescaler
                                                                to
                                                                       1024
                                                                flag
       TIMSK1 =
                     (1<<TOIE1); //enable</pre>
                                                 interrupt
       TCNT1 =
                     49911; //set TCNT
       sei();//enable
                            interrupt
       while(1)
       {
              //main loop
       }
}
ISR(TIMER1 OVF vect)
       ADCSRA = (1<<ADSC); //start conversion
       while((ADCSRA&(1<<ADIF)) ==0); //wait for conversion to finish</pre>
       ADCSRA |= (1<<ADIF);
       //***************
       //used to convert and print temperatures
       int a = ADCL;
       a = a \mid (ADCH < < 8);
       a = (a/1024.0) * 5000/10;
       usart_send((a/100)+'0');
       a = a \% 100;
       usart_send((a/10) + '0');
       a = a \% 10;
       usart_send((a)+'0');
       usart_send('\r');
       TCNT1 = 49911;
       //*****************
}
usart_init(void)
{
       UCSR0B = (1<<TXEN0); //enable tx</pre>
       UCSROC = (1 << UCSZO1) | (1 << UCSZOO); //8-bit data
       UBRRØL = F_CPU/16/BAUD_RATE - 1;
}
void usart_send(unsigned char ch)
{
       while(!(UCSR0A & (1<<UDRE0))); //wait until UDR0 is empty</pre>
                                                        //transmit ch
       UDR0 = ch;
}
void usart_print(char * str)
       int i = 0; //initialize to 0
       while(str[i] != 0) //
       usart_send(str[i]); //print string at i index
}
```

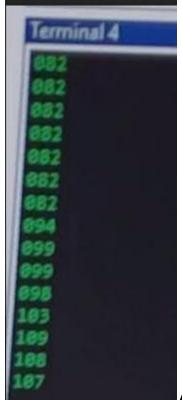
TASK 2

See Atmel Studio Output

3. SCHEMATICS

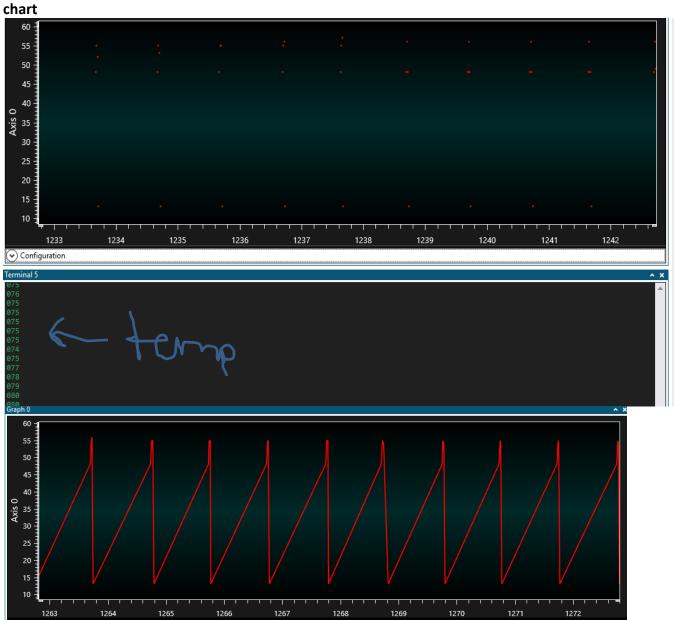


4. SCREENSHOTS OF EACH TASK OUTPUT (ATMEL STUDIO OUTPUT)



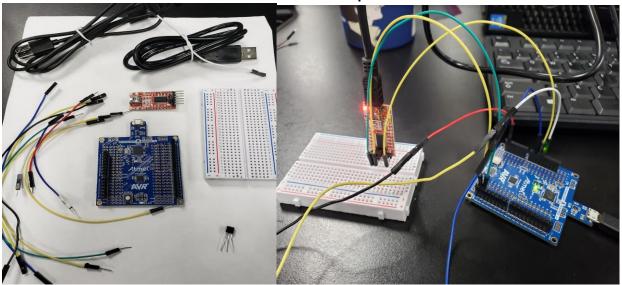
//temperatures when heat is applied

Terminal demonstrates temperature changing however I had a tough time displaying it on chart



5. SCREENSHOT OF EACH DEMO (BOARD SETUP)

Before After Setup



6. VIDEO LINKS OF EACH DEMO

https://www.youtube.com/watch?v=gv1vmSvwG6s

7. GITHUB LINK OF THIS DA

https://github.com/HadidBuilds/hw_sub_da1

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"This assignment submission is my own, original work".

Itzel Becerril