

# M2i.60xx/-Exp VEE driver

Library LibM2iBase.vee Replay Example

English version

November 16, 2017

### (c) SPECTRUM INSTRUMENTATION GMBH AHRENSFELDER WEG 13-17, 22927 GROSSHANSDORF, GERMANY

SBench, digitizerNETBOX and generatorNETBOX are registered trademarks of Spectrum Instrumentation GmbH.

Microsoft, Visual C++, Visual Basic, Windows, Windows 98, Windows NT, Window 2000, Windows XP, Windows Vista, Windows 7,

Windows 8, Windows 10 and Windows Server are trademarks/registered trademarks of Microsoft Corporation.

LabVIEW, DASYLab, Diadem and LabWindows/CVI are trademarks/registered trademarks of National Instruments Corporation.

MATLAB is a trademark/registered trademark of The Mathworks, Inc.

Delphi and C++Builder are trademarks or registered trademarks of Embarcadero Technologies, Inc.

Keysight VEE, VEE Pro and VEE OneLab are trademarks/registered trademarks of Keysight Technologies, Inc.

FlexPro is a registered trademark of Weisang GmbH & Co. KG.

PCIe, PCI Express, PCI-X and PCI-SIG are trademarks of PCI-SIG.

PICMG and CompactPCI are trademarks of the PCI Industrial Computation Manufacturers Group.

PXI is a trademark of the PXI Systems Alliance.

LXI is a registered trademark of the LXI Consortium.

IVI is a registered trademark of the IVI Foundation

Oracle and Java are registered trademarks of Oracle and/or its affiliates.

Intel and Intel Xeon are trademarks or registered trademarks of Intel Corporation.

AMD and Opteron are trademarks or registered trademarks of Advanced Micro Devices.

Installation	4
Driver structure	4
Installation of board drivers	4
Installation of VEE drivers	4
VEE Driver Update	
Demo mode	
Library LibM2iBase.vee/LibM3iBase.vee	5
Example files in directory spcm_examples	
Library LibM2iBase.vee	6
General Information	6
Initialisation	6
Error processing	
Importing library	
General User functions	
InitCard	/
Closing the card	/
CmdReset	
CmdStart	
CmdStop	
CmdEnDisTrigger	
CmdForceTrigger	
ErrorMessage	
SetupClock	
SetupFIFOMode	
ReadCardStatus	10
ReadDataStatus	10
General User Objects	11
SetupClock	11
Analog Output User functions	12
SetupAOChannel	12
SetupAOTrigger	13
SetupRepStd	
SetupRepFIFO	
WriteData i16	
Analog Output User Objects	
SetupRepStd	1.5
SetupRepFIFO	15
SetupAOTrigger	
SetupAOChannel	
BaseXIO Option	
SetupBaseXIO	
WriteBaseXIO	
ReadBaseXIO	
User Objects BaseXIO	
·	
Examples	19
Analog_Std_Out.vee	19
Analog_FIFO_Out	
Error Čodes	22

Driver structure Installation

## Installation

#### **Driver structure**

The driver for VEE is based on the standard board drivers for Windows 2000, Windows XP and Windows Vista, Windows 7 and Windows 8. New versions of the operating system and VEE drivers can be download from the internet www.spectrum-instrumentation.com free of charge. This driver supports HP-VEE, Agilent VEE, Keysight-VEE beginning with version 5.0. The driver consist of libraries for accessing the board and some demo programs that show how to use the library. The demo programs may be used as a base for own programs.

### Installation of board drivers

The standard board drivers should be installed prior to the VEE driver installation. An installation guide is found in the hardware manual of the specific board. Normally board drivers are automatically installed when the board is first time plug in the PC.

An update to e newer driver version could be done at any time without changing the VEE driver. Use at least the board driver version that was shipped together with the VEE driver. Using an older driver version is not recommended and could lead to any errors.

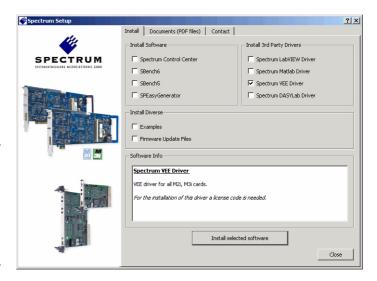
### **Installation of VEE drivers**

Please follow these steps when installing the VEE driver:

- Install the card(s) into the system as shown in the hardware manual
- Install the standard Windows driver as shown in the hardware manual
- Install the VEE driver as explained below

The VEE driver is delivered as a self extracting archive secured by an installation code. If you purchased the VEE driver together with your card you'll find the current driver on the CD delivered with the card. Please follow the CD menu to "Software Installation" -> "VEE driver" as shown on the right side.

It is also possible to install the VEE driver manually selecting the install file with the windows explorer. Please select the path CD:\Install\win\spcm\_drv\_vee\_install.exe and execute the file. The installer will guide you through the installation routing step by step.



If you purchased the VEE driver license later you'll can download the latest version from the it the Spectrum homepage. Please store the exe file somewhere on your system and start it from this location.

The VEE driver is delivered together with an installation license. You need this license for installation on a new machine. The installation license is bound to one person name/company. Please enter the license information in the exact way as it is found in the license paper. The name and company as well as the license itself is case sensitive. Please be sure to use the correct spelling including upper and lower characters. The license code itself is completely of lower characters. Please do not add any further blanks neither in the license code not in the name/company information.

The VEE driver files are installed under the program folder in an extra directory \program files\Spectrum GmbH\VEE drivers. When moving the files please make sure to move the complete directory with all sub-directories as the driver consists of several examples and libraries that are used together with the examples.





Please note that the installer and the licensing has been updated June 2010. If you purchased a license prior to that date, you'll need to obtain an updated license code from Spectrum. Please request a free update providing your card's serial number.

### **VEE Driver Update**

As the VEE driver also uses the standard Windows drivers as a base, any updates on these drivers will improve the system and and any changes are available under VEE immediately. Updating the VEE driver can simply be done by installation of the new VEE driver archive. It is normally not necessary to insert the license code again as long as the driver is updated on a machine where a previous version of the VEE driver already has been installed.

Please note that the installer and the licensing has been updated June 2010. If you purchased a license prior to that date, you'll need to obtain an updated license code from Spectrum. Please request a free update providing your card's serial number.



#### **Demo mode**

The VEE driver runs fine with demo cards installed in your system. Please follow the steps in the hardware manual to see how you insert a simulated demo card into your system. Please keep in mind that the generated data is only simulated. The simulation and calculation of demo data takes more time than just transferring data from hardware to the PC. Therefore the performance of the system is worse when using demo cards

### <u>Library LibM2iBase.vee/LibM3iBase.vee</u>

This library contains all driver functions that are related to the M2i and M2i-Express series (LibM2iBase.vee) or M3i and M3i-Express series (LibM3iBase.vee). These functions found in this library cover the complete range of boards of one series. Therefore there may be some functions that are not used on your board type. It accesses the board driver DLL. For using this library the appropriate header file Spcm\_vee16.h must be present in the same directory. If you encounter any problems when using the library please check for this file.

The library is shipped as a source code file. This allows the user to implement changes or additional functionality in the drivers. If you find any bugs in the driver library please inform us so that we can fix them.

### **Example files in directory spcm\_examples**

Together with the VEE driver library there are several examples installed in the same directory. These examples show the use of the library for different setups and board versions. However, these files are only examples that show the use of the library, they are not fully-tested applications. And they do not offer the complete functionality of the board. The examples are shipped "as-is". They could be modified in any way by the user and could also be used as a base for own applications.

General Information Library LibM2iBase.vee

## **Library LibM2iBase.vee**

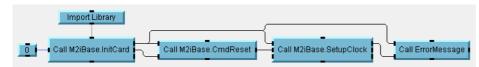
### **General Information**

This library is designed to cover the complete range of M2i- and M2i-Express boards. Therefore it has a lot of functions for data read/write and all functions for setting up the base board related stuff. Not all of these functions have to be used for your special board. Please refer to the examples to see which ones are necessary and have a look at the table on the next page.

### **Initialisation**

Before accessing the board an initialisation of the driver and the board must be performed. This is done with the user function "InitCard" described afterwards. All user functions except the Init function require the card handle to know which board should be accessed. The card handle is derived by the "InitCard" function and must be routed to all hardware accessing functions. The "InitCard" function retrieves an index of the card to be initialised. The index starts with zero with the first card found in the system.

### **Error processing**



Each user function that could generate an error in the driver has an error input and an error output. Before processing the functionality the user functions checks the error code whether it is still not set. The error code could be simply

routed through all functions calls until a final error checking with the function "ErrorMessage". The "ErrorMessage" function is described later in more detail

### **Importing library**



Before using the driver library it must be imported in your VEE program. Use the "Import library" function to do so. You need to select the path where you installed the library to. This must also be done for all the examples. The library functions are available in VEE after you one time called the "Import library" function.

Library LibM2iBase.vee General User functions

### **General User functions**

The library contains more than 20 user functions that could be used to control the M2i- and M2i-Express board series. Not all of these functions are related to the board type that you use. All settings are directly written through to the driver. Therefore all limitations, all format and constant definitions of the standard driver and are also used for the VEE driver. To see what values are allowed for an input, take a look at the hardware manual and check the setup that is described there. The use of the library is shown with the different examples explained later.

### **InitCard**

The Init user function initialises loads the driver alls that are needed, initialises the card with the given index and reads out some basic information of the card. If more than one card is in the system this function must be called multiple times with a different index to open all different cards.



The "InitCard" function has to be called at the very beginning of the program to load the needed basic driver functions.

#### **Inputs**

Devldx The card index that should be opened

#### **Outputs**

CardHandle If the card was opened successful this output contains the valid

card handle. All other hardware accessing functions need this

card handle.

Contains the error code of this function. If everything went well FrrorOut

this output is zero (ERR\_OK)

A record containing some basic card information. Feel free to CardInfo

add other information to this record if your program needs additional information of the card.

#### **Record CardInfo**

Sn

CardType The exact type of card that was found under that index. The

card types are described in the hardware manual. As an example the M2i.3011 returns #H33011

The serial number of the card as also found on the delivery

Installed features as found in register SPC\_PCIFEATURES and

described in the hardware manual

Function The basic card function as found in register SPC\_FNCTYPE The maximum sampling rate of the card

MemL The installed memory in bytes. Split into a 32 bit low part

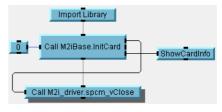
MemH and a 32 bit high part

#### Build Record CardType Sn Output Shape Features Function Record MaxSR Scalar MemH

### Closing the card

After using the "InitCard" function the driver and handle of this card is open inside VEE. It is only possible to open a hardware driver once. At the end of the program it is absolutely necessary to close the handle again to re-use this card later on. This is shown at the VEE program except at the right.

The card handle is closed automatically if the driver library is unloaded. Unfortunately VEE does not unload the drivers it has once opened when finishing a program. Therefore you may get the error message "card with that device name wasn't found" when starting your VEE program a second time. This indicates that the card handle wasn't closed properly and



the card can't be accessed. In that case please just close the VEE environment and re-open it immediately. Don't forget to update your program to have the close call at every program exit!.

General User functions Library LibM2iBase.vee

#### **CmdReset**

The reset function performs a software reset on the board. After reset all internal settings are set back to the default values and the board is in idle condition.



#### Inputs

CardHandle a valid card handle as returned from the "InitCard" function The routed error code from the last driver function ErrorIn

#### **Outputs**

ErrorOut The error code of this function or the error code of the last user

function if "Errorln" was not zero at call time

### **CmdStart**

Starts the board with current setup. All settings must be done before. If used on an output board, valid data must be written to the hardware with one of the "WriteData" user functions before. The "EnableTrigger" selects whether the card is started with trigger engine already enabled or whether this should be done later on by a specific call.



#### **Inputs**

CardHandle a valid card handle as returned from the "InitCard" function The routed error code from the last driver function ErrorIn

EnableTrigger

Write a "1" to this input to enable the trigger engine right from the start. Write a "0" to start the card without trigger enabled. It is send necessary to enable the trigger with a call to the "CmdEnDisTrigger" function later.

#### **Outputs**

ErrorOut The error code of this function or the error code of the last user function if "Errorln" was not zero at call time

### **CmdStop**

Immediately stops the board output either in standard or in FIFO mode.

#### <u>Inputs</u>

CardHandle a valid card handle as returned from the "InitCard" function The routed error code from the last driver function ErrorIn

### **Outputs**

The error code of this function or the error code of the last user ErrorOut

function if "Errorln" was not zero at call time

### **CmdEnDisTrigger**

Enables or disables the trigger engine while the card is running. While the trigger engine is disabled none of the trigger inputs will generate a card trigger. This function need to be used if the card has been started with the trigger engine disabled.



**Function Name** 

CmdStop

ErrorOut

CardHandle

ErrorIn

#### **Inputs**

CardHandle a valid card handle as returned from the "InitCard" function ErrorIn The routed error code from the last driver function EnableTrigger

Write a zero to this input to disable the trigger engine. Write anything not zero to enable the trigger engine

#### **Outputs**

8

ErrorOut The error code of this function or the error code of the last user

function if "Errorln" was not zero at call time

Library LibM2iBase.vee General User functions

### **CmdForceTrigger**

Sends a force trigger command to the card. In case that the card has not triggered on an external signal so far the card immediately triggers one time. The card then behaves as if used together with the software trigger.



#### **Inputs**

CardHandle a valid card handle as returned from the "InitCard" function ErrorIn The routed error code from the last driver function

#### **Outputs**

The error code of this function or the error code of the last user ErrorOut

### **ErrorMessage**

Checks the error code. If the error code is not zero the error information is read from the driver and displayed in a message box. Afterwards the driver is closed and the execution of the program terminates.



#### **Inputs**

CardHandle a valid card handle as returned from the "InitCard" function The routed error code from the last driver function ErrorIn

#### **Outputs**

ErrorOut The error code of this function or the error code of the last user

function if "Errorln" was zero at call time and no error was to

### **SetupClock**

Sets all clock information for the board to the driver. The driver checks them for correct settings and gives back an error code if the setup is not allowed. Some of the record fields are only used if the matching clock mode is selected. Please refer to the hardware manual for further information.



#### **Inputs**

CardHandle a valid card handle as returned from the "InitCard" function The routed error code from the last driver function ErrorIn ClockSetup A record containing all the clock setup of the card.

#### **Outputs**

ErrorOut The error code of this function or the error code of the last user

function if "Errorln" was not zero at call time

The sampling rate that was finally set by the driver. As the SamplingRate

driver sets the nearest matching sampling rate depending on the capabilities of the card this may differ from the desired

The oversampling factor is read out from the driver. This factor shows the rate by which the internal ADC is running faster than the sampling rate itself. It is used for scaling of timestamps. OverSamplingFactor

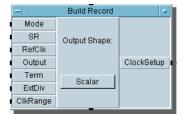
### Record ClockSetup

Mode Selected clock mode as found in the hardware manual

(SPC\_CM\_xxx)

Desired sampling rate if internal clock mode is used (in Hz) SR RefClk Fed in reference clock if reference clock mode is used (in Hz) Output A zero disables the clock output, a non-zero enables it A zero disables the clock termination, a non-zero enables it Term ExtDiv The external clock divider if one of the divided modes are used

ClkRanae The clock range if external clock is used



### **SetupFIFOMode**

This is a general setup function for the FIFO mode. Most of the FIFO mode work including the buffer handling is done inside the library. Prior to starting the FIFO mode all buffer settings have to be done with this function.

#### **Inputs**

CardHandle a valid card handle as returned from the "InitCard" function ErrorIn The routed error code from the last driver function

Buffer Type The kind of FIFO buffer to be allocated 0 = data, 1 = timestamp, 2 = aba

Writing a 1 allocates the buffer data. Writing a 0 frees the buffer data. The old Buffer is also automatically freed if a new Allocate

buffer is allocated.

The desired transfer direction: 0 = Write, 1 = Read Read/Write Buffer Length The length of the buffer to be allocated in Bytes

The notify size of the transfer in bytes. Please see the hardware manual for details on the notify size Notify Size

#### **Outputs**

ErrorOut The error code of this function or the error code of the last user

function if "Errorln" was not zero at call time.

### ReadCardStatus

Reads out the current card run status and splits the status word into the 3 run flags PretriggerFull, TriggerDetected and CardReady.

#### **Inputs**

CardHandle a valid card handle as returned from the "InitCard" function ErrorIn The routed error code from the last driver function

#### **Outputs**

TriggerDetected

The error code of this function or the error code of the last user FrrorQut

function if "Errorln" was not zero at call time.

Returns a  $_{''}1''$  if the pretrigger area has been filled and the card is able to detect trigger PretriggerFull

Returns a "1" if the trigger has been detected and the acquisi-

tion/output has started Returns a "1" if the card has stopped and the acquisition/ CardReady

replay has finished

#### **ReadDataStatus**

Reads out the current data transfer status and splits the status word. The function also reads the available bytes and the current fill size of the buffer. This function is solely used for FIFO mode.

### **Inputs**

CardHandle a valid card handle as returned from the "InitCard" function The routed error code from the last driver function FrrorIn

#### **Outputs**

NextBlock

ErrorOut The error code of this function or the error code of the last user

function if "Errorln" was not zero at call time Returns a "1" if the next data block is available

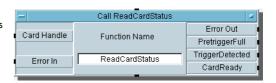
Returns a "1" if an overrun/underrun has occurred while FIFO Overrun

transfer was running

AvailableBytes The number of bytes that is available for transfer

Fillsize The current fill size of the on-board buffer in promille (1/1000)







Library LibM2iBase.vee General User Objects

## **General User Objects**

Inside the VEE library there are some different user objects that group settings that belong together and also contain the initial setup of the inputs objects. These user objects can be used to speed up vee programming and to have an easy-to-use interface that automatically disables input objects that are not used on the current setup. These user objects are also used throughout the examples.



To use these objects please just copy them from the main part of the library inside your vee program. After this you only need to connect the output of the object to the matching setup function and add the user object to the panel.

### **SetupClock**

This object handles the complete clock setup of one card. All clock related settings are grouped inside the panel. Depending on the selected clock mode some of the inputs are then disabled.

### **Inputs**

no inputs available

### **Outputs**

ClockSetup

A record containing the clock setup as needed by the function "SetupClock". Please see this chapter to have a documentation of the record



## **Analog Output User functions**

### **SetupAOChannel**

Makes the setup of one output channel. Please be sure to call this function only for output channels that are installed on the specific card type otherwise you will get an error.

#### **Inputs**

CardHandle a valid card handle as returned from the "InitCard" function ErrorIn The routed error code from the last driver function AOSetup

A record containing all the analog setup of one specific output

#### **Outputs**

The error code of this function or the error code of the last user function if " $\rm Error ln"$  was not zero at call time. ErrorOut

#### Record AOSetup

Please note that this function is a general setup function covering the complete range of Spectrum analog generator cards. Not all features are supported by all card types. The "SetupAOFunction" will automatically determine the features available and program only those features. Please just ignore the features inside the record that are not supported by your hardware.

Mode Defines the output mode of the selected channel. Depending

on the card version and the channel selection some special output modes are available. The output modes are described in detail in the hardware manual.

Filter Selects the index of the used output filter for this channel. Gain Programs the programmed bipolar output gain in mV.

Offset Programs the output offset in mV.

StopLvl Programs the behavior after replay by using the card's Stop

Channel The index of the channel which has to be programmed.





### **SetupAOTrigger**

The setup AO trigger functions offers an easy way to access some of the basic trigger functions of the card. It only supports a part of the very powerful trigger engine. The more complex trigger settings need to be programmed separately.



#### **Inputs**

CardHandle A valid card handle as returned from the "InitCard" function The routed error code from the last driver function ErrorIn AOTrigge A record containing all the trigger settings

#### **Outputs**

The error code of this function or the error code of the last user function if "Errorln" was not zero at call time. ErrorOut

#### **Record AOTrigger**

OrMask The global trigger OR mask. Inhere one of the global trigger

ces, software or external are selected

If external trigger source is selected, one of the external trigger modes need to be selected. Available trigger modes can be found in the hardware manual (SPC\_TM\_POS,..) ExtTrMode

Termination A zero disables the external trigger termination, a non-zero

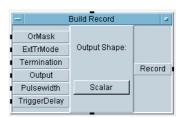
value enables i

A zero disables the external trigger output, a non-zero value Output

Pulsewidth Programs the pulsewidth in case that an trigger mode using the

pulsewidth counter has been selected.

TriggerDelay Programs the trigger delay in samples.



### **SetupRepStd**

The function programs one of the standard generation modes and all mode related settings like memory size and segment size. Please use the related "SetupRepFIFO" function in case that the FIFO mode should be used.

### **Inputs**

CardHandle a valid card handle as returned from the "InitCard" function The routed error code from the last driver function ErrorIn ChMaskHiah The channel mask of activated channels for the next run. Split

into a high 32 bit and a low 32 bit part ChMaskLow ModeSetup A record containing all the mode settings

#### **Outputs**

ErrorOut The error code of this function or the error code of the last user function if "Errorln" was not zero at call time.

The total number of activated channels. Channel Count BitsPerSample The number of Bits used per sample for storage Memsize The programmed memsize per channel.

#### Record ModeSetup

The selected replay mode as described in the hardware manual (SPC\_REP\_STD\_SINGLE, ... ). Please be sure that the selected mode is installed on your hardware. Some of the modes require Mode

an extra option to be installed

Segment size in samples/channel for all modes that use a segment size Seg Mem

Total memory in samples/channel to be replayed. Be sure not to exceed the amount of installed

memory when programming this value.

Defines the number of loops to output. A zero stands for endless looping, a 1 for one loop until the programmed memory size is one time completely replayed. The meaning of this value differs

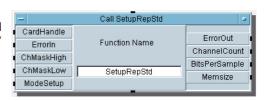
a little depending on the selected mode:

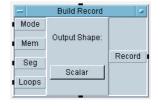
Singleshot: Defines the number of singleshots that are performed. Each detected trigger event will generate one singleshot until the loop counter expires.

Continuous: Defines the number of loops the programmed memory is replayed after one trigger

Multiple Replay: Values > 1 defines the number of segments that are replayed. Each segemnt will be replayed after detection of a new trigger event.

Gated Replay: Values > 1 defines how many gate segments are replayed





### **SetupRepFIFO**

The function programs one of the FIFO replay modes and all mode related settings like Post trigger and segment size. Please use the related "SetupRepStandard" in case that the standard mode shall be used.

#### **Inputs**

CardHandle a valid card handle as returned from the "InitCard" function ErrorIn The routed error code from the last driver function

ChMaskHighThe channel mask of activated channels for the next run. Split into a high 32 bit and a low 32 bit part

ChMaskLow ModeSetup A record containing all the mode settings

#### **Outputs**

ErrorOut The error code of this function or the error code of the last user

function if "Errorln" was not zero at call time The total number of activated channels Channel Count BitsPerSample The number of Bits used per sample for storage.

#### **Record ModeSetup**

Mode

The selected replay mode as described in the hardware manual (SPC\_REP\_FIFO\_SINGLE, ... ). Please be sure that the selected mode is installed on your hardware. Some of the modes require an extra option to be installed

Segment size in samples/channel for all modes that use a seg-Seg

Loops Selects the number of segments/gates to output, leave zero if

FIFO replay shall run endlessly.

### WriteData i16

This function writes out sorted data to the on-board memory of the FIFO buffer. It is feed with the matching setup to be written consisting of data offset and data length. Data is sorted to up to 4 output channels and normalized to 16 bit integer values.

Please be sure to set the input parameters matching the current generation. Otherwise data may be corrupted.

#### **Inputs**

Handle A valid card handle as returned from the "InitCard" function The routed error code from the last driver function FrrorIn Data Offset The offset in samples/channel from where the read function

should start

The data length in samples/channel to be written. In standard mode this is similar to the programmed memory size (SPC\_MEMSIZE) and in FIFO mode this is similar to the notify Data Length

size. When using standard mode one can write the replay data in smaller chunks by using the offset and the length

Channel Count The number of channels to be written. This must be similar to

the programmed number of channels to generate

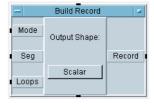
Ch0 .. Ch3 Normalized and sorted channel data in 16 bit integer format

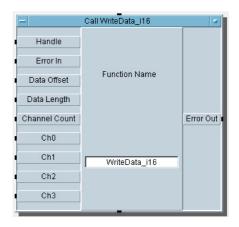
#### **Outputs**

Error Out The error code of this function or the error code of the last user

function



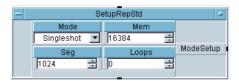




### **Analog Output User Objects**

### **SetupRepStd**

The object handles all standard data generation setup of one card. All mode and memory related settings are grouped inside this panel. Depending on the selected mode some of the fields are disabled. The mode drop-down box also contains card modes that are only available when a specific option is installed like Multiple Replay or Gated Replay. Please be sure to have this option installed before selecting this mode. Otherwise the driver will return an error code indicating that this option is not available.



#### **Inputs**

no inputs available

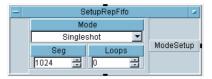
#### **Outputs**

ModeSetup

A record containing the standard mode setup as needed by the function "SetupRepStandard". Please see this chapter to have a documentation of the record.

### **SetupRepFIFO**

The object handles all FIFO data generation setup of one card. All mode and memory related settings are grouped inside this panel. Depending on the selected mode some of the fields are disabled. The mode drop-down box also contains card modes that are only available when a specific option is installed like Multiple Replay or Gated Replay. Please be sure to have this option installed before selecting this mode. Otherwise the driver will return an error code indicating that this option is not available.



#### Inputs

no inputs available

#### **Outputs**

ModeSetup

A record containing the standard mode setup as needed by the function "SetupRepFIFO". Please see this chapter to have a documentation of the record.

### **SetupAOTrigger**

This object handles the AO trigger setup. It allows to select one single trigger source and sets up all related trigger settings for this source. Depending on the selected trigger source and also depending on the selected mode some of the fields are disabled by the object. The trigger source list contains all possible trigger inputs.



#### <u>Inputs</u>

no inputs available

#### **Outputs**

AOTrigger

A record containing the standard trigger setup as needed by the function "SetupAOTrigger". Please see this chapter to have a documentation of the record.

### **SetupAOChannel**

This AOChannel object covers the complete range of Spectrum analog data generation card. Therefore it contains all possible settings that may be modified for the different card types. The ChMask input and output is an easy option to let the channel mask be setup automatically by the AOChannel object. Therefore the object examines the activation checkbox and sets the corresponding bits inside the ChMask bit-field. Please simply route the ChMask through all SetupAOChannel objects, initialised with a zero.



### <u>Inputs</u>

The index of the channel for which the setup object is called Channel

ChMask The current channel mask to be routed through

Enabled Enable or disable the channel

### **Outputs**

A record containing the AO channel setup as needed by the function "SetupAOChannel". Please see this chapter to have a documentation of the record The updated channel mask AOSetup

 $\mathsf{ChMask}$ 

## **BaseXIO Option**

The following user functions and user objects are only available if the option BaseXIO is installed on your card.

### <u>SetupBaseXIO</u>

Checks whether the BaseXIO option is installed and sets the direction of each group of 4 BaseXIO lines.

#### **Inputs**

CardHandle a valid card handle as returned from the InitCard" function FrrorIn The routed error code from the last driver function DirectHigh Direction of the upper 4 BaseXIO lines (line 4 to 7). A zero

turns the lines to input, a "1" to output.

Direction of the lower 4 BaseXIO lines (line 0 to 3). A zero turns the lines to input, a  $_{\rm n}1^{\prime\prime}$  to output. DirectLow

#### **Outputs**

ErrorOut The error code of this function or the error code of the last user

function if "Errorln" was not zero at call time

**BaseXIOAvailable** A  $_{\prime\prime}1^{\prime\prime}$  indicates that the BaseXIO option is installed

#### **WriteBaseXIO**

The function writes a BaseXIO value to the outputs

#### **Inputs**

CardHandle a valid card handle as returned from the "InitCard" function The routed error code from the last driver function ErrorIn XIOHigh The upper 4 BaseXIO outputs (line 4 to 7) The lower 4 BaseXIO outputs (line 0 to 3) XIOLow

#### **Outputs**

ErrorOut The error code of this function or the error code of the last user function if "Errorln" was not zero at call time

#### **ReadBaseXIO**

The functions reads the BaseXIO lines.

### **Inputs**

CardHandle a valid card handle as returned from the "InitCard" function The routed error code from the last driver function ErrorIn

#### **Outputs**

ErrorOut The error code of this function or the error code of the last user function if "Errorln" was not zero at call time

The upper 4 BaseXIO inputs (line 4 to 7) XIOHigh XIOLow The lower 4 BaseXIO inputs (line 4 to 7)

## **User Objects BaseXIO**

#### **SetupBaseXIO**

**SetDataBaseXIO** 

This user object only has a function if the option BaseXIO is installed on the card. The direction and some write data of the BaseXIO option are selected and collected to a record.

### **Outputs**

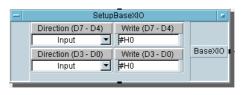
Record containing the direction and write data for both bundles of BaseXIO lines BaseXIO

The function writes the data from the SetupBaseXIO function and reads out and displays the BaseXIO lines.











### <u>Inputs</u>

CardHandle a valid card handle as returned from the "InitCard" function

The routed error code from the last driver function record collected by "SetupBaseXIO" ErrorIn

BaseXIO

### **Outputs**

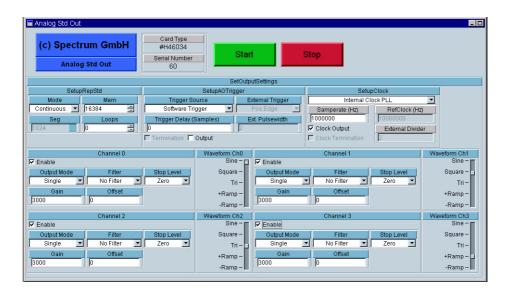
The error code of this function or the error code of the last user function if "Errorln" was not zero at call time. ErrorOut

Examples Analog\_Std\_Out.vee

## **Examples**

These examples show the use of the driver for different types of boards. There are examples for standard mode and for FIFO mode. The standard mode examples can be used as a simple arbitrary waveform generator utility for the board. The examples can be used to test the board or as a base for an own program. The examples can be changed and used for own applications without any limitations.

Prior to the first start the path to the library must be changed in the examples to work properly on the installed system.



### **Analog Std Out.vee**

This example shows the use of the VEE library for analog data generation cards with up to 4 channels. The example works with analog data generation cards of the M2i and M2i-Express series. After running the vee program the interface will be updated permanently with the changes and certain input fields will be disabled based on the current selection.

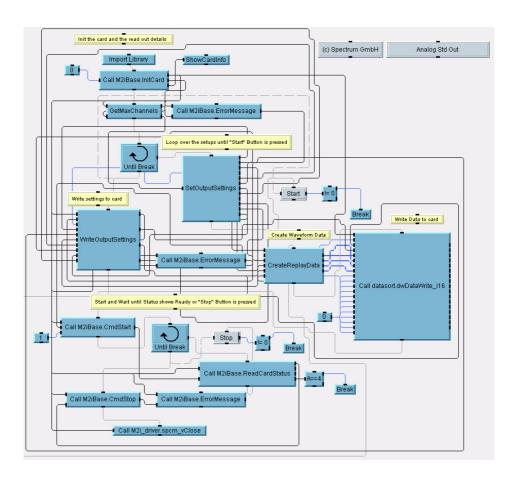
Pressing the "Start" button starts the data output. The output data is directly available at the outputs afterwards. The replay will run for a finite or infinite time (depending on the selected mode) or until the stop button has been pressed.

### Local objects

The calls to the user functions of the library are encapsulated in local user objects to get a better visibility. These user objects are directly taken from the library and can be also used for own programs.

Analog\_Std\_Out.vee Examples

### <u>Setup</u>

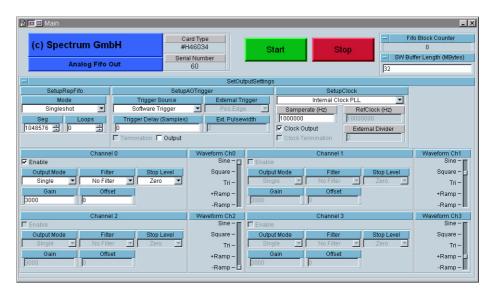


## **Analog FIFO Out**

This example shows the use of the FIFO mode with the VEE driver. It works similar to the Standard mode example, but will continuously generate data until the user stops the generation. The example supports up to 4 analog output channels.

#### <u>Setup</u>

After running the example one can adjust the setup to match the requirements. In that stage all the setup objects are running inside a loop and input fields that are not available due to the current setup are disabled.



### FIFO speed

The speed of the FIFO mode is affected by system speed, by other tasks that run in the background and by the software itself. If buffer underruns occur, set the buffer size and number of buffers to higher values.

### Fifo Block Counter

The display box shows the number of buffers that have been transferred so far by the FIFO mode example. Each buffer has a size that has been defined by the function "SetupFIFO".

## **Error Codes**

The following error codes could occur when a driver function has been called. Please check carefully the allowed setup for the register and change the settings to run the program.

error name	value (hex)	value (dec.)	error description
ERR_OK	Oh	0	Execution OK, no error.
ERR_INIT	1h	1	An error occurred when initializing the given card. Either the card has already been opened by another process or an hardware error occurred.
ERR_TYP	3h	3	Initialization only: The type of board is unknown. This is a critical error. Please check whether the board is correctly plugged in the slot and whether you have the latest driver version.
ERR_FNCNOTSUPPORTED	4h	4	This function is not supported by the hardware version.
ERR_BRDREMAP	5h	5	The board index re map table in the registry is wrong. Either delete this table or check it carefully for double values.
ERR_KERNELVERSION	6h	6	The version of the kernel driver is not matching the version of the DLL. Please do a complete re-installation of the hard-
EMEREM IEE/EMOIO!	· · ·	· ·	ware driver. This error normally only occurs if someone copies the driver library and the kernel driver manually.
err_hwdrvversion	7h	7	The hardware needs a newer driver version to run properly. Please install the driver that was delivered together with the card.
err_adrrange	8h	8	One of the address ranges is disabled (fatal error), can only occur under Linux.
ERR_INVALIDHANDLE	9h	9	The used handle is not valid.
err_boardnotfound	Ah	10	A card with the given name has not been found.
ERR_BOARDINUSE	Bh	11	A card with given name is already in use by another application.
ERR_EXPHW64BITADR	Ch	12	Express hardware version not able to handle 64 bit addressing -> update needed.
err_fwversion	Dh	13	Firmware versions of synchronized cards or for this driver do not match -> update needed.
ERR_LASTERR	10h	16	Old error waiting to be read. Please read the full error information before proceeding. The driver is locked until the
ERR_BOARDINUSE	11h	1 <i>7</i>	error information has been read.  Board is already used by another application. It is not possible to use one hardware from two different programs at the
			same time.
ERR_ABORT	20h	32	Abort of wait function. This return value just tells that the function has been aborted from another thread. The driver library is not locked if this error occurs.
ERR_BOARDLOCKED	30h	48	The card is already in access and therefore locked by another process. It is not possible to access one card through multiple processes. Only one process can access a specific card at the time.
ERR_DEVICE_MAPPING	32h	50	The device is mapped to an invalid device. The device mapping can be accessed via the Control Center.
ERR_NETWORKSETUP	40h	64	The network setup of a digitizerNETBOX has failed.
ERR_NETWORKTRANSFER	41h	65	The network data transfer from/to a digitizerNETBOX has failed.
ERR FWPOWERCYCLE	42h	66	Power cycle (PC off/on) is needed to update the card's firmware (a simple OS reboot is not sufficient!)
ERR_NETWORKTIMEOUT	43h	67	A network timeout has occurred.
ERR_BUFFERSIZE	44h	68	The buffer size is not sufficient (too small).
ERR_RESTRICTEDACCESS	45h	69	The access to the card has been intentionally restricted.
ERR_INVALIDPARAM	46h	70	An invalid parameter has been used for a certain function.
ERR_REG	100h	256	The register is not valid for this type of board.
ERR_VALUE	101h	257	The value for this register is not in a valid range. The allowed values and ranges are listed in the board specific documentation.
ERR_FEATURE	102h	258	Feature (option) is not installed on this board. It's not possible to access this feature if it's not installed.
err_sequence	103h	259	Command sequence is not allowed. Please check the manual carefully to see which command sequences are possible.
ERR_READABORT	104h	260	Data read is not allowed after aborting the data acquisition.
ERR_NOACCESS	105h	261	Access to this register is denied. This register is not accessible for users.
ERR_TIMEOUT	107h	263	A timeout occurred while waiting for an interrupt. This error does not lock the driver.
ERR_CALLTYPE	108h	264	The access to the register is only allowed with one 64 bit access but not with the multiplexed 32 bit (high and low double word) version.
ERR_EXCEEDSINT32	109h	265	The return value is int32 but the software register exceeds the 32 bit integer range. Use double int32 or int64 accesses instead, to get correct return values.
err_nowriteallowed	10Ah	266	The register that should be written is a read-only register. No write accesses are allowed.
ERR_SETUP	10Bh	267	The programmed setup for the card is not valid. The error register will show you which setting generates the error mes-
ERR_CLOCKNOTLOCKED	10Ch	268	sage. This error is returned if the card is started or the setup is written.  Synchronization to external clock failed: no signal connected or signal not stable. Please check external clock or try to
			use a different sampling clock to make the PLL locking easier.
err_channel	110h	272	The channel number may not be accessed on the board: Either it is not a valid channel number or the channel is not accessible due to the current setup (e.g. Only channel 0 is accessible in interlace mode)
ERR_NOTIFYSIZE	111h	273	The notify size of the last spcm_dwDefTransfer call is not valid. The notify size must be a multiple of the page size of 4096. For data transfer it may also be a fraction of 4k in the range of 16, 32, 64, 128, 256, 512, 1k or 2k. For ABA
			and timestamp the notify size can be 2k as a minimum.
err_running	120h	288	The board is still running, this function is not available now or this register is not accessible now.
ERR_ADJUST	130h	304	Automatic card calibration has reported an error. Please check the card inputs.
ERR_PRETRIGGERLEN	140h	320	The calculated pretrigger size (resulting from the user defined posttrigger values) exceeds the allowed limit.
ERR_DIRMISMATCH	141h	321	The direction of card and memory transfer mismatch. In normal operation mode it is not possible to transfer data from PC memory to card if the card is an acquisition card nor it is possible to transfer data from card to PC memory if the card is a generation card.
ERR_POSTEXCDSEGMENT	142h	322	The posttrigger value exceeds the programmed segment size in multiple recording/ABA mode. A delay of the multiple
err_segmentinmem	143h	323	recording segments is only possible by using the delay trigger!  Memsize is not a multiple of segment size when using Multiple Recording/Replay or ABA mode. The programmed seg-
			ment size must match the programmed memory size.
ERR_MULTIPLEPW	144h	324	Multiple pulsewidth counters used but card only supports one at the time.
err_nochannelpwor	145h	325	The channel pulsewidth on this card can't be used together with the OR conjunction. Please use the AND conjunction of the channel trigger sources.
ERR_ANDORMASKOVRLAP	146h	326	Trigger AND mask and OR mask overlap in at least one channel. Each trigger source can only be used either in the AND mask or in the OR mask, no source can be used for both.
err_andmaskedge	1 <i>47</i> h	327	One channel is activated for trigger detection in the AND mask but has been programmed to a trigger mode using an edge trigger. The AND mask can only work with level trigger modes.
ERR_ORMASKLEVEL	148h	328	One channel is activated for trigger detection in the OR mask but has been programmed to a trigger mode using a level trigger. The OR mask can only work together with edge trigger modes.
ERR_EDGEPERMOD	149h	329	This card is only capable to have one programmed trigger edge for each module that is installed. It is not possible to
err_dolevelmindiff	14Ah	330	mix different trigger edges on one module.  The minimum difference between low output level and high output level is not reached.
ERR_STARHUBENABLE	14Bh	331	The card holding the star-hub must be enabled when doing synchronization.

error name	value (hex)	value (dec.)	error description
ERR_PATPWSMALLEDGE	14Ch	332	Combination of pattern with pulsewidth smaller and edge is not allowed.
ERR_PCICHECKSUM	203h	515	The check sum of the card information has failed. This could be a critical hardware failure. Restart the system and check the connection of the card in the slot.
ERR_MEMALLOC	205h	51 <i>7</i>	Internal memory allocation failed. Please restart the system and be sure that there is enough free memory.
ERR_EEPROMLOAD	206h	518	Timeout occurred while loading information from the on-board EEProm. This could be a critical hardware failure. Please restart the system and check the PCI connector.
ERR_CARDNOSUPPORT	207h	519	The card that has been found in the system seems to be a valid Spectrum card of a type that is supported by the driver but the driver did not find this special type internally. Please get the latest driver from www.spectrum-instrumentation.com and install this one.
err_fifohwoverrun	301h	769	Hardware buffer overrun in FIFO mode. The complete on-board memory has been filled with data and data wasn't transferred fast enough to PC memory. If acquisition speed is smaller than the theoretical bus transfer speed please check the application buffer and try to improve the handling of this one.
err_fifofinished	302h	770	FIFO transfer has been finished, programmed data length has been transferred completely.
ERR_TIMESTAMP_SYNC	310h	784	Synchronization to timestamp reference clock failed. Please check the connection and the signal levels of the reference clock input.
ERR_STARHUB	320h	800	The auto routing function of the Star-Hub initialization has failed. Please check whether all cables are mounted correctly.
err_internal_error	FFFFh	65535	Internal hardware error detected. Please check for driver and firmware update of the card.