- a) Which objects in the diagram instantiate which? random_test -> env_h env_h -> random_tester_h, uvm_tlm_fifo, driver_h, coverage_h, command_monitor_h, result_monitor_h, scoreboard_h scoreboard_h -> uvm_tlm_analysis_fifo
- b) The following objects in the diagram are created by the new() function: uvm_tlm_analysis_fifo, uvm_tlm_fifo
- c) The following objects in the diagram are created by reference to the UVM factory: random_test, env_h, random_tester_h, driver_h, coverage_h, command_monitor_h, result_monitor_h, scoreboard_h
- d) Objects of the following classes retrieve BFM information from UVM by calling uvm_config_db: driver, command_monitor, result_monitor
- e) Objects of the following classes instantiate uvm_analysis_port: command_monitor, result_monitor
- f) Objects of the following classes instantiate uvm_get_port: driver
- g) Objects of the following classes instantiate uvm_put_port: base_tester, random_tester, add_tester
- h) The following uvm_*_port classes are parametrized: uvm_analysis_port, uvm_get_port, uvm_put_port
- i) The following classes include the void write() method: coverage, scoreboard

Name	Type	Size	Value
uvm test top	random test		@1936
env h	env		@2013
command f	uvm tlm fifo #(T)		@2043
get ap	uvm analysis port		@2247
get_peek_export	uvm get peek imp		@2147
put ap	uvm analysis port		@2197
put export	uvm put imp		@2095
command monitor h	command monitor		@2509
ар	uvm_analysis_port		@2599
coverage h	coverage		@2342
analysis imp	uvm analysis imp		@2392
drive h	driver		@2311
command port	uvm get port		@2655
random tester h	random tester		@2280
command port	uvm put port		@2707
result monitor h	result monitor		@2540
ар	uvm_analysis_port		@2761
scoreboard h	scoreboard		@2426
analysis imp	uvm analysis imp		@2476
cmd f	uvm tlm analysis fifo #(T)		@2797
analysis_export	uvm_analysis_imp		@3043
get_ap	uvm_analysis_port		@2992
get_peek_export	uvm_get_peek_imp		@2894
put_ap	uvm_analysis_port		@2943
put_export	uvm_put_imp		@2845

Fig. 1. Printout of the UVM structure

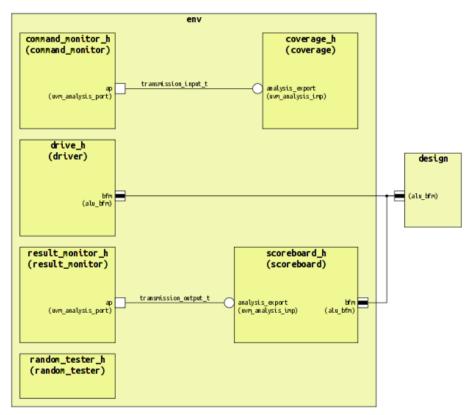


Fig. 2. UVM block diagram

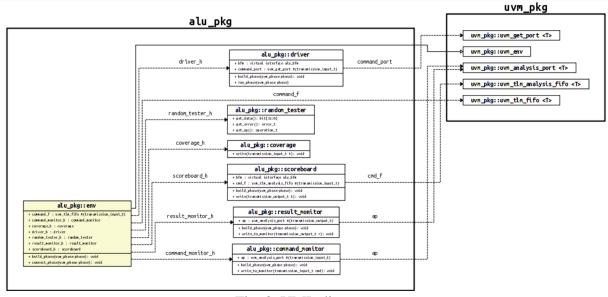


Fig. 3. UML diagram