

# MTM ALU test specification

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GIT repo: <https://github.com/Haggs3/VDIC>

## **A. Operations & sequences**

1. Test all operations.
2. Execute all operations after reset.
3. Execute reset after all operations.
4. Simulate all operations twice in a row.
5. Check ALU flags after all operations.

## **B. Specific data corners**

1. Simulate all zeros on an input for all operations.
2. Simulate all ones on an input for all operations.

## **C. Error detection**

1. Simulate all errors.