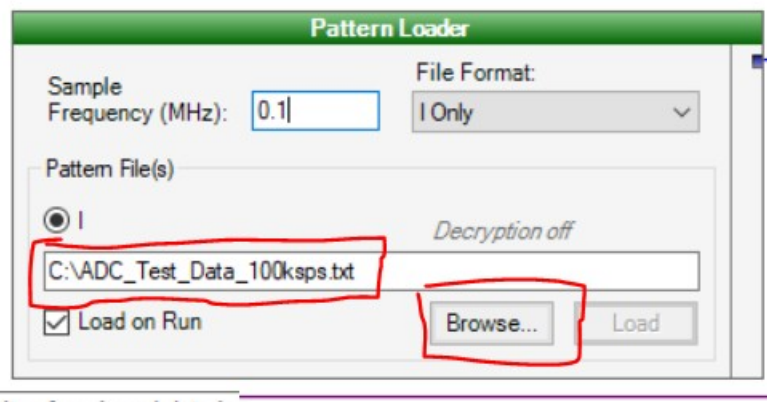


Analyze Any ADC Data with Analog Devices VisualAnalog

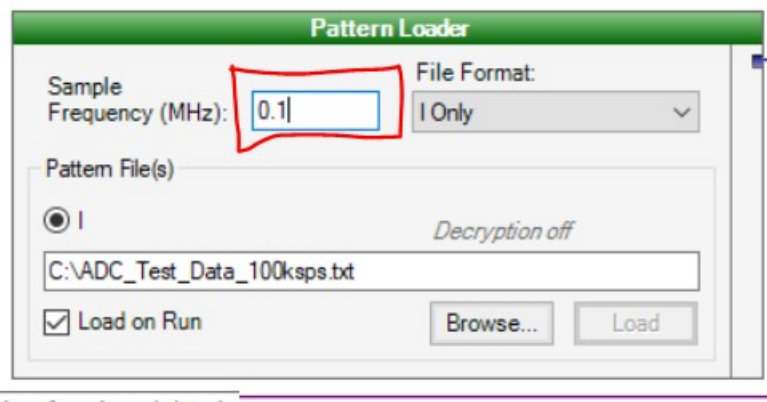
By: Steve Hageman / July 4, 2022

- 1) Download and install VisualAnalog from the Analog Devices Website.
- 2) Download my VisualAnalog analysis script from my Github at, <https://github.com/Hagtronics/BlasterAmp/tree/main/VisualAnalog>
- 3) Optional - Download the sample data CSV file from the same Github page above.
- 4) Start VisualAnalog, load the script: “Analyze_ADC_Data.vac” that you downloaded.
- 5) In the Green box named “Pattern Loader”, bit the “Browse” button and load the “ADC_Test_Data_100kps.txt” file that was optionally downloaded as per above. Or you can load any of your actual ADC Data now,

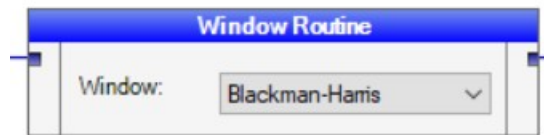
Note: The ADC data to analyze can be any length and is not constrained to power of two only.



- 6) Tell the script what the sampling rate is, my test data was sampled at 100 kps (100,000 sps),

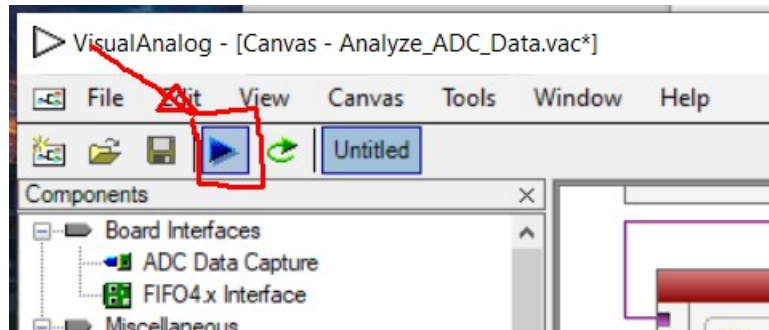


7) Optionally you can change the analysis window that is applied to the ADC test data,

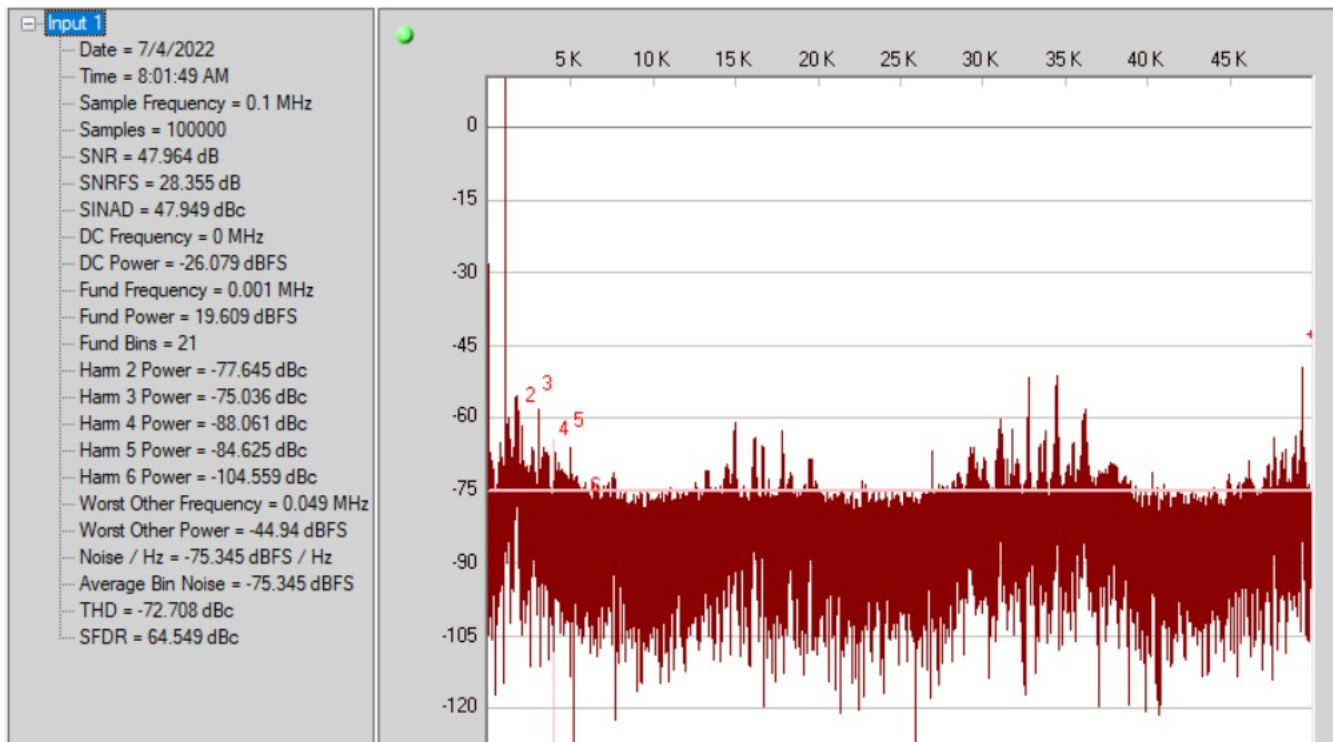


Note: The Blackman-Harris window is good for any analysis.

8) Press “Run” from the VisualAnalog Main Menu,



9) The “Plot” will now show the FFT of the data and along the left edge is the analysis parameters containing the calculated: SNR, SINAD, etc.



- Fini -