





The incoming NMEA stream from MAIANA arrives at USART2. Application firmware does not enable USART1 and must also explicitly enable this buffer in order to consume data via USART2. If the MCU enters the ROM bootloader, both USART1 and USART2 will listen for incoming commands. But since this buffer will be disabled (PBO stays low), nothing will arrive at USART2, so only USART1 will become active. +3.30 NMEA_RX_EN R10 10k TRANSPONDER_UART_TXD NMEA_RX Q3 BSS138 +3.3V \rightarrow NRST 9 9 GND GND PA1 31 _{PH3} -DSPI_CS воотор— NMEA_RX PA4 10 ×2 PC14 ×3 PC15 PA5 11 R8 -DSPI_SCK PA6 12 10k —DSPI_MISO 10k NMEA_RX_EN 14 15 PB1 26 PB3 27 PB4 28 PB5 29 PB6 30 PB7 PA7 13 —dspi_mosi PA8 19 PA9 20 PA10 21 GND —DMCU_UART_TX —dMCU_UART_RX PA11 21 PA12 23 PA13 23 PA14 24 PA15 25 ♥ U1 ♥ STM32L432KBUx Maverick Labs LLC Sheet: /MCU/ File: mcu.kicad_sch Title: MAIANA NMEA2000 Adapter Size: A4 Date: 2023-09-23 Rev: 8 KiCad E.D.A. kicad 7.0.5-0 ld: 4/6



