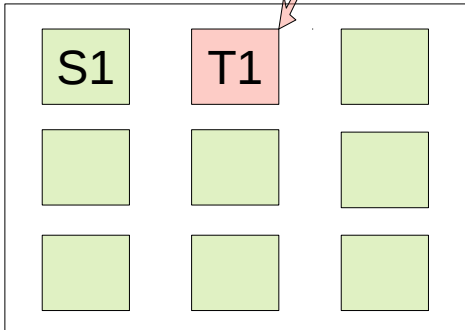


FPGA



$t+1$

A thick gray arrow pointing from the left FPGA grid to the right FPGA grid, indicating a transition or update from time  $t$  to time  $t+1$ .

FPGA

