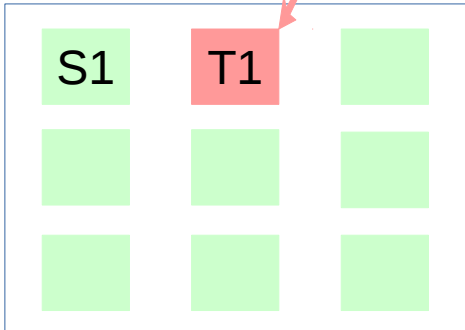


FPGA



$t+1$

A blue arrow pointing from the left FPGA grid to the right FPGA grid, indicating a transition from time t to time $t+1$.

FPGA

