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* Model Usage Notes:
* A. The following features have been modeled
*   1. Current limit, Hiccup & restart.
*   2. PGOOD threshold and hysteresis.
*   3. Minimum switch on time.
*   4. Frequency of operation depending RT pin connection.
*   5. External clock synchronization.
*   6. Ultra low IQ mode of operation.
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* B. Features have not been modelled
*   1. Operating Quiescent Current and Shutdown Current
*   2. Sync feature
*   3. DITH pin functionality and Temperature dependent characteristics
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* C. Application Notes
*   1. The parameter MODE has been used to reach the steady state faster.
*      Keep MODE = 0 to observe startup behaviour.
*      Keep MODE = 1 and appropriate IC on Inductor and capacitor to observe for faster Steady state.
*   2. Ground Pins have been tied to OV internally and hence model does not support Inverting
*      topologies.
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