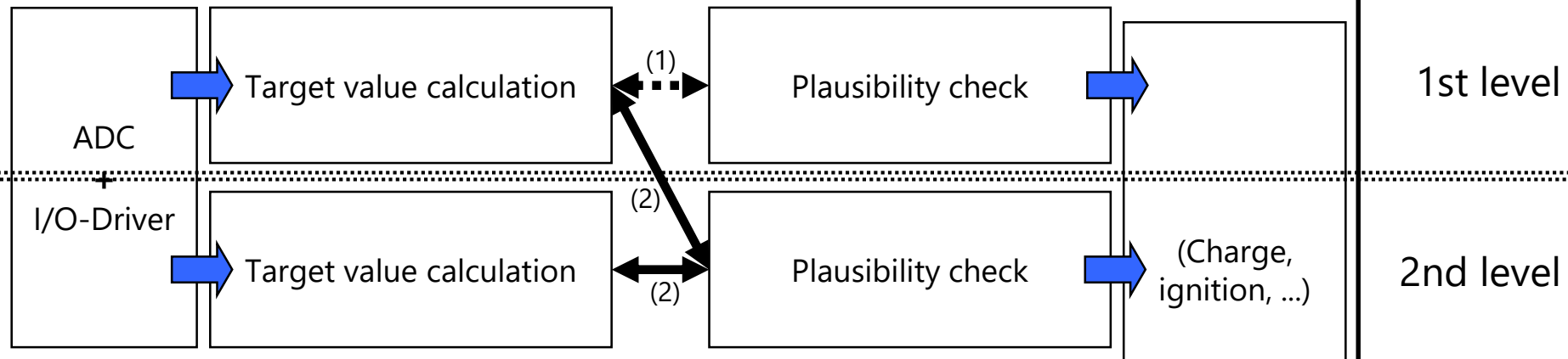
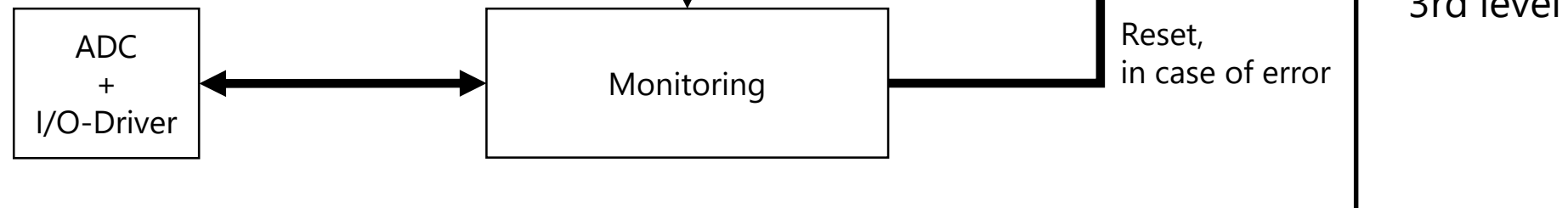


Processor A



Monitoring

Processor B



3rd level

Reset,
in case of error