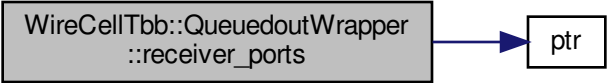


WireCellTbb::QueuedoutWrapper
::receiver_ports



```
graph LR; A[WireCellTbb::QueuedoutWrapper::receiver_ports] --> B[ptr];
```

ptr