

HW 6: Memory

CS 162

Due: April 17, 2020

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1 Introduction

The Pintos userspace that you implemented in Project 1 is subject to two major limitations. First, the stack, which you set up properly to load a new process, is confined to a single page. If a user program's stack grows beyond 4,096 bytes, due to a long chain of function calls (e.g., due to a recursive function) and/or stack-allocated data, then the process will be terminated on its first memory access beyond the first stack page. Second, there is no way to perform any dynamic memory allocation in a user program. Presently, all memory in a user process must either have been loaded from the executable (e.g., code, globals) or must be allocated on the stack.

In this homework, you will remove both of these limitations. Furthermore, you will be implementing a software page table walker using an x86 architecture with Physical Address Extension (PAE). In Part A, you will modify Pintos to dynamically extend the stack space of a process in response to memory accesses beyond the currently allocated stack. In Part B, you will provide a way for a user process to explicitly request more memory from the Pintos kernel, and then use that functionality to implement dynamic memory allocation (`malloc/calloc/realloc/free`) for user processes.

This assignment is due at 11:59 pm on 4/17/2020.

2 Logistics

This homework builds on the Pintos userspace that you implemented in Project 1. Unlike Project 1, however, this is an **individual** assignment. **You should NOT share code with your group members to complete this assignment!**

To simplify the logistics, we are providing a minimal implementation of the requisite functionality from Project 1 to complete this assignment. *You should complete this assignment by building on the userspace implementation that we provide you in the starter code, NOT using your group's implementation from Project 1.* That said, the nature of this assignment's dependency on Project 1 is that, while certain features of the Pintos userspace must be working in order for this assignment to be well-formed (e.g., you have to be able to start a user process before it makes sense to extend its stack), the code you write for this assignment will not depend directly on the implementation of Project 1 features. Therefore, we expect that it will be easy for you to port your implementation of this assignment onto your implementation of the Pintos projects at the end of the term, should you desire to do so.

3 Address Translation Exercise

Before we work on Pintos, we will begin with an exercise translating a virtual address to a physical address based on an x86 architecture with Physical Address Extension (PAE). This memory hierarchy describes a three-level page table (assuming a 4KB page size).

3.1 Translation Hierarchy

With Physical Address Extension¹, there are different formats for each page entry. Recall in x86, our CR3 register serves as the page table base register. Given a 32-bit virtual address, the CR3 register first indexes to the page global directory (page directory pointer table), using the first 2 bits of our virtual address. We then extract the physical frame number (PFN) to retrieve the page middle directory (page directory table in the diagram below). Once we receive the page middle directory, we then use the second-level index, or the next 9 bits, to index into our page middle directory table. Like before, we extract the PFN from the page directory table to get the "leaf" page table. A "leaf" page table is a page table that does not point to other page tables. We then use our third-level index, the next 9 bits after, to finally get our page table entry. This process is visualized below. For reference, the formats of

¹For more information on PAE, you may consult the [IA32 Vol3 Manual](#)² at section 3.8.1

the different page entries and the virtual address are listed below. Each page entry is 64-bits long, while the virtual address is 32-bits. In the diagrams below, the right-most bit is the least significant bit.

Virtual Address Format

PD Pointer Index (2 bits)	PD Index (9 bits)	PT Index (9 bits)	Offset (12 bits)
---------------------------	-------------------	-------------------	------------------

Page Directory Pointer Entry

R: 12	PFN: 40	I: 3	R: 4	PCD: 1	PWT: 1	R: 2	P: 1
-------	---------	------	------	--------	--------	------	------

Page Directory Entry

NXE: 1	R: 11	PFN: 40	I: 4	PG_SIZE: 1	I: 1	A: 1	PCD: 1	PWT: 1	S: 1	W: 1	P: 1
--------	-------	---------	------	------------	------	------	--------	--------	------	------	------

Page Table Entry

NXE: 1	R: 11	PFN: 40	I: 3	G: 1	PAT: 1	D: 1	A: 1	PCD: 1	PWT: 1	S: 1	W: 1	P: 1
--------	-------	---------	------	------	--------	------	------	--------	--------	------	------	------

Field Description

P	Present bit, indicates whether page is in physical memory
R	Reserved bits, must be 0
S	Page is only accessible in supervisor mode
PFN	Physical Frame Number (physical page number)
G	Global bit, on if accessible by all processes
PG_SIZE	1 if the entry points to a 2 MB page, otherwise 0
I	These bits are ignored
PCD	Page-level cache disable
PWT	Page-level write through
NXE	No-Execute Enable (prevents code being run)
A	Access bit (indicates if page has been read/written)
W	Page is writeable
D	Dirty bit (indicates whether page has been modified)

3.2 Translation Objective

Now that you are more familiar with x86 with PAE, we will implement a software page table walker that will translate a 32-bit virtual address to its physical address using x86 with PAE. The relevant code is in `hw6/pt-starter` and the relevant source/header files are listed below.

src/mmu.c This file will contain the software MMU that you will implement

src/ram.c Simulates physical memory, you should NOT need to modify this, but it may be interesting to look at

include/page.h Contains structures which represent the various page table entries.

include/constants.h Contains macros with relevant PAE constants. You should NOT need to modify any of these constants.

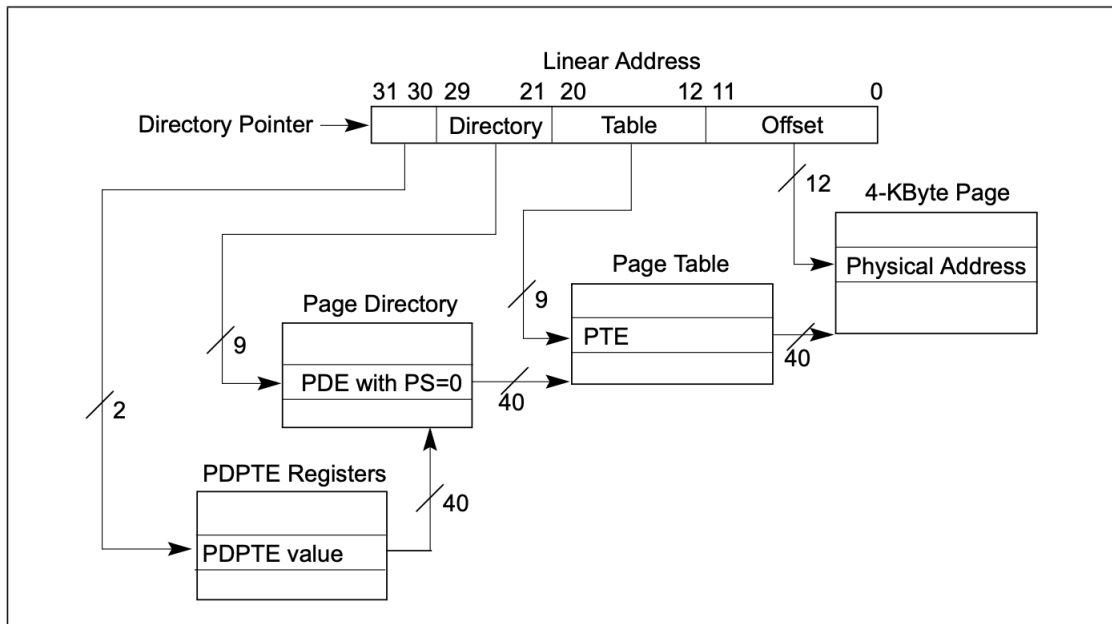


Figure 4-5. Linear-Address Translation to a 4-KByte Page using PAE Paging

Figure 1: Image from [IA32/64 Software Development Manual](#)³

Your objective to complete `virt_to_phys` in `mmu.c`. This function takes in a virtual address `vaddr`, the CR3 register `cr3`, and a pointer to a physical address `paddr`. This function should translate the given virtual address to a physical address using `cr3`. The translated physical address should then be stored in `paddr`. The return value of the function should indicate whether the translation succeeded without a page fault.

1. The return value should be 0 if no page fault occurs and the translation succeeds.
2. The return value should be non-zero if a page fault occurs.

For this homework, you can assume that the only page faults that can occur will be due to the present field not being set — this means the following won't be tested.

1. Access violations (i.e. a write is done on a page that is not marked as writable).
2. The `PG_SIZE` bit is never set — all pages will be 4 KB.
3. User mode process accessing a supervisor marked page.

Before implementing the function, we strongly recommend you to think about the answers to the following question to help guide you. You do not need to submit your answers to these.

1. How many bytes is a page?
2. How large is a page table entry?
3. How many page table entries fit on a page?

4. How many bytes is a page table?
5. How many bytes is the page directory table?
6. How many bytes is the page directory pointer table?

3.3 Debugging Tips

We have provided a file called `student.dat`. This file represents physical memory and assumes that the CR3 register points to `0x2000`. You may use this file to do a sanity check for your function. At virtual address `0x675f40a2`, the string `162 rulez` is stored. The virtual address should translate to the physical address `0xa2`.

You may run the following command:

```
./mmu student.dat 0x2000 0x675f40a2
```

The expected output should be the following:

```
Virtual address 0x675f40a2 translated to physical address 0xa2
String representation of data at virtual address 0x675f40a2: 162 rulez
```

In order to assist you with debugging, we provide the PFN given from the page-directory-pointer table, the page directory, and the page table given the virtual address `0x675f40a2`.

Hierarchy	PFN	PTE
PD Pointer	3	0x3001
Page Directory	1	0x1481
Page Table	0	0x8c1

We strongly recommend creating your own `student.dat` file to test other virtual addresses. You can call `ram_save` in `ram.c` to help you do this!

4 Getting Started With Pintos

Log in to your Vagrant Virtual Machine and run:

```
cd ~/code/personal
git pull staff master
cd hw6
cd hw6-pintos-starter
```

Then run `cd src/memory` and run `make` to build the code. Run `make check` to run the tests for this assignment.

We have also included the tests and changes to `lib/` in the `group0` repository. This is to make it easier to you to merge in your changes with those in your project at the end of your semester, if you wish to do so. You should *not* work on this assignment with your project group, and you should use your personal repository (`studentXXX`) for development, not your group repository (`groupXXX`).

4.1 Overview of Starter Code

The starter code implements the following subset of Project 1:

1. The one-line `do-nothing` hack needed to complete the “Getting Acquainted with Pintos” exercise.
2. The `write` system call for file descriptor 1 (`stdout`).
3. The ability to `open`, `read`, `write`, and `close` a *single* file per process.
4. Argument validation for the provided system calls using the page fault handler.

Notably, the provided starter code does **not** provide the ability to pass arguments on the command line, `exec` a new process, or open multiple files. We chose the smallest possible subset of Project 1 that we could identify to minimize the amount of Project 1 implementation code you would have to read.

Importantly, the provided starter code validates arguments to system calls using the following technique. Rather than validating every byte of user-provided arguments, the code merely checks that the arguments’ memory is below `PHYS_BASE`, and then reacts to page faults by terminating the running process. This is more efficient than validating the entire buffer up front, and makes implementing stack extension easier. **You should carefully read the provided code that validates system call arguments to understand how it works.**

4.2 Overview of Source Files

threads/palloc.c Page allocator.

threads/vaddr.h Helper functions for working with virtual addresses in Pintos.

userprog/process.c Loads ELF binaries, starts processes, and switches page tables on context switch.

You should be familiar with this code based on your experience in Project 1.

userprog/pagedir.c Manages the page tables. You probably won’t need to modify this code, but you may want to call some of these functions.

userprog/syscall.c This is basic system call handler that implements the system calls mentioned above.

lib/user/syscall.c Provides library functions for user programs to invoke system calls from a C program. Each function uses inline assembly code to prepare the syscall arguments and invoke the system call. We do expect you to understand the calling conventions used for syscalls.

lib/user/stdlib.c Provides library functions for the C standard library linked into Pintos user applications. In Part B, you will implement `malloc/calloc/realloc/free` in this file.

lib/syscall-nr.h This file defines the syscall numbers for each syscall.

userprog/exception.c Handle exceptions. You are expected to modify `page_fault`, the Pintos page fault handler, to complete this assignment. We already made some modifications to it in order to validate arguments to system calls.

You should read carefully through the functions in `threads/vaddr.h` and `userprog/pagedir.h`, as many of them will be useful to you in this assignment.

5 Pages and Page Tables in Pintos

In this section, we discuss how to allocate user pages and map them into the page tables of user processes.

5.1 Allocating Pages

Use the following functions (`#include "threads/palloc.h"`) to allocate and deallocate pages in the Pintos kernel:

```
void *palloc_get_page (enum palloc_flags);
void *palloc_get_multiple (enum palloc_flags, size_t page_cnt);
void palloc_free_page (void *);
void palloc_free_multiple (void *, size_t page_cnt);
```

The `palloc` functions use a bitmap to keep track of which pages are free and which pages are allocated. The `flags` argument is a bitmask of the following choices:

```
enum palloc_flags
{
    PAL_ASSERT = 001,      /* Panic on failure. */
    PAL_ZERO = 002,        /* Zero page contents. */
    PAL_USER = 004         /* User page. */
};
```

The set of pages is partitioned into two separate *pools*: a user pool and a kernel pool. Pages in the kernel pool are meant for use inside the kernel (e.g., the stack for kernel threads) and pages in the user pool are meant to be mapped into the virtual address spaces of user processes. The reason for this separation is to prevent failures in the kernel if user programs run out of memory. The `PAL_USER` flag tells the `palloc` function to allocate the requested pages from the user pool. Otherwise, it will allocate the requested pages from the kernel pool. **If you intend to map a page into the virtual address space of a process, you should allocate it from the user pool using `PAL_USER`.**

5.2 Mapping a Page into a Virtual Address Space

The `pagedir` member of `struct thread` is a pointer to the page table of the process. When switching to a process, Pintos uses the `pagedir_activate` function to start using that process' page table for address translation, by setting the page directory base register (`%cr3`). Note that the entirety of kernel memory is mapped into every process' virtual address space at addresses `PHYS_BASE` and above, so all process' page tables are interchangeable as long as you are accessing kernel memory. For this reason, we sometimes refer to addresses at `PHYS_BASE` and above as *kernel virtual addresses*.

The physical address corresponding to a kernel virtual address can be computed by subtracting `PHYS_BASE` from it. In principle this need not be true, but Pintos sets up its page tables such that it holds. The functions `vtov` and `ptov` (`#include "threads/vaddr.h"`) are helper functions that perform this conversion for you, but we do not expect you to have to use those functions in this assignment.

Given a page allocated in kernel virtual memory, one can map it into the virtual address space of a process by calling `pagedir_set_page`, which handles traversal of the two-level hierarchical page table and allocation of leaves as needed. Two arguments to `pagedir_set_page` are the virtual address in the user process at which the page should be mapped, and the kernel virtual address of the page to map. **Although the physical page table entry contains the physical page number, remember to pass in the kernel virtual address into `pagedir_set_page`.** Once you've mapped the page into the process' virtual address space, you don't have to worry about deallocating it; when the process exits, the `process_exit` function will call `pagedir_destroy`, which will call `palloc_free_page` on all pages mapped into the process' address space. If you would like the page to continue to be allocated even after the process dies, you should first remove it from the page table using `pagedir_clear_page`.

Note that pages that you allocate using `palloc_get_page` could have been previously allocated and then freed, and therefore could contain data from the previous time it was allocated. If it was mapped into a user process, it could contain data from the previous user program. To properly enforce protection,

you should initialize the contents of a page before mapping it into a user process. This is typically done by setting all bytes in the page to zero, except in special situations where the page should contain specific data (e.g., loading new code into a process or bringing back a page from disk). Under no circumstances should memory used by the kernel, or by other processes, become visible to a process because a physical page frame was reused.

5.3 Summary and Example

In summary, here is how to map a fresh page into the virtual address space of a process.

1. Allocate the page from the user pool using `pallocc_get_page` and passing the `PAL_USER` flag.
2. Zero out the page's contents, either by using `memset` or passing the `PAL_ZERO` flag when allocating the page (e.g., `pallocc_get_page(PAL_ZERO | PAL_USER)`).
3. Use `pagedir_set_page` to map the page into the virtual address space of a process.
4. The page will be deallocated when the process exits and `pagedir_destroy` is called. Alternatively, if you would like the page to be deallocated at some other time, you can remove it from the page table with `pagedir_clear_page` and then deallocate it later using `pallocc_free_page`.

A simple example of this is in the `setup_stack` and `install_page` functions in `process.c`. **We strongly recommend that you review these functions and understand this simple example before attempting this homework.**

6 Debugging

You can run GDB for this assignment as you have done for the Pintos projects. Remember to include all flags to the `pintos` script used to run the actual test, especially for tests that depend on auxiliary files. For example, to run GDB for the `pt-grow-stk-sc` test, you can run:

```
pintos -v --gdb --qemu --filesys-size=2 -p tests/memory/pt-grow-stk-sc -a
pt-grow-stk-sc -p ../../tests/memory/sample.txt -a sample.txt --swap-size=4
-- -q -f run pt-grow-stk-sc
```

Remember that you can change `--qemu` to `--bochs` if desired. Then, run `pintos-gdb kernel.o` in another terminal, and then run `debugpintos` in the GDB to connect to Pintos in the first terminal. Don't forget to run `loadusersymbols tests/memory/pt-grow-stk-sc` if you would like to be able to step through the test itself in GDB.

You can review this information in the Project 1 specification. You may also find it useful to review Section 3.1.9 "Debugging Page Faults" from the Project 1 specification.

7 Part A: Stack Growth

Implement stack growth. In Project 1, the stack was a single page at the top of the user virtual address space, and programs were limited to that much stack. Now, if the stack grows past its current size, allocate additional pages as necessary.

Allocate additional pages only if they "appear" to be stack accesses. Devise a heuristic that attempts to distinguish stack accesses from other accesses.

You may assume that a correct Pintos user program will never attempt to write to its stack below the stack pointer. The stack pointer will point at or below any active variables on the stack. In real operating systems, the system may interrupt a process at any time to deliver a "signal," which pushes

data on the stack (or in kernel mode, an interrupt).⁴ So, you should check if the faulting address is above the user value of `%esp` before extending the stack. However, there is an important edge case you need to handle. For example, the x86 `push` instruction checks access permissions before it adjusts the stack pointer, so it may cause a page fault 4 bytes *below* the stack pointer.⁶ Similarly, the `pusha` instruction pushes 32 bytes at once, so it may fault 32 bytes below the stack pointer. Your implementation of stack growth should handle these cases.

You will need to be able to obtain the current value of the user program's stack pointer. Within a system call or a page fault generated by a user program, you can retrieve it from the `esp` member of the `intr_frame` passed to the system call handler or page fault handler, respectively. The provided code validates invalid arguments to system calls lazily, by terminating the user process in the system call handler for page faults to user memory addresses that occur when handling a system call.

Note that the page fault resulting in stack growth may not necessarily be for the next consecutive page on the stack—the stack may grow by multiple pages at a time. This leads to multiple ways to implement stack growth. Here are two possibilities:

1. Only map the page containing the faulting address, so that the set of mapped pages for the stack is not necessarily contiguous in the virtual address space.
2. Map *all* pages from the current stack until the page containing the faulting address, so that the set of mapped pages is contiguous in virtual addresses.

The first option is conceptually simpler, but may be trickier to implement because **the first access to a stack page may occur while handling a system call**. For an example of this, see the `pt-grow-stk-sc` test. The reason this is tricky is that, if a page fault occurs while handling a system call, the `esp` member of the `intr_frame` passed to the system call handler does *not* contain the stack pointer of the user program (think about why). The value of the stack pointer in the user program at the time of the page fault is in the `intr_frame` passed to the system call handler. If your design requires you to consider stack extension due to a page fault while handling a system call, you will need to arrange a way to obtain the user-mode value of `%esp` in the faulting process in the page fault handler so that you can decide if the stack should be grown as a result of the page fault.

If there is no free page in the user pool to use to extend the stack, you should terminate the process with exit code -1. **Except for this out-of-memory condition, do not implement an absolute limit on stack size.** Modern operating systems do implement other limits, however; see the appendix (Resource Limits) for more information.

The first stack page need not be allocated lazily. You can allocate it at load time, as is done by the code provided to you in `process.c`, with no need to wait for it to be faulted in. However, future stack pages should only be allocated on demand. **It is not acceptable to simply allocate a large stack up front when the user program is loaded.**

8 Part B: Dynamic Memory Allocation

Implement the `malloc`, `calloc`, `realloc`, and `free` functions in `lib/user/stdlib.c`. Along the way, you'll need to extend Pintos with the `sbrk` system call, so that your dynamic memory allocator can request memory from the operating system. More details on this are given below.

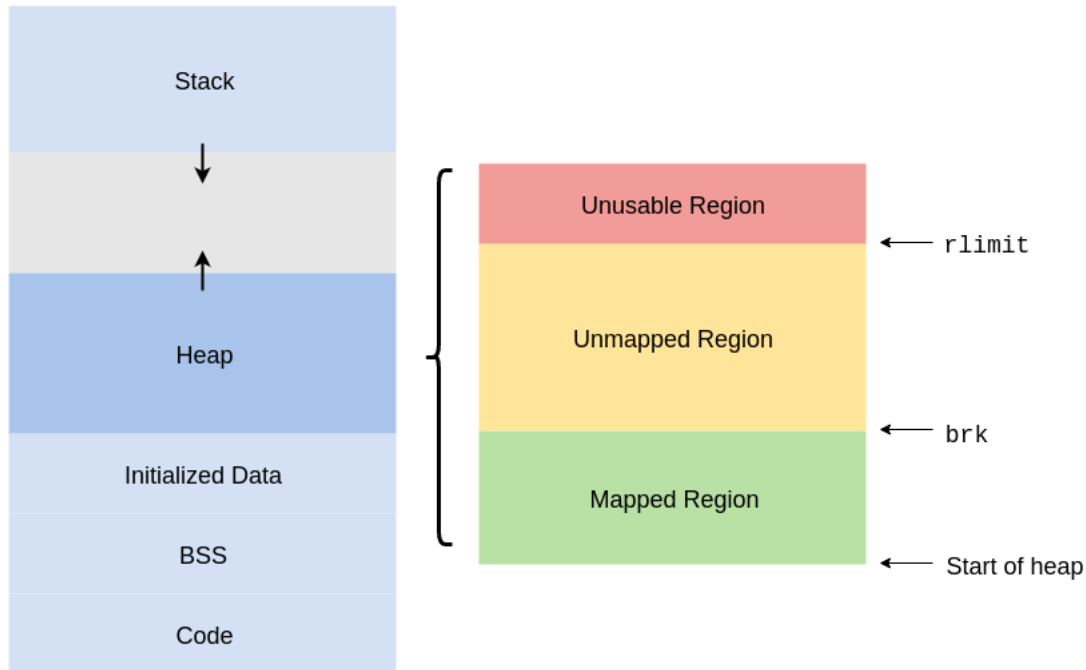
8.1 Process Memory

Each process has its own virtual address space. Parts of this address space are mapped to physical memory through address translation. In order to build a memory allocator, we need to understand

⁴This rule is common but not universal. One modern exception is the [x86-64 System V ABI](#)⁵, which designates 128 bytes below the stack pointer as a “red zone” that may not be modified by signal or interrupt handlers.

⁶If `%esp` were to be decremented by 4 bytes in the page fault handler, then the `push` instruction would not be restartable in a straightforward fashion.

how the heap in particular is structured. Here we describe the process memory layout, focusing on the structure of the heap, within a *Linux* process. You'll have to implement a simplified version of the heap in Pintos with the `sbrk` system call.



The heap is a space of memory, continuous in the virtual address space of a process, with three bounds:

- The bottom of the heap.
- The top of the heap, known as the break. The break can be changed using `brk` and `sbrk`. The break marks the end of the mapped memory space. Above the break lies virtual addresses which have not been mapped to physical addresses by the operating system. **For simplicity, you only need to implement `sbrk` in Pintos for this assignment. You do not need to bother with `brk`. You should implement `sbrk` as a new system call in Pintos.**
- The hard limit of the heap, which the break cannot surpass. See the appendix (Resource Limits) for more information. **For simplicity, you should *not* implement a hard limit on the heap size for this assignment.**

In this assignment, you'll be allocating blocks of memory in the mapped region and moving the break appropriately whenever you need to expand the mapped region.

8.2 Part B.1: Requesting Memory from the Operating System

Initially the mapped region of the heap will have a size of 0. To expand the mapped region, we have to manipulate the position of the break. The way to do this is `sbrk`, defined in `lib/user/syscall.c`:

```
void *sbrk(intptr_t increment);
```

You should implement `sbrk` as a system call that increments the position of the break by `increment` bytes and returns the address of the previous break (i.e., the beginning of newly mapped memory if

`increment` is positive). To get the current position of the break, pass in an `increment` of 0. Run `man 2 sbrk` on Linux for additional useful information.

To implement the `sbrk` system call in Pintos, you'll need to keep track of two variables for each process: the start of the heap, and the segment break (end of the heap). We describe how to use these variables below.

8.2.1 Determining the Start of the Heap

You should make sure that the process heap is located above (i.e., at a higher virtual address than) the process' code and other data loaded from the executable. You should determine at what address the heap should start when the program is loaded, after which it should remain fixed for the duration of the process. Look closely at the `load` function in `process.c`. For each loadable segment in the executable (remember segments from Homework 0?), `load` allocates pages in the virtual address space of the process, according to the read/write permissions and virtual address specified in the executable, and reads data from the executable file into those pages. Based on where the segments are loaded into memory, you should determine at what address the heap should start.

The ELF executable format guarantees that loadable segments will be listed in the executable in ascending order by virtual address space. **Thus, you should start the heap at a virtual address after the *last* loadable segment processed by the `load` function.** We recommend choosing a page-aligned address to start the heap.

To learn more about ELF, read `man 5 elf`.

8.2.2 Manipulating the Segment Break

The segment break should be first address past the end of the heap, so you can initialize the data segment break to the start of the heap after loading the process. You should move it only in response to `sbrk` system calls. **The user program should be able to write data starting at the start of the heap, up to and not including the segment break.** If the user program moves the segment break to increase the size of the heap, you should allocate pages and map them into the user's virtual address space as necessary. If the user program moves the segment break to decrease the size of the heap, you should deallocate pages that no longer contain part of the heap as necessary. You should only have to allocate or deallocate pages if the segment break crosses a page boundary.

Memory can only be mapped into a virtual address space in quanta of pages. Therefore, if the segment break is not page-aligned, it is acceptable for memory including after the system break, but before the next page boundary, to be accessible by a process. See the appendix (Unmapped Region and No Man's Land) for more information.

Allocating additional pages for a process' heap will fail if, for example, the user memory pool is exhausted and `palloc_get_page` fails. If `sbrk` fails, the net effect should be that `sbrk` returns `(void*) -1` and that the segment break and the process heap are unaffected. You might have to undo any operations you have done so far in this case.

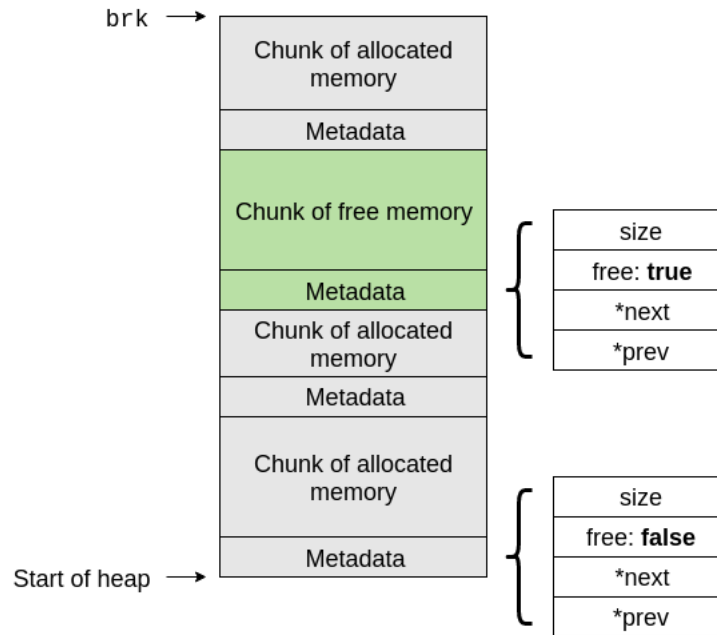
Finally, real operating systems may allocate pages for `sbrk` lazily, similar to stack growth. While `sbrk` moves the segment break, pages are not allocated until the user program actually tries to access data in its heap. **For simplicity, you are not required to implement this optimization.**

8.3 Heap Data Structure

A simple memory allocator can be implemented using a linked list data structure. The elements of the linked list will be the allocated blocks of memory on the heap. To structure our data, each allocated block of memory will be preceded by a header containing metadata.

For each block, we include the following metadata:

- **prev**, **next**: pointers to metadata describing the adjacent blocks
- **free**: a boolean describing whether or not this block is free
- **size**: the allocated size of the block of memory



You might also consider using a [zero-length array](#) to serve as a pointer to the memory block.

8.4 Part B.2: Implementing Dynamic Memory Allocation

For this portion of the homework, you may use your implementation of HW5.

8.4.1 Allocation

```
void *malloc(size_t size);
void *calloc(size_t nmemb, size_t size);
```

You may copy your implementation of `mm_malloc` from the previous homework. For more details on `mm_malloc` please refer to HW5.

Because `calloc`'s functionality is a strict superset of `malloc`'s, most of the allocation tests test `calloc`, despite being named `malloc`-*.

8.4.2 Deallocation

```
void free(void *ptr);
```

You may copy your implementation of `mm_free` from the previous homework. For more details on `mm_free` please refer to HW5.

8.4.3 Reallocation

```
void* realloc(void* ptr, size_t size);
```

You may copy your implementation of `mm_realloc` from the previous homework. For more details on `mm_realloc` please refer to HW5.

9 Stretch (Optional)

There are many things you can do to improve your allocator. (**Warning:** The autograder expects you to use the first-fit algorithm. If you want to implement extra features, do so only *after* you've received full credit on the autograder.)

- In Pintos, thread safety is not a concern because each process has only one thread. But, if you use your allocator in Linux, you can consider making it threadsafe. This doesn't mean putting a lock around all calls to `malloc`, but actually protecting the data structures such that multiple threads can make allocations at the same time. A good way to do this (not the only way) is to lock certain sizes of allocations, so two threads asking for 4kb would block, but two thread askings for 4kb and 32kb would not block.
- You can improve your allocation algorithm. First fit is one of the simplest to implement. Another more advanced strategy is the [buddy allocator](#).
- Implement `realloc` properly to extend the current allocation block if possible.

There are also other data structures you can consider using to keep track of blocks of memory:

- A list of free blocks for each allocation size. The advantage here is that `malloc` can be a constant time operation if a sufficiently large free block exists, as opposed to the linear time operation of iterating over a linked list in search of a large enough free block, without even knowing if there exists such a block.
- A free interval tree. These let you represent every memory allocation as an extent (`start`, `length`). The leaves of the tree correspond to regions of unused memory. If N bytes are requested, it should be possible to scan the interval tree for $(> N)$ -sized pieces of memory in $O(\log n)$ time, so long as it's properly balanced.

10 Submission Details

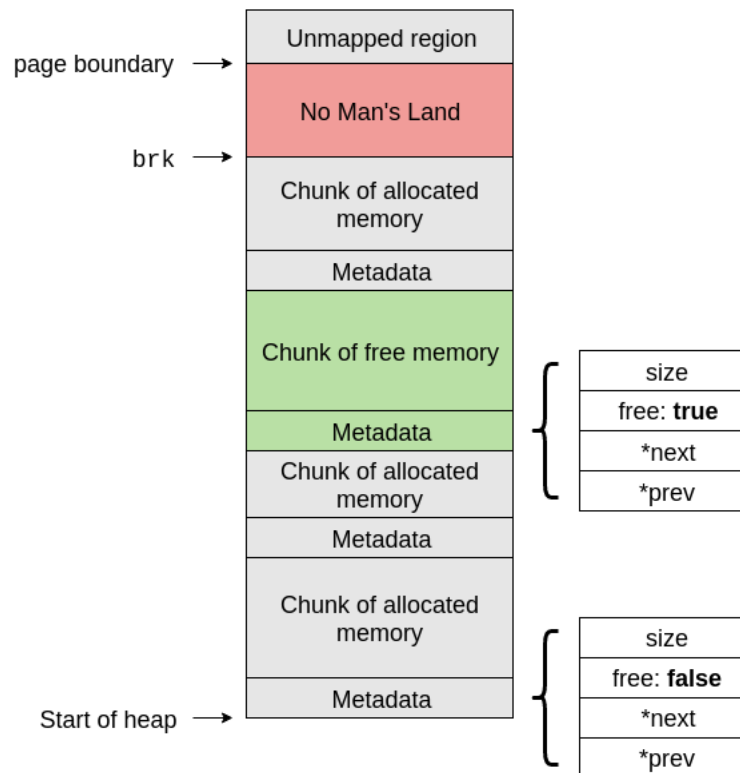
To submit and run to autograder, first commit your changes. Then do:

```
git push personal master
```

A Unmapped Region and No Man's Land

We saw earlier that the break marks the end of the mapped virtual address space. By this assumption, accessing addresses above the break should trigger an error (“bus error” or “segmentation fault”).

The virtual address space is mapped in quanta of pages (usually some multiple of 4096 bytes). When `sbrk` is called, the operating system will have to map more memory to the heap. To do that, it maps an entire page from physical memory to the mapped region of the heap. Now, it is possible that the break doesn't end up exactly on a page boundary. In this situation, what is the status of the memory between the break and the page boundary? It turns out that this memory is accessible, even though it is above the break and thus should be unmapped in theory. Bugs related to this issue are particularly insidious, because no error will occur if you read from or write to this “no man's land.”



B Resource Limits

In Homework 0, you briefly explored the `getrlimit` limit. In modern operating systems like Linux, processes have limits on their resource usage. For example, the maximum size of the stack and heap are governed by limits on the resources `RLIMIT_STACK` and `RLIMIT_DATA` (see `man 2 getrlimit`). Each of these resources has a *hard limit* and a *soft limit*. A process can raise its own soft limits; the soft limit exists to catch bugs (e.g., resource leaks) early by causing an error if a process uses more resources than expected. The hard limit can only be raised by the superuser (`root`), and exists to prevent resource abuse.

For this assignment, do not place an upper bound on the stack size and heap size. You do not need to implement resource limits.