

Figure 1: Circuit Diagram

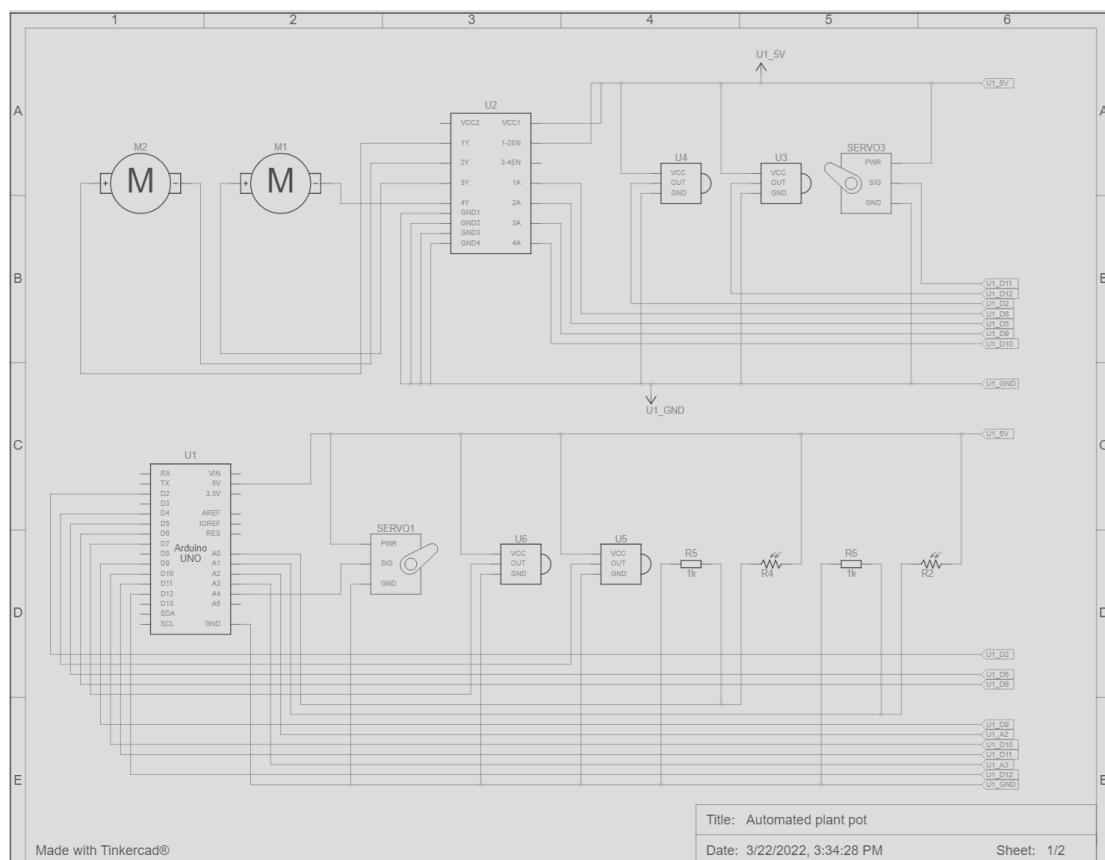


Figure 2: Schematic Part 1

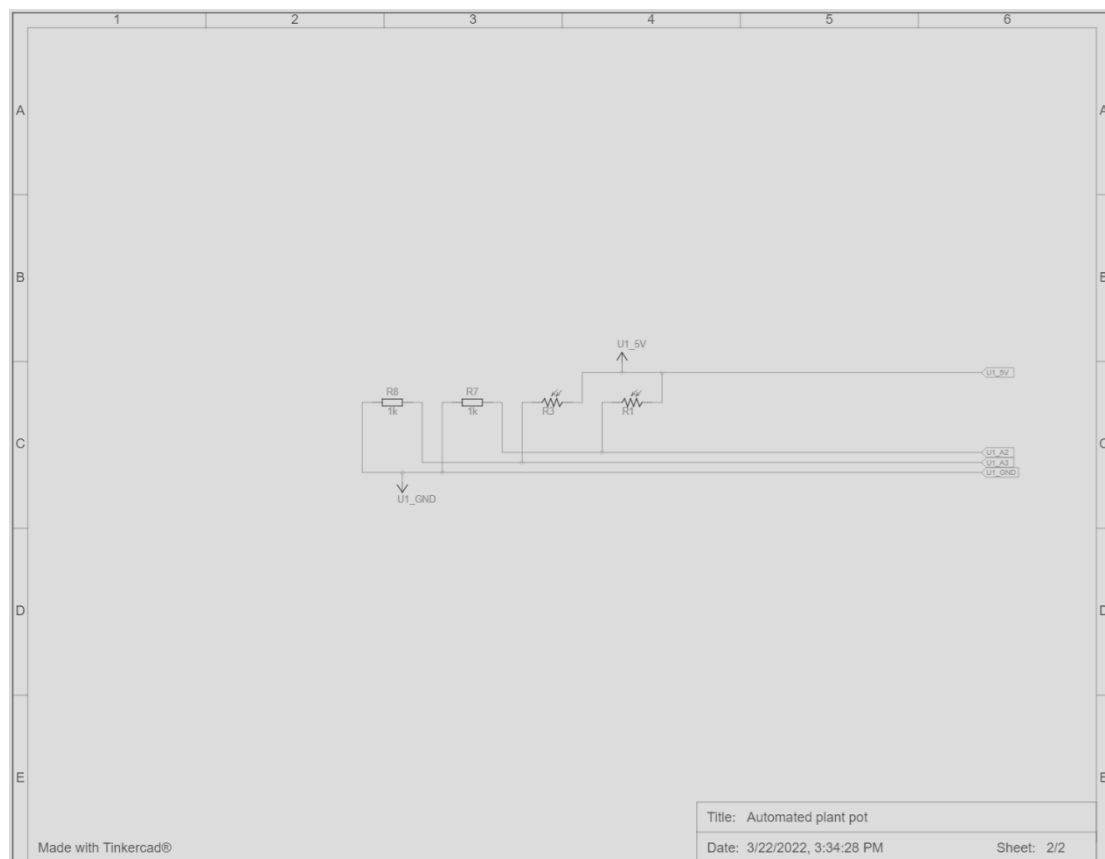


Figure 3: Schematic part 2