

Name: _____

- For a snooping cache implementation, complete the following matrix for the operations given. The spaces in the spreadsheet between operations are not necessarily an indication of the content that must be provided.

	P1			P2			Bus				Memory	
Step	State	Addr	Value	State	Addr	Value	Action	Proc	Addr	Value	Addr	Value
P1 Read A1												
P2 Read A3												
P1 Write 10 to A2												
P1 Write 20 to A3												

A1 not Equal A2

A1 and A2 share cache block

A3 does not share cache block with either

- ### Matrix for Directory Problem

- A1 not Equal A2
- A1 and A2 share cache block
- A3 does not share cache block with either

3. Why are directory protocols dominant in machines with large numbers of processors?

scale better

memory traffic is distributed across direct transfer

directory protocols work with distributed memory and an interconnect they can provide multiple connections and transfers to directories.