

# Wincom Tech CO., LTD.

## The LCD(M) Specialist

CONTACT ADDRESS: 6F, Block 105, Jing Di Industrial Park, Fu Qiang Rd. Fu Tian, Shenzhen City, China.

Tel: 0086-755-83308729 Fax: 0086-755-83308659 E-mail: <u>craig.jiang@wincomlcd.com</u>



PART NO.:	WG12864D-SFYLYHTC06
FOR MESSRS.:	

### **CONTENTS**

<i>NO</i> .	ITEM	PA GE
1.	COVER	1
2.	RECORD OF REVISION	2
3.	GENERAL SPECIFICATION	3
4.	MECHANICAL DATA	3
5.	ABSOLUTE MAXIMUM RATINGS	4
6.	ELECTRICAL CHARACTERISTICS	5
7.	OPTICAL CHARACTERISTICS	5
8.	OUTLINE DIMENSION	6~7
9.	BLOCK DIAGRAM	7
10	INTERFACE TIMING CHART	8-9
11	INSTRUCTION CODE	10

ACCEPTED BY	: 	PROPOSED BY:	
ACCEPTED DV		DDODOCED DV.	

# **RECORD OF REVISION**

DATE	PAGE	SUMMARY

Tel:0086-755-83308729 Fax:0086-755-83308659 WG12864D-SFYLYHTC06 PAGE: 2/10 Wincom Tech. CO., LTD.

### 3. General specifications

3.1 General specifications

PLEASE REFER TO:

"CUSTOMER ACCEPTANCE STANDARD SPECIFICATIONS (MS-10-10000)".

3.2 Quality Assurance and Warranty

PLEASE REFER TO:

"QUALITY ASSURANCE MANUL (MS-10-10001)".

3.3 This individual specification is prior to general specifications

#### 4. Mechanical data

• Display format: 128 x 64 DOTS

• LCD type: STN Positive Yellow-Green

• Backlight color: Yellow-Green

• Viewing angle: 6:00

• Data transfer: 8Bit Parallel

• LCD controller: S6B0108

• Module size: 75x53 mm

• View area: 60 x 32.4 mm

• Dot size: 0.4 x 0.4 mm

• Dot pitch: 0.43 x 0.43mm

•Driving method: 1/64duty, 1/9 bias

### 5. Absolute maximum ratings

### 5.1 Electrical absolute maximum ratings

I T E M	SYMBOL	MIN.	MAX.	UNIT	COMMENT
POWER SUPPLY FOR LOGIC	V <sub>DD</sub> -V <sub>SS</sub>	-0.3	5.5	V	
INPUT VOLTAGE	$V_{\rm I}$	Vss	$V_{\mathrm{DD}}$	V	
STATIC ELECTRICITY				V	
POWER SUPPLY FOR	Vs	3.8	4.2	V <sub>rms</sub>	
BACKLIGHT	$\mathbf{f}_{FL}$			KHz	
STARTING VOLTAGE FOR				$V_{\text{rms}}$	Ta = 25 ℃
BACKLIGHT				$V_{\text{rms}}$	Ta = 25 ℃
POWER SUPPLY FOR LCD	Vlcd		10.2	V	

#### 5.2 Environmental absolute maximum ratings

ITEM	OPER.	ATING	STOR	AGE	COMMENT
	MIN.	MAX.	MIN.	MAX.	COMMENT
AMBIENT TEMPERATURE	-20℃	70℃	-30℃	80℃	
HUMIDITY	NOTE (2)		NOTE (2)		NO CONDENSATION
VIBRATION NOTE (3)		0.5G		2G	10~300Hz XYZ DIRECTIONS 1 Hr EACH
SHOCK NOTE (3)		3G		5G	10 msec XYZ DIRECTIONS 1 TIME EACH
CORROSIVE GAS	NOT ACCEPTA	ABLE	NOT ACCEPTA	ABLE	

NOTE (2): Ta  $\leq 70^{\circ}\text{C}$ : 75% RH MAX.

 ${\tt Ta} > 70\,{\tt ^{\circ}C}$  : ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF

75% RH AT 70℃.

NOTE (3):  $1G = 9.8 \text{ m/s}^2$ 

#### 6. Electrical characteristics

 $Ta = 25^{\circ}C$   $V_{DD} = 5.0V$ 

I T E M	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power supply voltage for circuit	V <sub>DD</sub> -V <sub>SS</sub>		4.75	5.0	5.25	V
Power supply voltage for LCD drive	Vdd-Vee		8.5		V	
Data input valtaga	Vih	H LEVEL	2.4		$V_{\text{DD}}$	V
Data input voltage	VIL	L LEVEL	-0.3		0.4	V
LCD display duty ratio	DUTY			1/64		
	Ifp	I mseo plus 10% Dutg cyele				mA
LED BACKLIGHT		Operating voltage	3.8	4.0	4.2	V
		Forward current		60		mA
LED Lifetime		V <sub>FL</sub> = 4.0Vrms f <sub>FL</sub> = KHz		100,000		Hr

NOTE: LED backlight: Due to the LED backlight working current is XXX Max, and LED chips Vop may be different, Wincom will adjust the backlight resistor according to the LED chips Vop, to meet the brightness maximium.

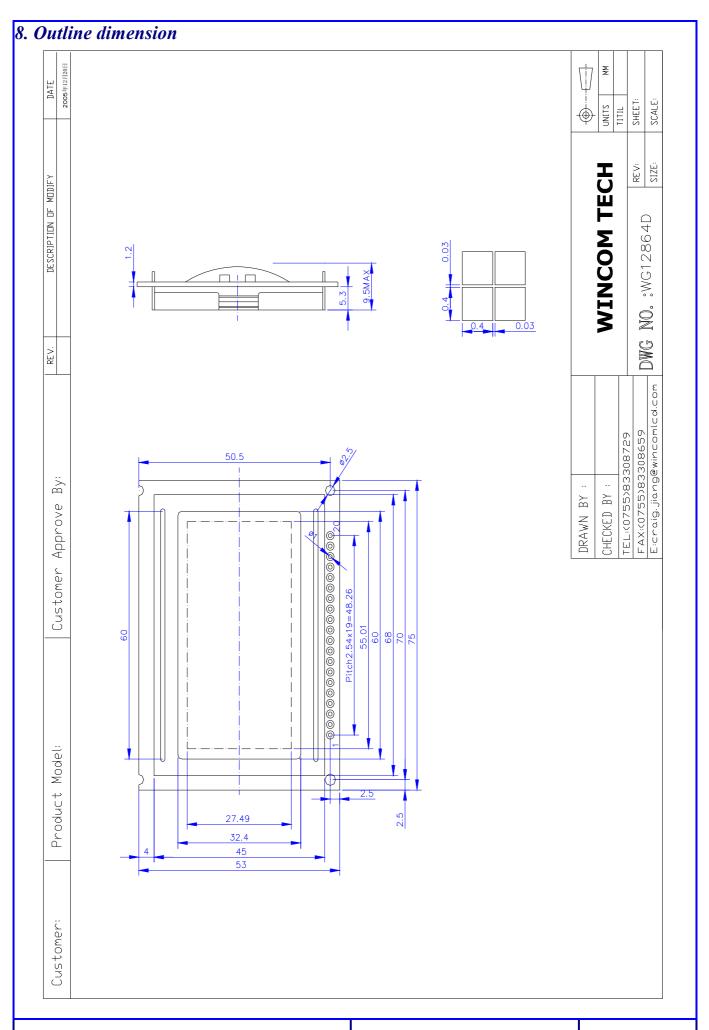
### 7. Optical characteristics

 $Ta = 25^{\circ}C$   $V_{LCD} = 8.5V$ 

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE
Viewing angle	Ф2-Ф1	K ≥ 2.0	-35		20	deg.	1
Contrast ratio	K	$ \Phi = 10^{\circ}  \theta = 0^{\circ} $	4. 0				1
Response time	tr (rise)	$ \Phi = 10^{\circ}  \theta = 0^{\circ} $			250	ms	1
(at 25℃)	tf (fall)	$ \Phi = 10^{\circ}  \theta = 0^{\circ} $			250	ms	1
The brightness of backlighting source	В	VFL= 4.0Vrms fFL= KHZ		60		$\mathrm{cd/m^2}$	2

NOTE (1): SEE CUSTOMER ACCEPTANCE STANDARD SPECIFICATION FOR DEFINITION OF OPTICAL CHARACTERISTICS

NOTE (2): UNDER NORMAL TEMPERATURE AND HUMIDITY IN A DARK ROOM

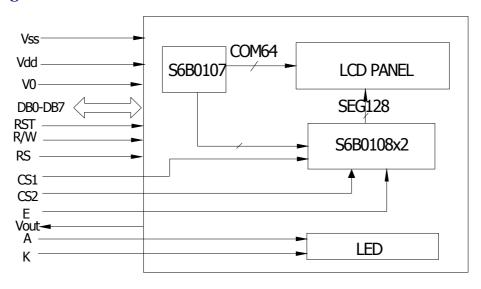


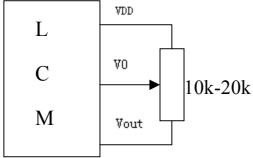
#### Interface *8.2*

### Pin Assignment

PIN NO.	Symbol	Leve	Function
1	VDD	5.0V	Power supply for logic circuit
2	Vss	0V	Ground
3	V0		Contrast Adjust
4	DB0	H/L	Data Bus Line
5	DB1	H/L	Data Bus Line
6	DB2	H/L	Data Bus Line
7	DB3	H/L	Data Bus Line
8	DB4	H/L	Data Bus Line
9	DB5	H/L	Data Bus Line
10	DB6	H/L	Data Bus Line
11	DB7	H/L	Data Bus Line
12	CS1	H/L	Chip Selection Signal 1 (Active " Low")
13	CS2	H/L	Chip Selection Signal 2 (Active " Low")
14	RST	H/L	Reset (Active " Low")
15	RW	H/L	Data read / write
16	RS	H/L	Register select signal
17	Е	H/L	Enable signal
18	Vout	-10V	Output Voltage for LCD Driving
19	A	(+5.0v)	Power supply for BL LED(+)
20	K	(-)	Power supply for BL LED(-)

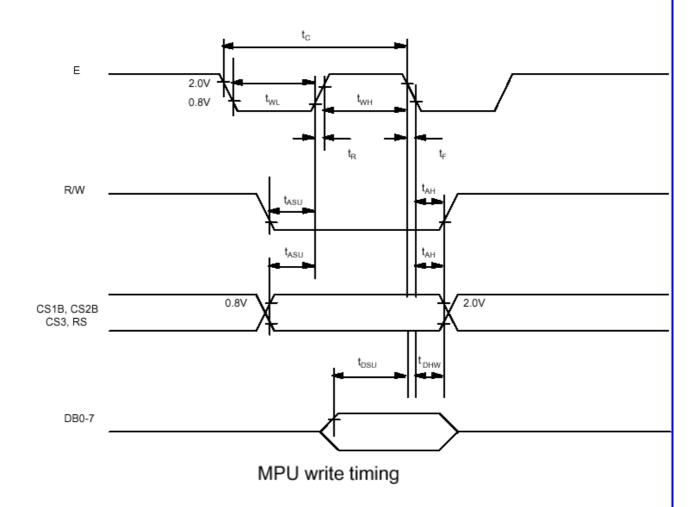
### 9 Block diagram

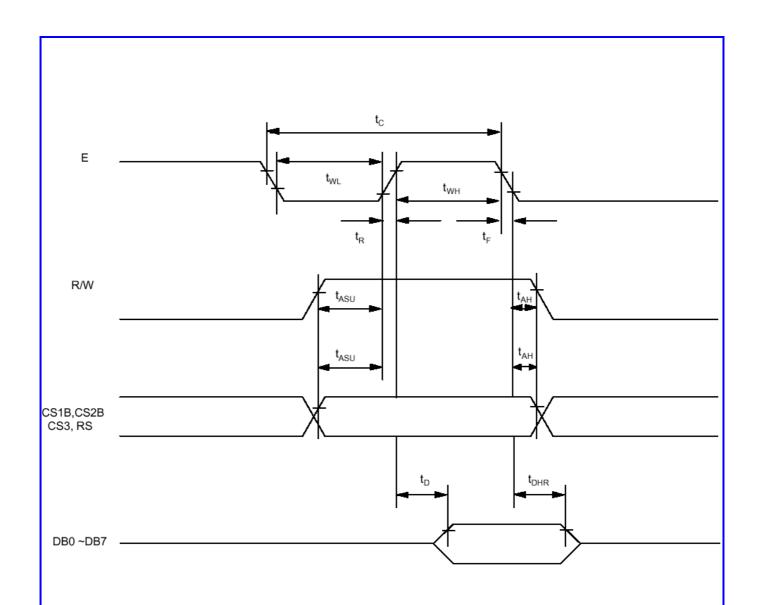




# 10.Interface Timing Chart

Characteristic	Symbol	Min	Тур	Max	Unit
E Cycle	tc	1000	-	-	ns
E High Level Width	t <sub>wH</sub>	450	-	-	ns
E Low Level Width	t <sub>WL</sub>	450	-	-	ns
E Rise Time	t <sub>R</sub>	-	-	25	ns
E Fall Time	t <sub>F</sub>	-	-	25	ns
Address Set-Up Time	t <sub>ASU</sub>	140	-	-	ns
Address Hold Time	tah	10	-	-	ns
Data Set-Up Time	t <sub>DSU</sub>	200	-	-	ns
Data Delay Time	t <sub>D</sub>	-	-	320	ns
Data Hold Time (Write)	t <sub>DHW</sub>	10	-	-	ns
Data Hold Time (Read)	t <sub>DHR</sub>	20	-	-	ns





# 11.Instruction Code

Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Display ON/OFF	٦	L	L	L	I	I	I	н	I	L/H	Controls the display on or off. Internal status and display RAM data is not affected. L:OFF, H:ON
Set Address (Y address)	П	Г		Н		Υa	ddress (	(0~63)			Sets the Y address in the Y address counter.
Set Page ( X address)	L	L	I	L	Η	Н	Н		Page (0~7)		Sets the X address at the X address register.
Display Start Line (Z address)		Г	H	H		I		start line 63)	9		Indicates the display data RAM displayed at the top of the screen.
Status Read	L	H	B U S Y	L	0 N / 0 F F	R E S E T	L	L	L	L	Read status. BUSY L: Ready H: In operation ON/OFF L: Display ON H: Display OFF RESET L: Normal H: Reset
Write Display Data	Н	L		Write Data						Writes data (DB0:7) into display data RAM. After writing instruction, Y address is increased by 1 automatically.	
Read Display Data	Ħ	H				Read D	ata				Reads data (DB0:7) from display data RAM to the data bus.