TOSHIBA PHOTOCOUPLER GaAs IRED & PHOTO-TRANSISTOR

# TLP281, TLP281-4

### PROGRAMMABLE CONTROLLERS AC/DC-INPUT MODULE PC CARD MODEM(PCMCIA)

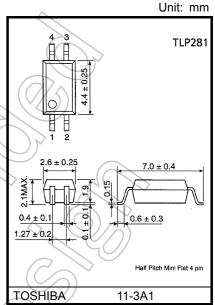
TLP281 and TLP281-4 is a very small and thin coupler, suitable for surface mount assembly in applications such as PCMCIA Fax modem, programmable controllers.

TLP281 and TLP281-4 consist of photo transistor, optically coupled to a gallium arsenide infrared emitting diode.

Collector-Emitter Voltage : 80 V (min)
 Current Transfer Ratio : 50% (min)
 Rank GB : 100% (min)
 Isolation Voltage : 2500 Vrms (min)

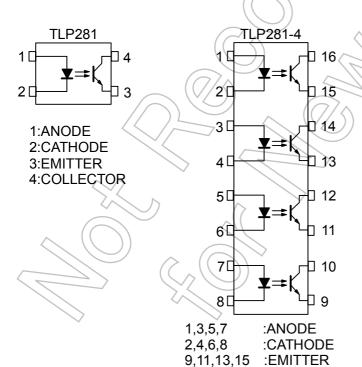
UL Recognized : UL1577, File No. E67349
 BSI Approved : BS EN 60065: 2002,

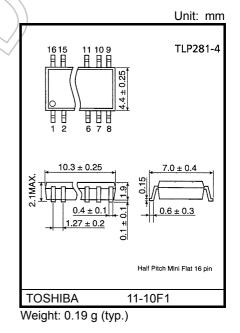
: BS EN 60950-1: 2002 Certificate No. 8143, 8144



Weight: 0.05 g (typ.)

### Pin Configuration (top view)





Start of commercial production 1996/03

10,12,14,16 :COLLECTOR

#### **Current Transfer Ratio**

TYPE	Classification(*1)	Current Transfer Ration (%) (I <sub>C</sub> / I <sub>F</sub> )  I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V, Ta = 25°C		Marking of Classification
		Min	Max	
	Blank	50	600	Blank,Y <sup>■</sup> ,YE,G,G <sup>■</sup> ,GR,B,BL,GB
	Rank Y	50	150	YE
	Rank GR	100	300	GR
	Rank BL	200	600	BL
TLP281	Rank GB	100	600	GB
	Rank YH	75	150	Y. (V)
	Rank GRL	100	200	G
	Rank GRH	150	300	G <sup>®</sup>
	Rank BLL	200	400	В
TLP281-4	Blank	50	600	Blank, GB
1LF 201-4	Rank GB	100	600	GB 7

<sup>\*1:</sup> Ex. rank GB: TLP281 (GB)

(Note): Application type name for certification test, please use standard product type name, i.e. TLP281 (GB): TLP281, TLP281–4 (GB): TLP281–4

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#### Absolute Maximum Ratings (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RAT	UNIT	
		STIVIBUL	TLP281	TLP281-4	UNIT
	Forward Current	lF	50		mA
	Forward Current Derating	ΔI <sub>F</sub> /°C	−0.7 (Ta≥53°C)	-0.5 (Ta≥25°C)	mA /°C
LED	Pulse Forward Current (Note 1)	I <sub>FP</sub>	1		<\A
	Reverse Voltage	V <sub>R</sub>	Ę	5	V
	Junction Temperature	Tj	12	25	(°C
	Collector-Emitter Voltage	V <sub>CEO</sub>	80		y
	Emitter-Collector Voltage	V <sub>ECO</sub>	7		// v))
S	Collector Current	IC	50		mA
DETECT	Collector Power Dissipation (1 Circuit)	P <sub>C</sub>	150	100	mW
	Collector Power Dissipation Derating(Ta≥25°C) (1 Circuit)	ΔP <sub>C</sub> /°C	-1.5	-1.0	mW /°C
	Junction Temperature	Tj	125		°C
Оре	erating Temperature Range	T <sub>opr</sub>	-55 to 100		○ °C (
Storage Temperature Range		T <sub>stg</sub>	-55 to 125		,c
Lead Soldering Temperature		T <sub>sol</sub>	260	260 (10s)	
Total Package Power Dissipation (1 Circuit)		PT	200	170	mW)
Total Package Power Dissipation Derating (Ta≥25°C) (1 Circuit)		ΔP <sub>T</sub> /°C	-2.0		mW /°C
Isola	ation Voltage (Note 2)	BV <sub>S</sub>	2500(AC,1mi	n,R.H.≤60%)	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) Pulse width ≤ 100µs, frequency 100Hz

(Note 2) AC, 1 minute, R.H.≤60%,Device considered a two terminal device : LED side pins shorted together and DETECTOR side pins shorted together.

## Individual Electrical Characteristics (Ta = 25°C)

	CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
	Forward Voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
LED	Reverse Current	IR	V <sub>R</sub> = 5 V	_	_	10	μΑ
	Capacitance	$c_{I}$	V = 0, f = 1 MHz	_	30	_	pF
	Collector-Emitter Breakdown Voltage	V <sub>(BR)</sub> CEO	I <sub>C</sub> = 0.5 mA	80	_	_	٧
	Emitter-Collector Breakdown Voltage V <sub>(BR) ECO</sub>		I <sub>E</sub> = 0.1 mA	7	_	_	V
SR		ICEO	V <sub>CE</sub> = 48 V	_	0.01	0.1	
DETECTOR	Collector Dark Current		Ambient Light Below (100 &x) (Note 4)	_	2	10	μΑ
DEI	(Note 3)		V <sub>CE</sub> = 48 V, Ta = 85°C	_	2	50	
			Ambient Light Below (100 &x) (Note 4)	_	4	50	μA
	Capacitance (Collector to Emitter)	C <sub>CE</sub>	V = 0, f = 1 MHz	_	10	_	pF

(Note 3) Because of the construction,leak current might be increased by ambient light.

Please use photocoupler with less ambient light.

(Note 4) Irradiation to marking side using standard light bulb.

## **Coupled Electrical Characteristics (Ta = 25°C)**

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Current Transfer Ratio	lo / le	I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V	50		600	%
Current Transier Nauo	I <sub>C</sub> / I <sub>F</sub>	Rank GB	100	-	600	70
Saturated CTR	lo/le/ o	IF = 1 mA, VCE = 0.4 V	/_	60	1	%
Saluraled CTK	I <sub>C</sub> / I <sub>F (sat)</sub>	Rank GB	30	1	-	/0
		I <sub>C</sub> = 2.4 mA, I <sub>F</sub> = 8 mA	1	) /_	0.4	
Collector-Emitter Saturation Voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> = 0.2 mA, I <sub>F</sub> = 1 mA	) <sub>K</sub>	0.2	_	V
Catalansii Vallago		Rank GB	<u> </u>	1	0.4	
Off-State Collector Current	I <sub>C (off)</sub>	V <sub>F</sub> = 0.7 V, V <sub>CE</sub> = 48 V	_	_	10	μΑ

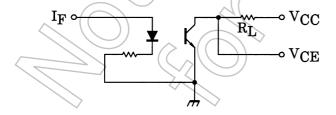
### **Isolation Characteristics (Ta = 25°C)**

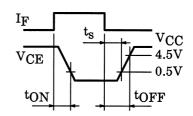
				7 7 1	_ ` `	
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Capacitance (Input to Output)	CS	V <sub>S</sub> = 0 V, f = 1 MHz	7-6	0.8	) —	pF
Isolation Resistance	$R_S$	V <sub>S</sub> = 500 V, R.H. ≤ 60%	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
		AC, 1 minute	2500	\ -	_	Vrms
Isolation Voltage	BVS	AC, 1 second, in oil		5000	_	VIIIIS
		DC, 1 minute, in oil	<u> </u>	5000		Vdc

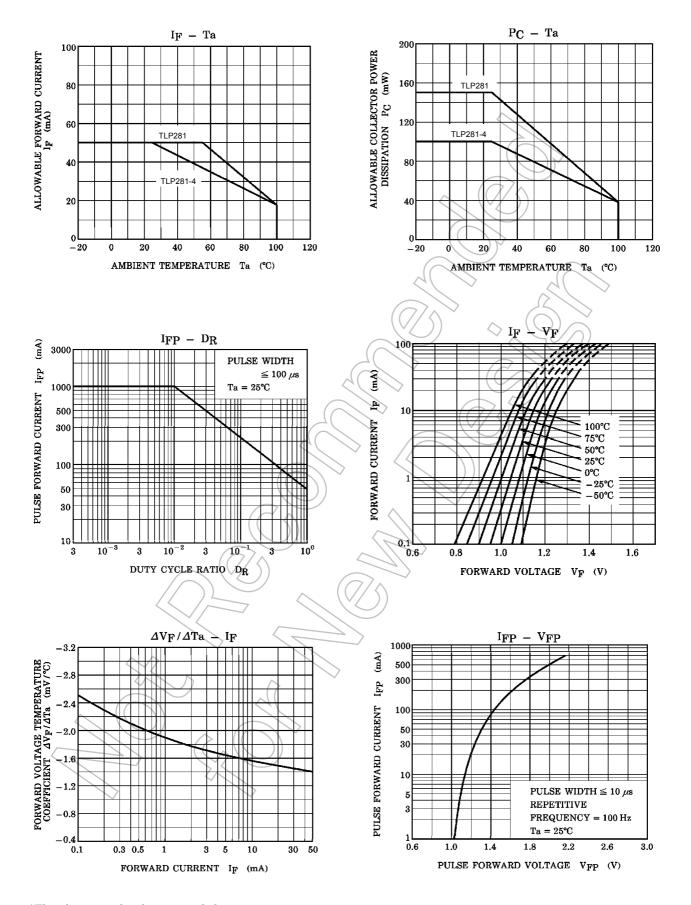
### Switching Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Rise Time	( t <sub>r</sub> )		_	2	_	
Fall Time	) If	$V_{CC} = 10 \text{ V}, I_{C} = 2 \text{ mA}$ $R_{L} = 100\Omega$	_	3	_	
Turn-On Time	( t <sub>on</sub>	$R_L = 100\Omega$	_	3	_	μs
Turn-Off Time	t <sub>off</sub>	(7/4)	_	3	_	
Turn-On Time	ton		_	2	_	
Storage Time	ts	$R_L$ = 1.9 kΩ (Fig.1) V <sub>CC</sub> = 5 V, I <sub>F</sub> = 16 mA	_	25	_	μs
Turn-Off Time	toff		_	40	_	

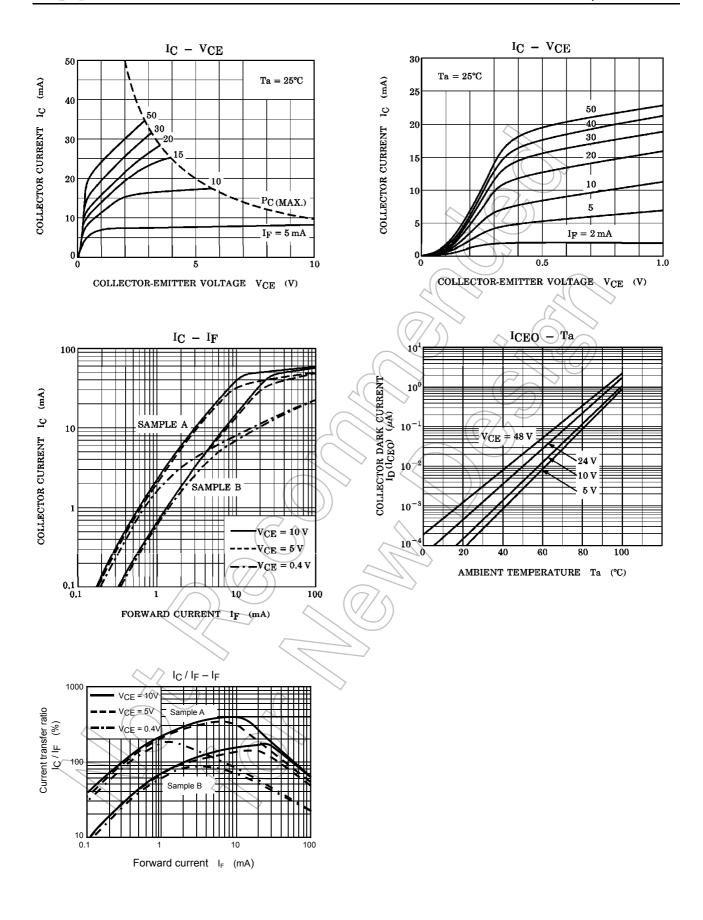
(Fig.1) SWITCHING TIME TEST CIRCUIT



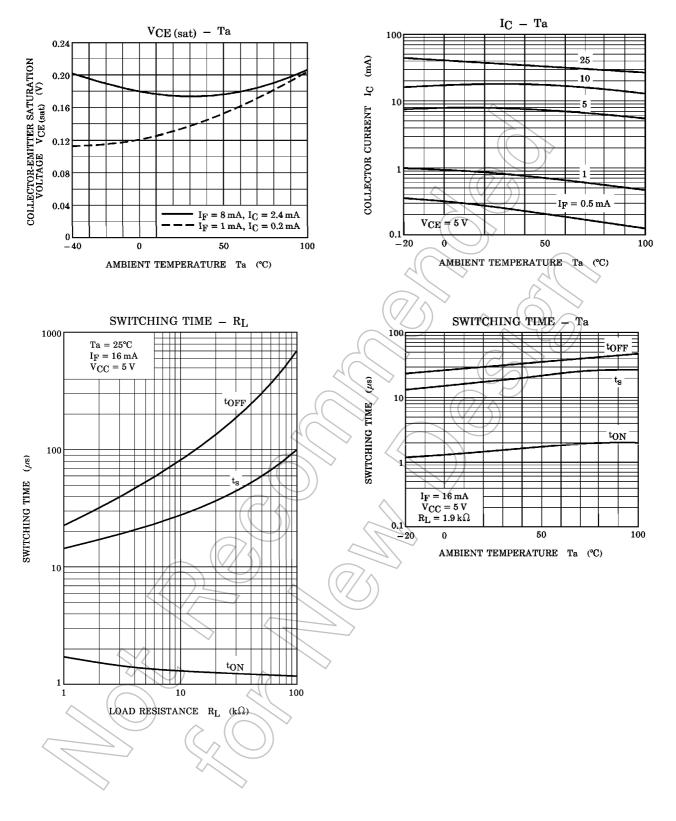




<sup>\*</sup>The above graphs show typical characteristic.



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