# 数电实验7

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#### 一、实验目的

将四位二进制数对应的 8421BCD 在七段数码管上显示出来.

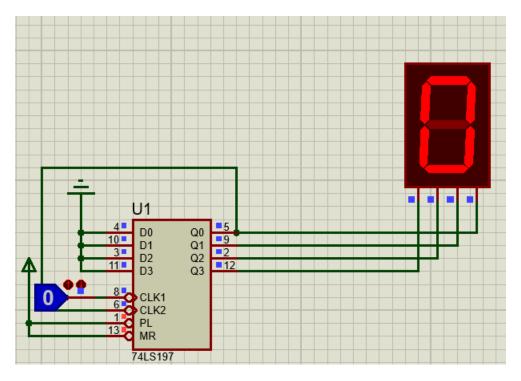
#### 二、实验要求

设计 8421BCD 码的十六进制数到七段码的转换电路

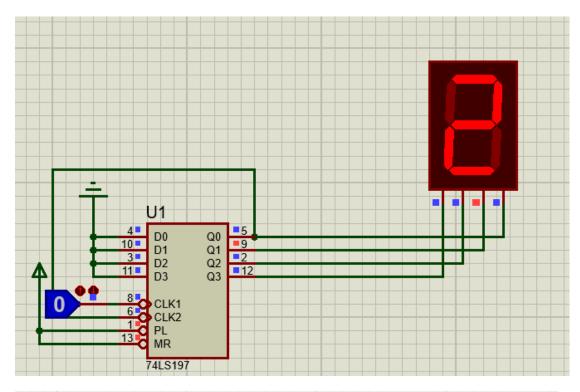
画出a,b、c、d、e、f、g、h的电路。

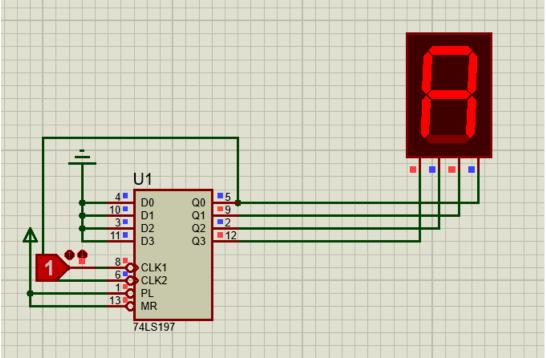
### 三、实验内容

### 8421bcd 码在七段数码管上显示:



注:实验内容的条理性和美观性将影响实验报告的分数。对实验结果是否拍照不作要求,重点在于实验内容的描述和关键代码的解释。



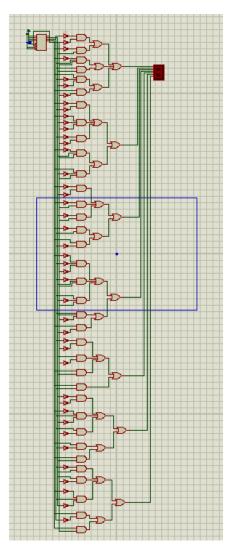


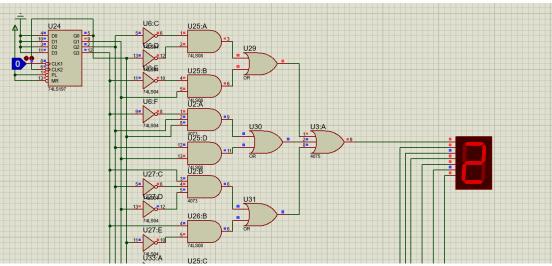
设计 8421BCD 码的十六进制数到七段码的转换电路:

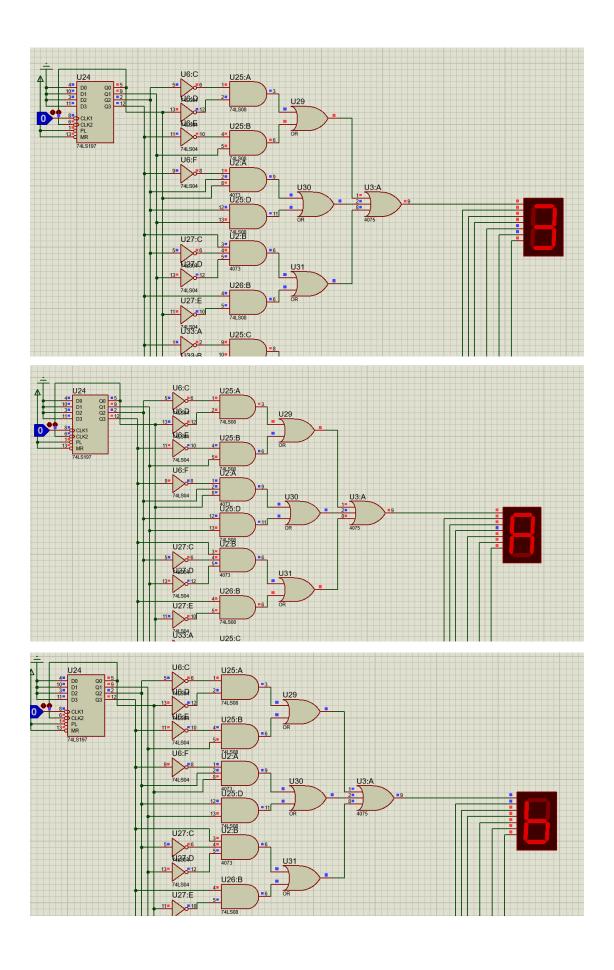
# 真值表:

| 十六进制 [ | D | С | В | Α | a | b | С | d | е | f | g |
|--------|---|---|---|---|---|---|---|---|---|---|---|
| 0      | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 |
| 1      | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 0 | 0 | 0 |
| 2      | 0 | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 1 | 0 | 1 |
| 3      | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 1 |
| 4      | 0 | 1 | 0 | 0 | 0 | 1 | 1 | 0 | 0 | 1 | 1 |
| 5      | 0 | 1 | 0 | 1 | 1 | 0 | 1 | 1 | 0 | 1 | 1 |
| 6      | 0 | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 |
| 7      | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |
| 8      | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| 9      | 1 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 1 |
| Α      | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 1 |
| b      | 1 | 0 | 1 | 1 | 0 | 0 | 1 | 1 | 1 | 1 | 1 |
| С      | 1 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 1 | 1 | 0 |
| d      | 1 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 0 | 1 |
| E      | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 1 | 1 | 1 | 1 |
| F      | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 1 | 1 | 1 |

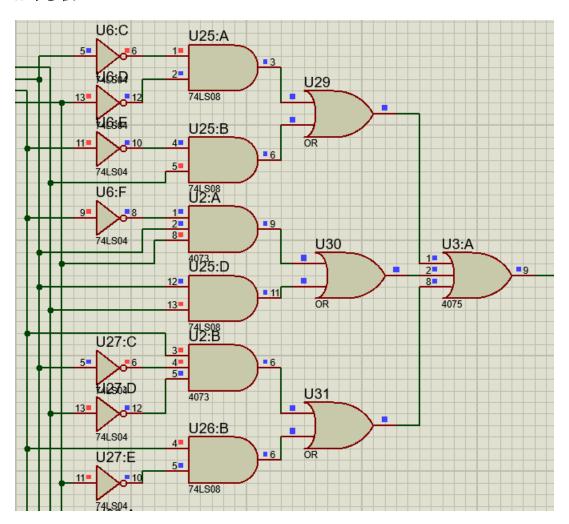
## 逻辑表达式:



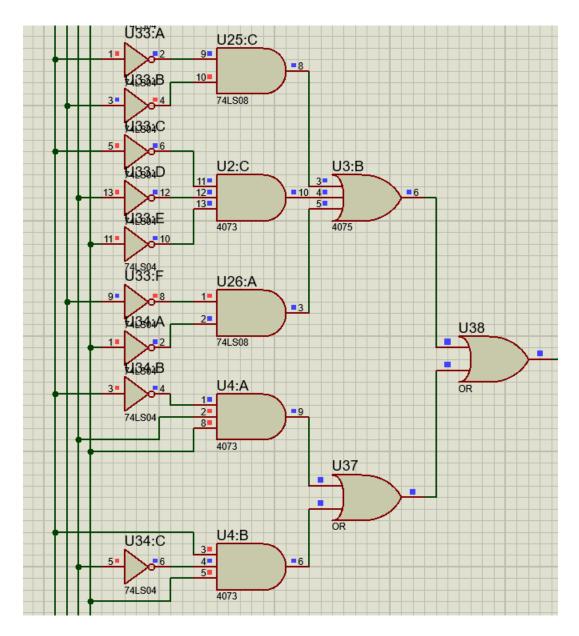




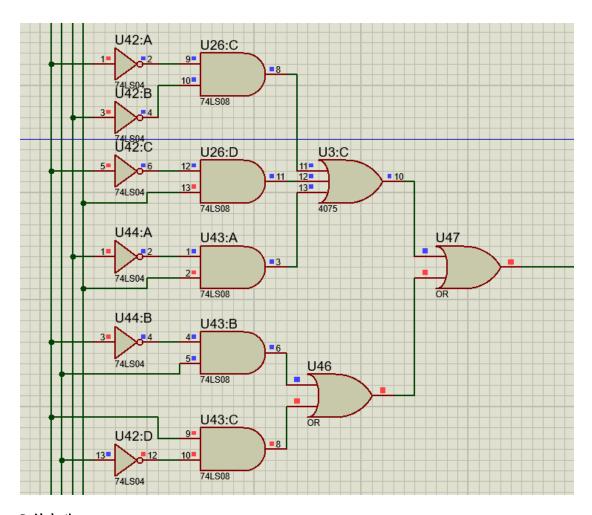
# A 的电路:



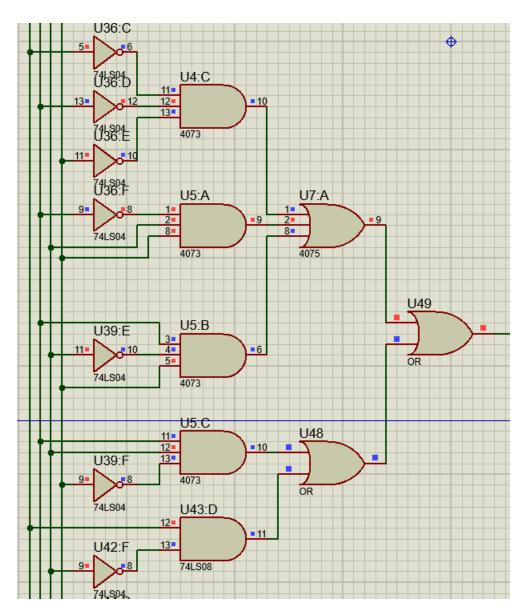
B 的电路:



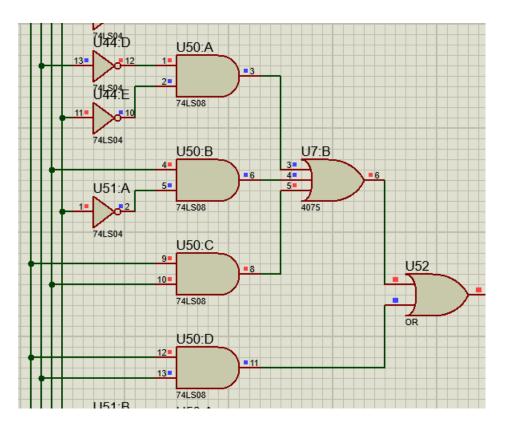
C 的电路:



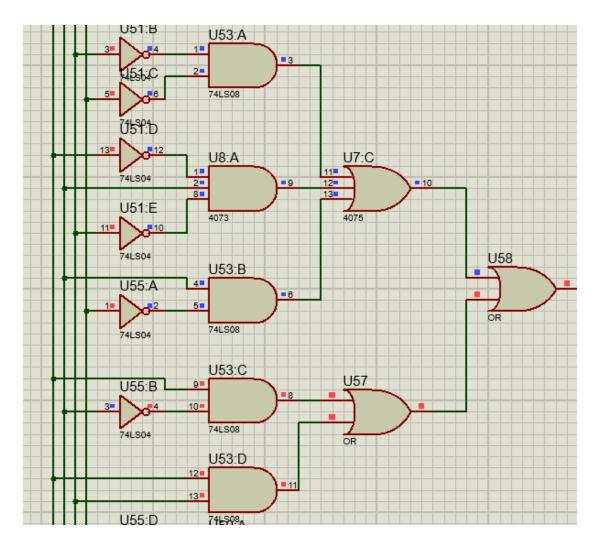
D 的电路:



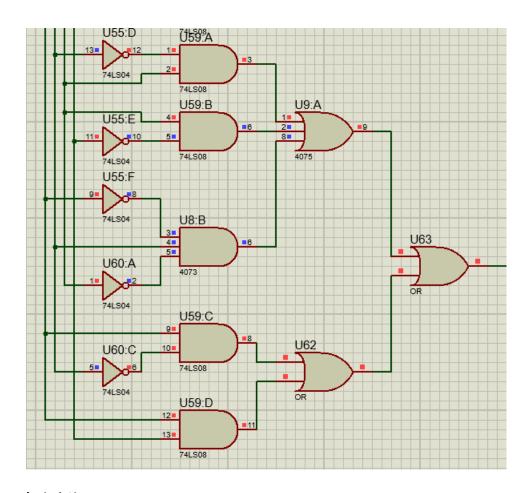
E 的电路:



F的电路:



G 的电路:



## 四、实验总结

通过实验懂得了如何通过真值表实现 4 位二级制 bcd 码的十六进制数转换位七段码并且在七段数码管显示。