



AROR UNIVERSITY  
OF ART, ARCHITECTURE,  
DESIGN & HERITAGE,  
SUKKUR, SINDH

## **Faculty of Artificial Intelligence & Multimedia Gaming**

BS – Multimedia Gaming

Digital Logic Design Lab

### **Lab # 04:**

Engr. Muhammad Younis

#### **Submission Profile**

Name:

Submission date (dd/mm/yy):

Marks obtained:

Comments:

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Instructor

## Task 01: Sum of Product (SOP)

A. Construct a digital circuits of the given Boolean expression in Multisim, which is presented in a non-standard Sum of Product (SOP) format.

1.  $AB+AC'+BC$
2.  $AB'+B'C+AC$
3.  $A'B+AC'$
4.  $B'C+A'C'+AB'C$

**Add snapshot of circuits of above expressions**

1	
2	
3	

4	
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- B. Convert the non-standard SOP expression from A into the standard SOP format and then build its digital circuit representation as a standard SOP Boolean expression in Multisim.

**Note: use the pen paper to convert the non-standard SOP to Standard SOP, add the snapshot of the solutions below**

1	
2	

3	
4	

**Add snapshot of circuits of above standard SOP expressions**

1	
2	
3	
4	

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C. Fill the following truth table for output of the both non-standard SOP and standard SOP circuits.

**1.  $AB+AC'+BC$**

A	B	C	Output of Non-Standard SOP	Output Standard SOP
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

**2.  $AB'+B'C+AC$**

A	B	C	Output of Non-Standard SOP	Output Standard SOP
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

**3.  $A'B+AC'$**

A	B	C	Output of Non-Standard SOP	Output Standard SOP
0	0	0		
0	0	1		
0	1	0		
0	1	1		

1	0	0		
1	0	1		
1	1	0		
1	1	1		

**4.  $B'C + A'C' + AB'C$**

A	B	C	Output of Non-Standard SOP	Output Standard SOP
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

## Task 02: Product of Sum (POS)

- A. Construct a digital circuits of the given Boolean expression in Multisim, which is presented in a non-standard Product of Sum (POS) format.
1.  $(A') \cdot (A' + B)$
  2.  $(A' + B') \cdot (A' + B' + C) \cdot (A + C)$
  3.  $(B' + C) \cdot (B' + C) \cdot (A + B' + C')$

**Add snapshot of circuits of above POS expressions**

1	
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2	
3	

- B. Convert the non-standard POS expression from A into the standard POS format and then build its digital circuit representation as a standard POS Boolean expression in Multisim.

**Note: use the pen paper to convert the non-standard POS to Standard POS, add the snapshot of the solutions below**

1	
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2	
3	

**Add snapshot of circuits of above standard POS expressions**

1	
2	
3	

C. Fill the following truth table for output of the both standard POS and standard POS circuits.

**1.  $(A') \cdot (A' + B)$**

A	B	C	Output of Non-Standard POS	Output Standard POS
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

**2.  $(A' + B') \cdot (A' + B' + C) \cdot (A + C)$**

A	B	C	Output of Non-Standard POS	Output Standard POS
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		

**3.  $(B' + C) \cdot (B' + C) \cdot (A + B' + C')$**

A	B	C	Output of Non-Standard POS	Output Standard POS
0	0	0		
0	0	1		
0	1	0		
0	1	1		
1	0	0		
1	0	1		
1	1	0		
1	1	1		