

Beyzanur DEMİR  
G161210045

### Bellek Birimi Binary Haritalama

			A <sub>15</sub> A <sub>14</sub> A <sub>13</sub> A <sub>12</sub>	A <sub>11</sub> A <sub>10</sub> A <sub>9</sub> A <sub>8</sub>	A <sub>7</sub> A <sub>6</sub> A <sub>5</sub> A <sub>4</sub>	A <sub>3</sub> A <sub>2</sub> A <sub>1</sub> A <sub>0</sub>
BOF	0x000					
	0x1000	4x8 8255	1000h	0 0 0 1	0 0 0 0	0 0 0 0
8255 (4x8)			1FFFh	0 0 0 1	1 1 1 1	1 1 1 1
	0x1FFF					
BOF	0x6000	8x8 RAM	6000h	0 1 1 0	0 0 0 0	0 0 0 0
			6FFFh	0 1 1 0	1 1 1 1	1 1 1 1
RAM (16x8)	0x9FFF		7000h	0 1 1 1	0 0 0 0	0 0 0 0
			7FFFh	0 1 1 1	1 1 1 1	1 1 1 1
BOF	0xC000		8000h	1 0 0 0	0 0 0 0	0 0 0 0
			8FFFh	1 0 0 0	1 1 1 1	1 1 1 1
ROM-1 (4x8)	0xCFFF		9000h	1 0 0 1	0 0 0 0	0 0 0 0
			9FFFh	1 0 0 1	1 1 1 1	1 1 1 1
BOF	0xE000	8x8 ROM-2	C000h	1 1 0 0	0 0 0 0	0 0 0 0
			CFFFh	1 1 0 0	1 1 1 1	1 1 1 1
ROM-2 (8x8)	0xFFFF		E000h	1 1 1 0	0 0 0 0	0 0 0 0
			FFFFh	1 1 1 0	1 1 1 1	1 1 1 1
			F000h	1 1 1 1	0 0 0 0	0 0 0 0
			FFFFh	1 1 1 1	1 1 1 1	1 1 1 1

A<sub>14</sub>, A<sub>13</sub>, A<sub>2</sub> → Seçim uçları

A<sub>15</sub> → decoder değişim

8255 → y<sub>1</sub>

RAM → y<sub>0</sub>, y<sub>1</sub>, y<sub>6</sub>, y<sub>7</sub>

ROM-1 → y<sub>4</sub>

ROM-2 → y<sub>6</sub>, y<sub>7</sub>

### I/O Birimi Binary Haritalama

			A <sub>15</sub> A <sub>14</sub> A <sub>13</sub> A <sub>12</sub>	A <sub>11</sub> A <sub>10</sub> A <sub>9</sub> A <sub>8</sub>	
BOF	0x00				
	0x40				
I/O-1 (8x8)	0x47		0 0 1 1	0 0 0 0	I/O-1 → y <sub>1</sub>
			0 1 0 0	0 1 1 1	
BOF	0xb0		0 1 1 0	0 0 0 0	I/O-2 → y <sub>3</sub>
			0 1 1 0	0 0 1 1	
I/O-2 (4x8)	0xb3		1 1 1 0	0 0 0 0	8255 → y <sub>7</sub>
BOF	0xE0		1 1 1 0	0 0 1 1	
8255 (4x8)	0xE3				
BOF	0xFF				

A<sub>15</sub>, A<sub>14</sub>, A<sub>13</sub> → Seçim uçları

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