**EXPERIMENT NO.6**

**Sequential Circuit Design**

**SUBMITTED TO:**

Dr. Ubaid Ullah Fayyaz

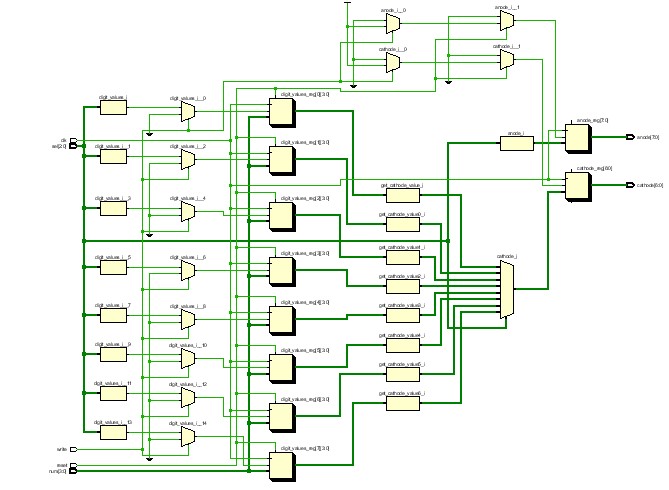
**SUBMITTED BY:**

Hammad Jafar

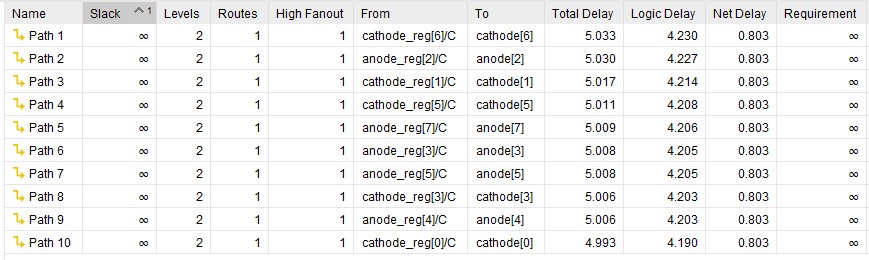
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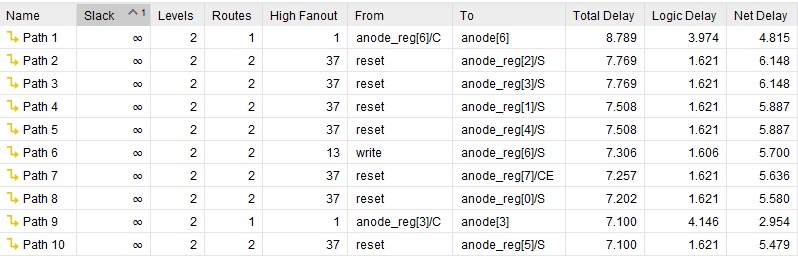
**Circuit diagram using Vivado:**



**Delay in Synthesis:**



**Delay in Implementation:**



**Resource Utilization:**

