

Examination Feedback for EEE6042 – Integrated Circuit Technology
Autumn Semester 2012-13

Feedback for EEE6042 Session: 2012-2013

Feedback: Please write simple statements about how well students addressed the exam paper in general and each individual question in particular including common problems/mistakes and areas of concern in the boxes provided below. Increase row height if necessary.

General Comments:

This year the students have given an excellent improvement in the exam, with much clarity and understanding of the various aspects of the course.

Question 1:

Some improvement still required in crystallography. It was pleasing to note that most students answered the question related to phonons in indirect bandgap semiconductors as well as in the assessment of the packing density in fcc crystals. Most students were weak on Miller indices, and in the drawing of the crystallographic planes.

Question 2:

Excellent response to all questions here, particularly in explaining MBE and MOCVD systems. Barring a few who did not explain pseudomorphic layers very well, almost all got the problem solved correctly.

Question 3:

Very pleasing to see almost 100% response on CMOS and lift-off processes, Qb was not that well answered.

Question 4:

Almost all students solved Q4d correctly, however, the response to junction spiking was not consistent. Most were able to explain the difference between positive and negative photoresist.