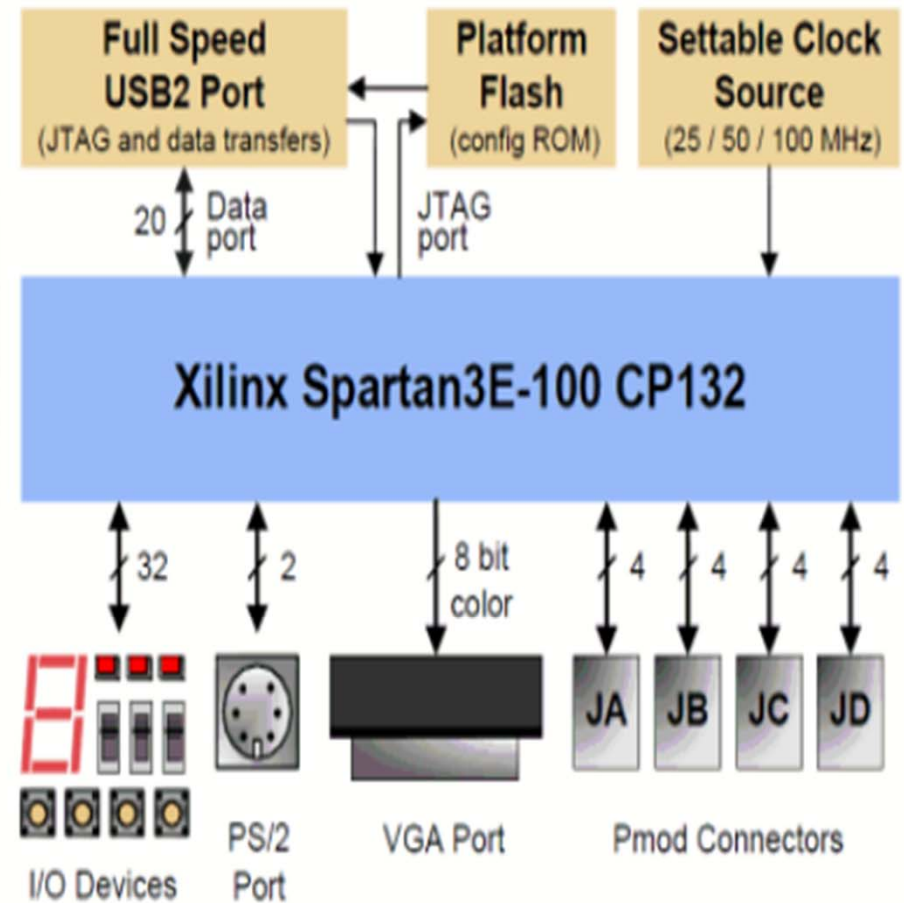
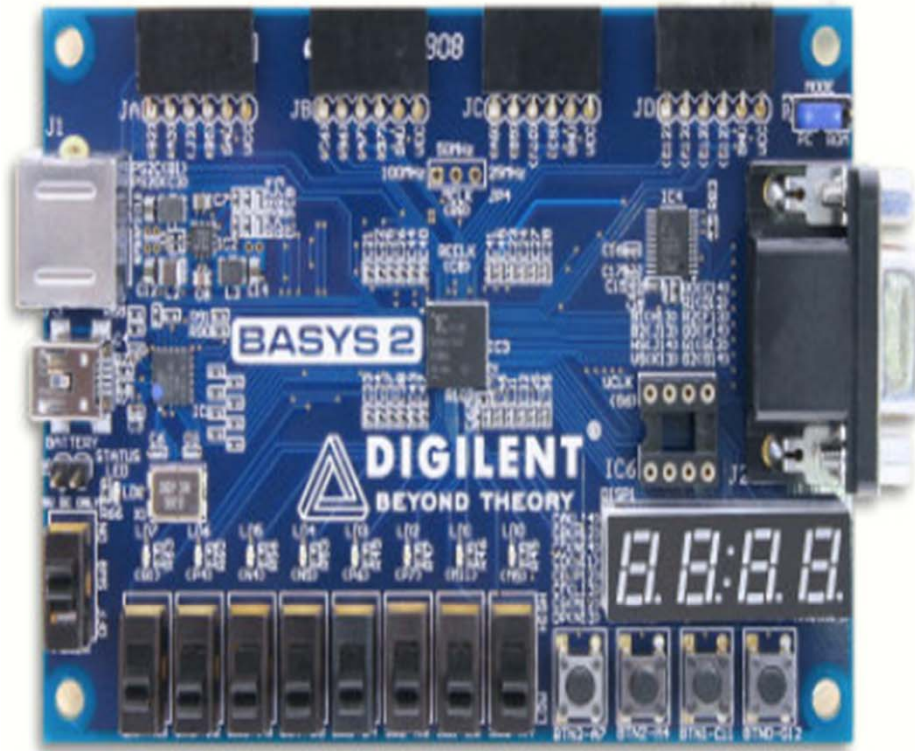


# Development Boards

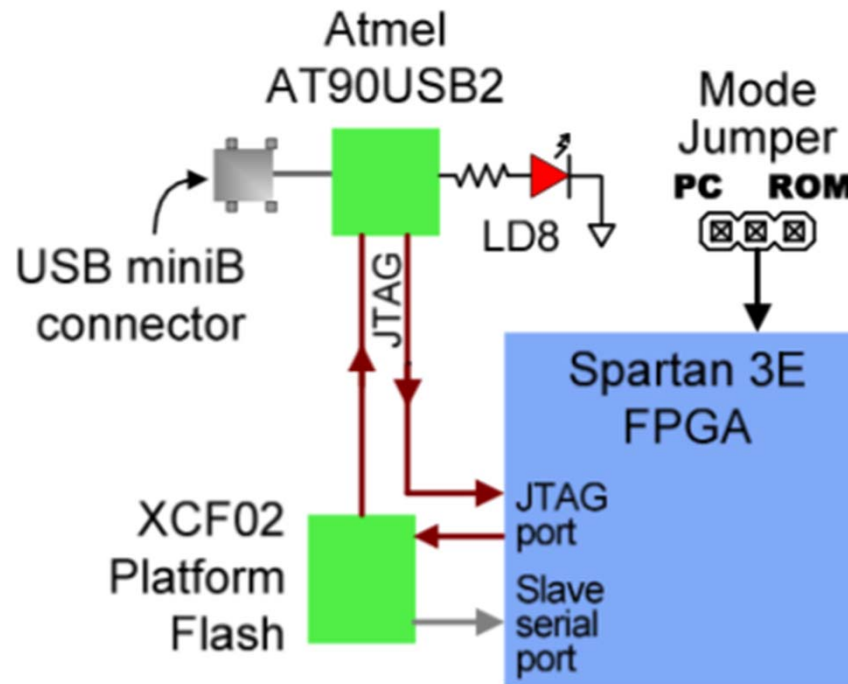
- Digilent BASYS2 (Xilinx FPGA)
- Programming
- I/O
- VGA output

SOURCES: Digilent Inc. Basys2 Board Reference Manual

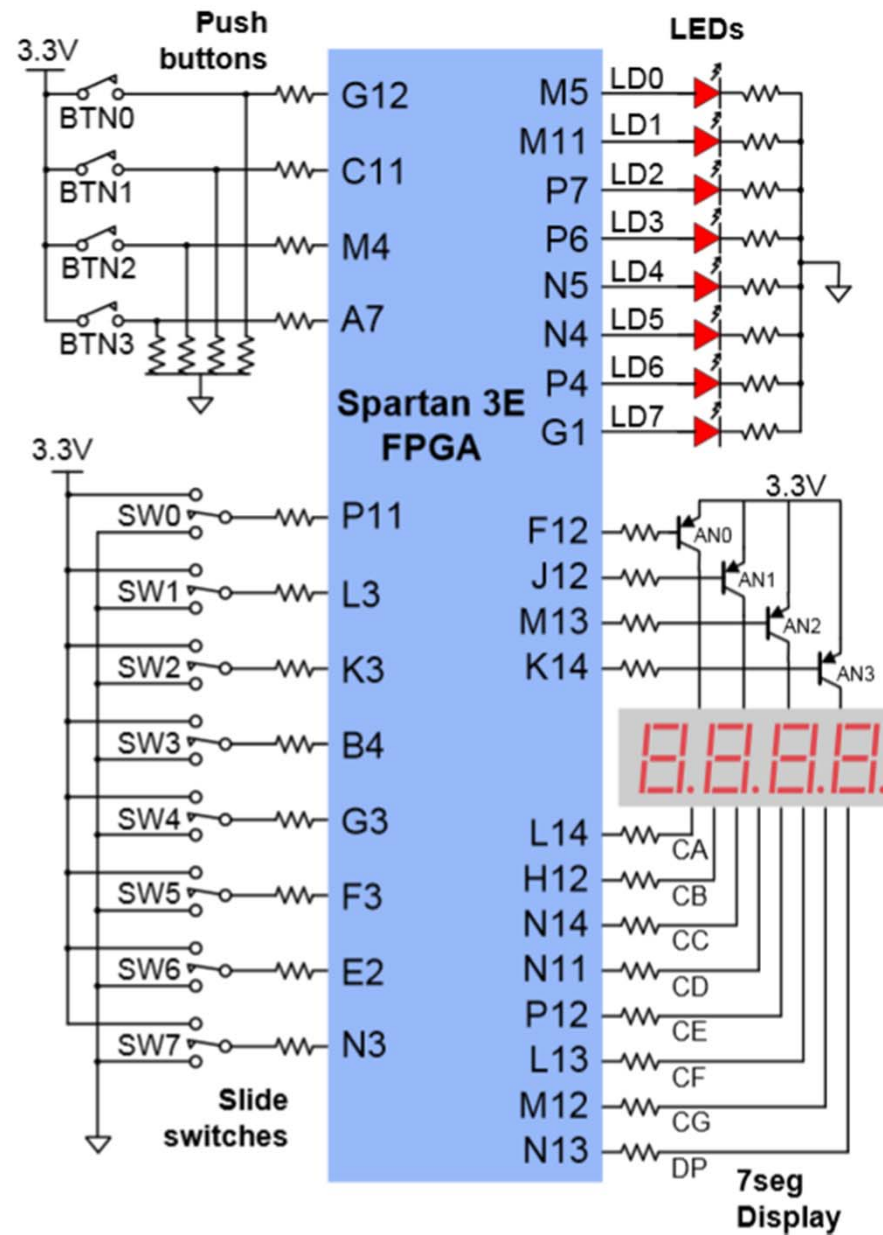
[http://digilentinc.com/Data/Products/BASYS2/Basys2\\_rm.pdf](http://digilentinc.com/Data/Products/BASYS2/Basys2_rm.pdf)

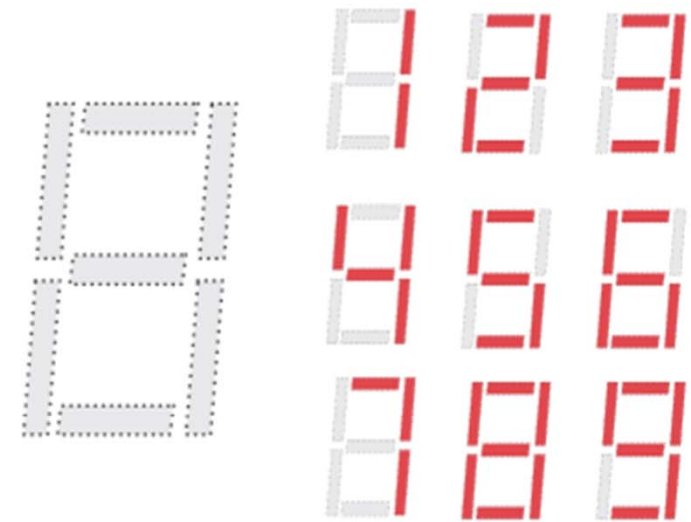
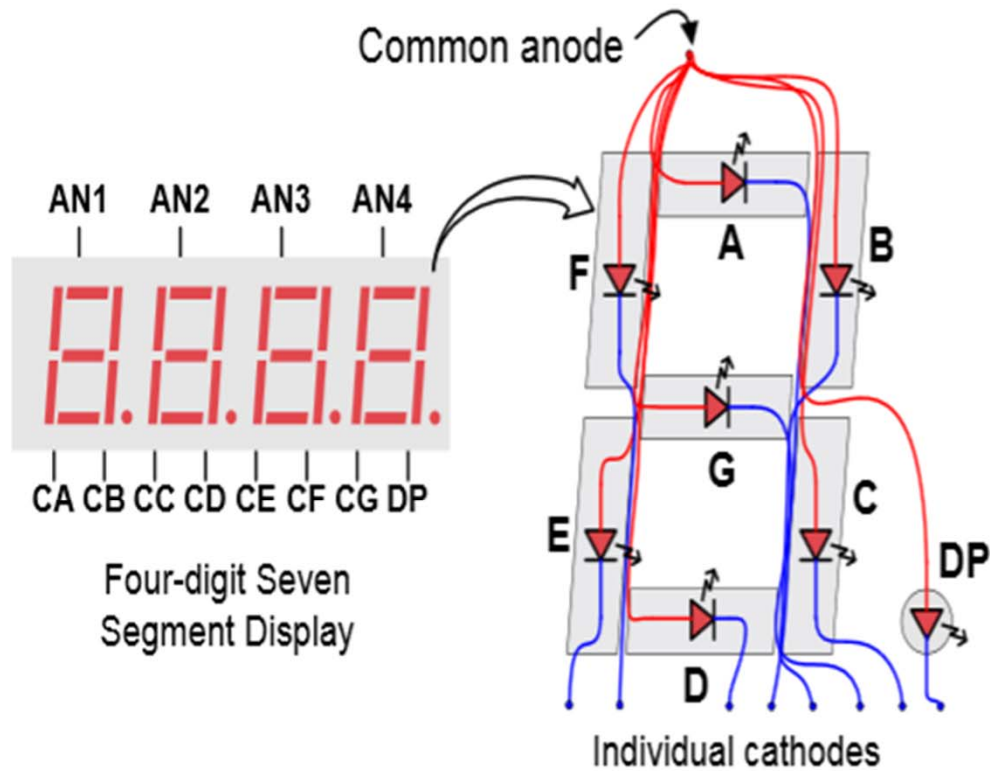


# Programming

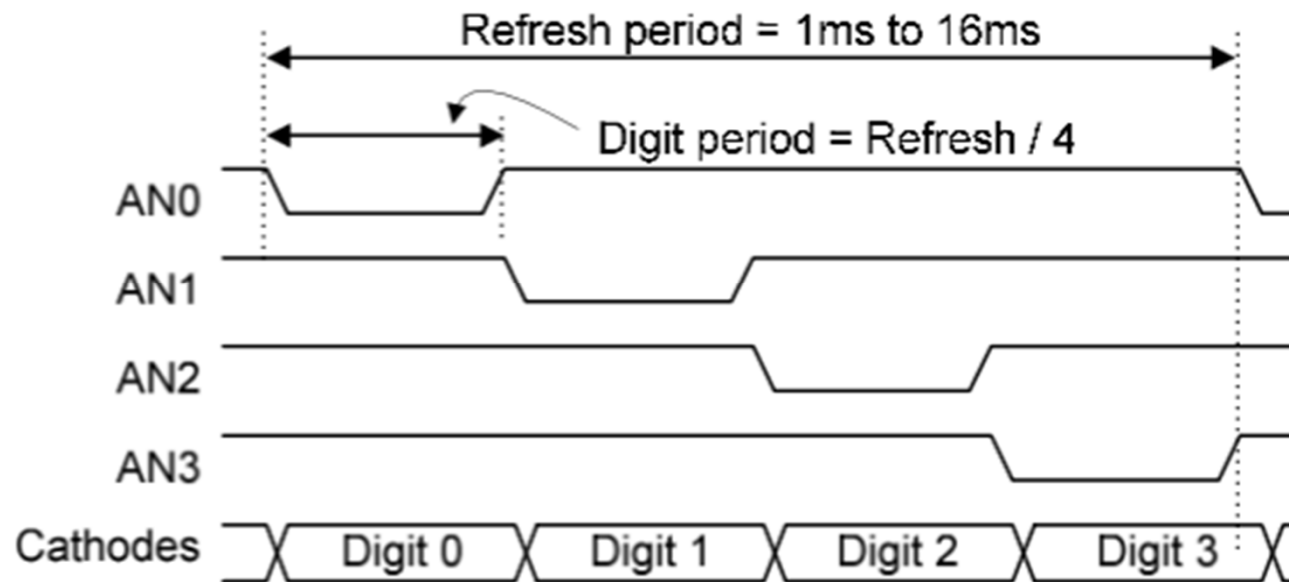


Configuration file can be held on the FPGA or on platform flash.

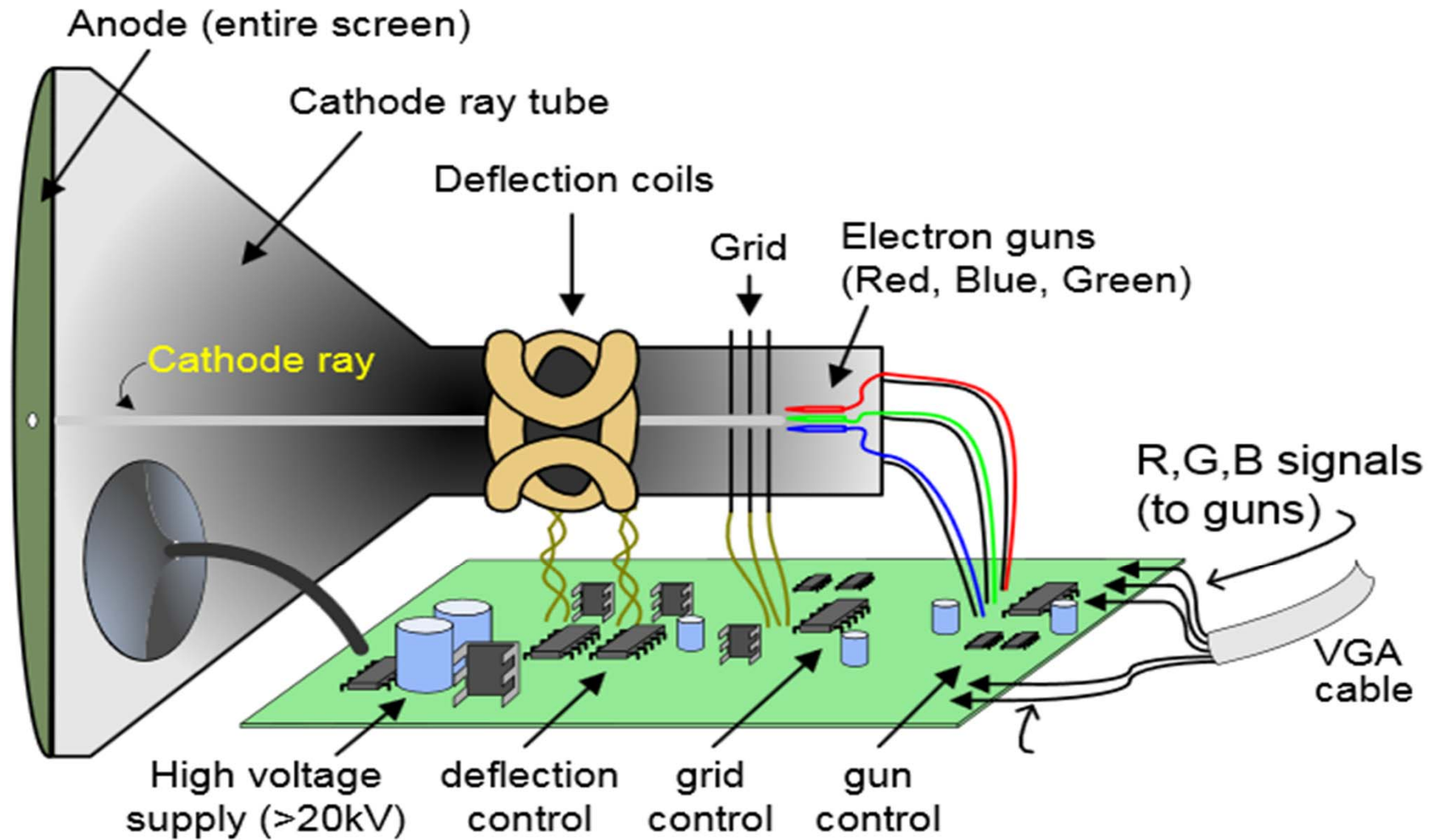




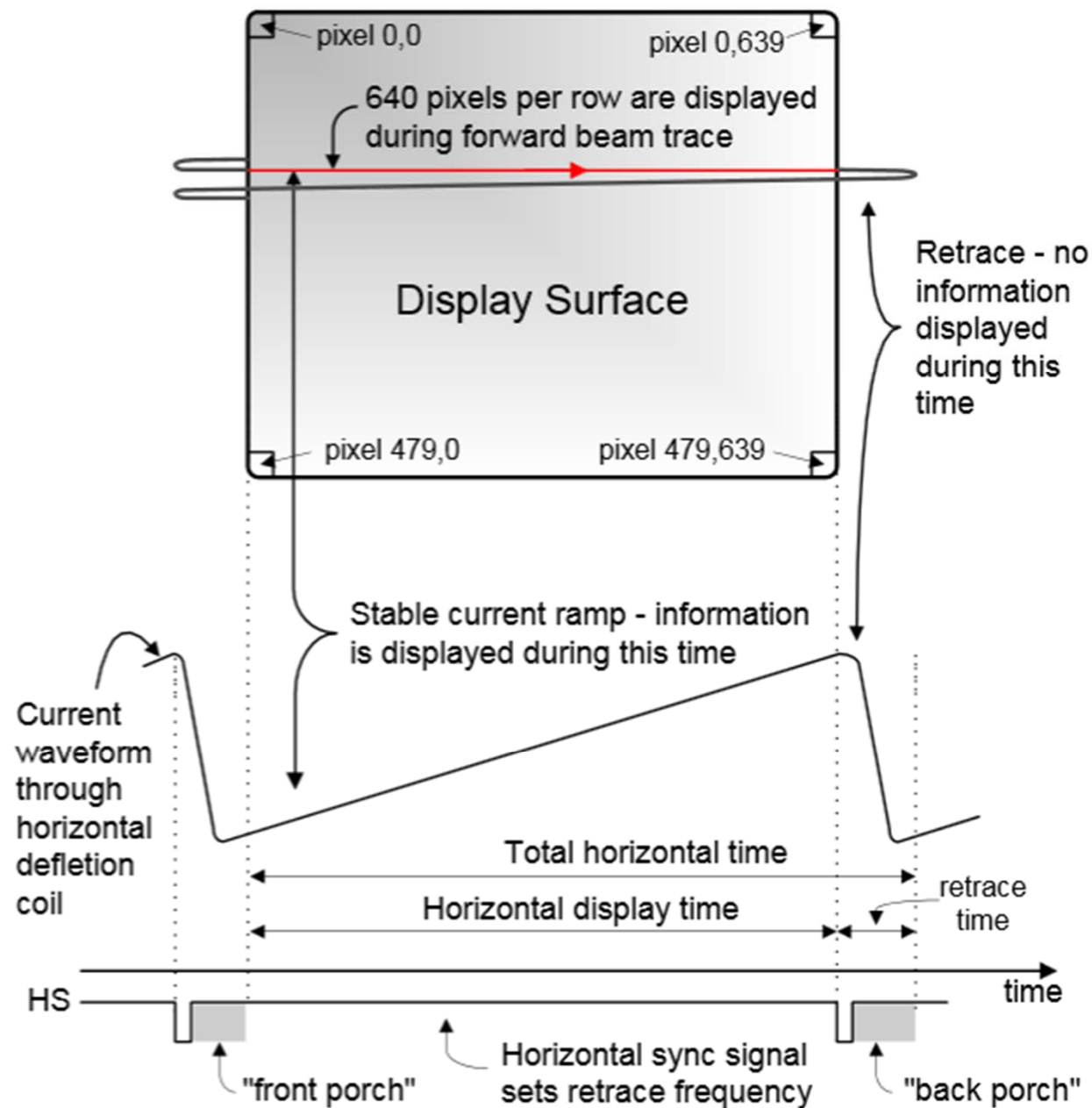
An un-illuminated seven-segment display, and nine illumination patterns corresponding to decimal digits



# VGA Interface - CRT









- VGA Interface with 10 FPGA signals
- 8 bit colour, 2 sync signals
- 8 levels on red & green, 4 on blue
- Binary weighted input

