Examination Feedback for EEE403– High Speed Circuit Design Autumn Semester 2007-08

Feedback for EEE403 Session:2007-2008

<u>Feedback:</u> Please write simple statements about how well students addressed the exam paper in general and each individual question in particular including common problems/mistakes and areas of concern in the boxes provided below. Increase row height if necessary.

General Comments:			

Question 1:

Most students managed parts (a and c) reasonably well.

Quite few students went wrong in part (b) by explaining the operation of a short, or open, circuit quarter wavelength transmission line instead of explaining the quarter wavelength transformer that is used in impedance matching.

Nearly all of the students struggled with part (d) since they have calculated the magnitude of the reflection coefficient correctly but they didn't calculate the phase of the reflection coefficient from the given position of standing wave's first minimum.

Question 2:

All students have attempted this question. Part (a) was exactly given in the lecture notes but still many students couldn't answer it correctly sine they went on explaining the advantages of using the Smith chart instead of admittance calculation using Smith chart.

Parts (b and c) were solved correctly by most of the students.

Question 3:

Parts (a and c) most students answered them correctly.

Part (b) is a simplified version of an example that has been done in the lectures quite few students couldn't solve it.

Although they have a similar example in the lectures, many students struggled with part (d).

Question 4:

Parts (a and b) were exactly given in the lecture notes and students managed to solve them reasonably well. Part (c) is a straightforward application of gain equations from lecture notes, most of the errors were made during complex number calculations. Part (c) is very similar to an example given in the lecture notes, few student has managed to solve it completely.