



The
University
Of
Sheffield.

DEPARTMENT OF ELECTRONIC AND ELECTRICAL ENGINEERING

EEE118 Electronic Devices and Circuits

Mid-year test Jan 2013

Student Registration Number:

as shown on U.Card, but not U.Card number

Mass of free electron, $m_e = 9.11 \times 10^{-31}$ kg

Charge on electron, $q = 1.60 \times 10^{-19}$ C

Boltzmann constant, $k = 1.38 \times 10^{-23}$ J/K

$$C = \frac{\epsilon_0 \epsilon_r A}{d}$$

$$\mu = \frac{q\tau}{m^*}$$

$$E = -\frac{dV}{dx}$$

$$J = qD \frac{dn}{dx}$$

$$R = \rho L/A$$

$$D = \frac{kT}{q} \mu$$

$$L = \sqrt{D\tau}$$

$$\partial p = \partial p_0 \exp\left(\frac{-x}{L_h}\right)$$

$$\langle v_d \rangle = -\mu E$$

Permittivity of free space, $\epsilon_0 = 8.85 \times 10^{-12}$ F/m

Planck's constant, $h = 6.63 \times 10^{-34}$ Js

Poisson's Equation $\frac{d^2V}{dx^2} = -\frac{\rho}{\epsilon}$

Energy of a photon $= hc/\lambda$

$$\sigma = \frac{1}{\rho} = ne\mu_e + pe\mu_h$$

$$n_i = C T^{3/2} \exp\left(-\frac{E_g}{2K_B T}\right)$$

$$n_i^2 = n_n p_n = n_p p_p$$

$$\langle v_d \rangle = -\frac{qE\tau}{m^*}$$

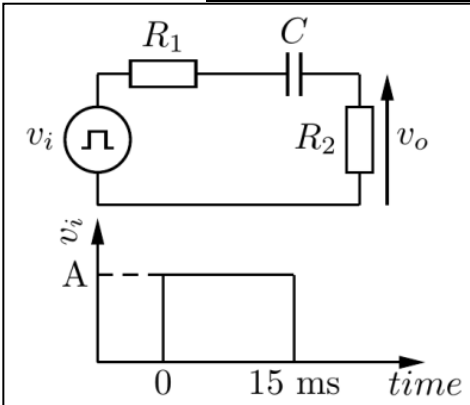
Answer all questions by writing the answer in the box provided (there is no requirement to show workings on this sheet)

One mark per question, unless indicated

Answer Here

- 1 A certain semiconductor has an electron effective mass of $0.1m_e$ and an effective scattering time of 1×10^{-10} s. Calculate the drift velocity of the electron for an applied field of 200 Vm^{-1} .

2	<p>Which one of the following statements about the built-in voltage of a diode is true?</p> <p>A. electrons and holes are repelled from the junction towards the surface of the material. These surface charges lead to a voltage across the diode which can be measured.</p> <p>B. n-type material is negatively charged and p-type material is positively charged, so when they are put together we get a field.</p> <p>C. It is created by the placing of an external bias across the device.</p> <p>D. It is created by the recombination of electrons and holes in the region around the p-n junction leaving ionized donor and acceptor atoms which cannot move and form a built-in field and voltage.</p>	
3	<p>Which one of the following statements is true:</p> <p>A dielectric is inserted between two capacitor plates. The capacitance will:</p> <p>A. Decrease since the dielectric can absorb charges from the capacitor plates.</p> <p>B. Decrease due to current flowing in the dielectric between the plates</p> <p>C. Not change. The only reason dielectrics are used in making capacitors is that it is an easy way to hold the plates a fixed distance apart.</p> <p>D. Increase since the fixed charges in the dielectric can change their distribution to oppose an electric field applied across the capacitor.</p> <p>E. Increase as the charges from the dielectric travel to the capacitor plates.</p>	
4	<p>What is the resistivity of a material with a free electron concentration of $1 \times 10^{23} \text{ m}^{-3}$ and mobility of $0.7 \text{ m}^2 \text{V}^{-1} \text{s}^{-1}$.</p>	
5	<p>What is the resistance of a rod that is 10 cm long, 3 mm^2 in cross-sectional area, and which has a conductivity of $500 \text{ } \Omega^{-1} \text{m}^{-1}$.</p>	
6	<p>Which one of the following statements correctly describes a “hole” in a semiconductor:</p> <p>A. When an electron is given sufficient energy it leaves a bond. All these free electrons repel each other, move to the sample surface, leaving a large positively charged region called a hole in the middle.</p> <p>B. When an electron is given sufficient energy to leave its bond then it leaves behind a hole in the bond it left. This hole is positively charged as the electron that left was negatively charged.</p> <p>C. A hole is a place in the semiconductor crystal where an atom is missing.</p> <p>D. When an electron is given sufficient energy to leave a bond it can move freely round the material, we call this negatively charged free particle a hole as it behaves as if it has a negative mass.</p> <p>E. When energy is applied to a material, atoms can jump out of their bonds and travel around the material, the atoms are positively charged ions and are called holes as they behave as a hole that electrons can fall into.</p>	
7	<p>In an intrinsic piece of silicon the electron and hole density is equal at $1.45 \times 10^{16} \text{ m}^{-3}$. The mobility of electrons and holes are $0.12 \text{ m}^2 \text{V}^{-1} \text{s}^{-1}$ and $0.045 \text{ m}^2 \text{V}^{-1} \text{s}^{-1}$, respectively. What is the conductivity of the material?</p>	
8	<p>A piece of Si with the same intrinsic carrier density as in question 7 above is doped to give a hole carrier density of $4 \times 10^{24} \text{ m}^{-3}$. What is the density of free electrons in this sample?</p>	
9	<p>Using a band description, the energy state associated with an acceptor atom will be:</p> <p>A. Inside the conduction band</p> <p>B. In the forbidden gap near the conduction band edge</p> <p>C. In the middle of the forbidden gap</p> <p>D. In the forbidden gap near the valence band edge</p> <p>E. Inside the valence band.</p>	

<p>10 Complete the following statement correctly: As the temperature is increased from room temperature the resistivity of an intrinsic semiconductor</p> <p>A: will decrease as extra electrons and holes are created to become free to move in the material.</p> <p>B: will decrease as the effective masses of the electrons and holes become less and so they become easier to move</p> <p>C: will increase as the effective masses of the electrons and holes become more and so become harder to move</p> <p>D: will stay the same as the resistivity depends on the materials properties, such as the defect density in the material and these do not change with temperature.</p>	
<p>11 A very short pulse of light is shone uniformly onto an n-type semiconductor sample, creating 10^{20} m^{-3} electron-hole pairs. The minority carrier lifetime is 100 ns. How many holes are left after 0.5 μs? [Hint: the equation you need to use is in the form of an exponential decay with time]</p>	
<p>12 Which two of the following statements are true: For an LED to emit light:</p> <p>A. A pn junction is required</p> <p>B. It needs to be forward biased</p> <p>C. It needs to be reverse biased</p> <p>D. It can be made from silicon</p>	
<p>13 The circuit in Fig. 1 shows an RC network driven by a voltage pulse, v_i. Circle the correct expression for the time domain function of the circuit. (1 mark).</p> <p>a. $v_o = A_0 \left(1 - \exp\left(\frac{t}{\tau}\right) \right)$</p> <p>b. $v_o = A_0 \left(1 - \exp\left(-\frac{t}{\tau}\right) \right)$</p> <p>c. $v_o = A_0 \left(\exp\left(-\frac{t}{\tau}\right) \right)$</p> <p>d. $v_o = A_0 \left(\exp\left(\frac{t}{\tau}\right) - 1 \right)$</p> <p>e. $v_o = -A_0 \left(1 + \exp\left(-\frac{t}{\tau}\right) \right)$</p>	 <p>The circuit diagram shows an AC voltage source v_i in series with a resistor R_1 and a capacitor C. The capacitor is in series with a resistor R_2, and the output voltage v_o is measured across R_2. Below the circuit is a graph of the input voltage v_i versus time. The graph shows a rectangular pulse that starts at $t = 0$ with an amplitude A and ends at $t = 15 \text{ ms}$.</p> <p>Fig. 1: Above, an RC circuit. Below, input voltage, v_i, graph.</p>
<p>14 What is the relationship (in terms of R_1 and R_2) between A_0 and A (the pulse amplitude of v_i on the graph in Fig. 1) in Q13. (1 mark).</p>	$A_0 =$
<p>15 What is the relationship between τ and R_1, R_2 and C in Q13. (1 mark).</p>	$\tau =$

16	For the circuit in Q13, sketch the output voltage v_o due to the input shown in Fig. 1. (2 marks) Anotate the sketch with, a. The peak value of the voltage in terms of the circuit components R_1 , R_2 . (1 mark) . b. The time constant in terms of the circuit components R_1 , R_2 and C . (1 mark) .	
17	Assuming $A = 10$ V, $R_1 = R_2 = 1$ k Ω and $C = 1$ μ F what is the value of v_o at $t = 2$ ms? (1 mark) .	
18	What is the significance of 2 ms in this circuit? (2 marks) .	
19	Draw a circuit symbol for a diode. (1 mark) .	
20	Label the anode and cathode in Q19. (1 mark) .	
21	Add two arrows on your diagram in Q19. Show, <ul style="list-style-type: none"> the direction of current flow when the diode is <i>forward biased</i> above the turn on voltage. Label this arrow I. (1 mark). the direction of the voltage which biases the diode. Label this arrow V. (1 mark). 	
22	What does “on the edge of conduction” mean when considering a diode circuit? (2 marks)	
24	Sketch a peak detector circuit. Label the components and the input and output voltages of your circuit. (1 mark) .	
25	Write on the time constant, τ , of the circuit in terms of the component names. (1 mark) .	
26	Give one application of a peak detector, other than the detection of AM radio . (1 mark) .	

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On the axes below, sketch:

- A sine input voltage waveform.
- A peak detector output voltage waveform.
- The capacitor current.

(3 marks)

Label the axes **and** write on the graph, the value of τ in terms of the components names you used in Q24 and Q25. Draw an arrow to link your expression for τ and the output waveform. (1 mark).

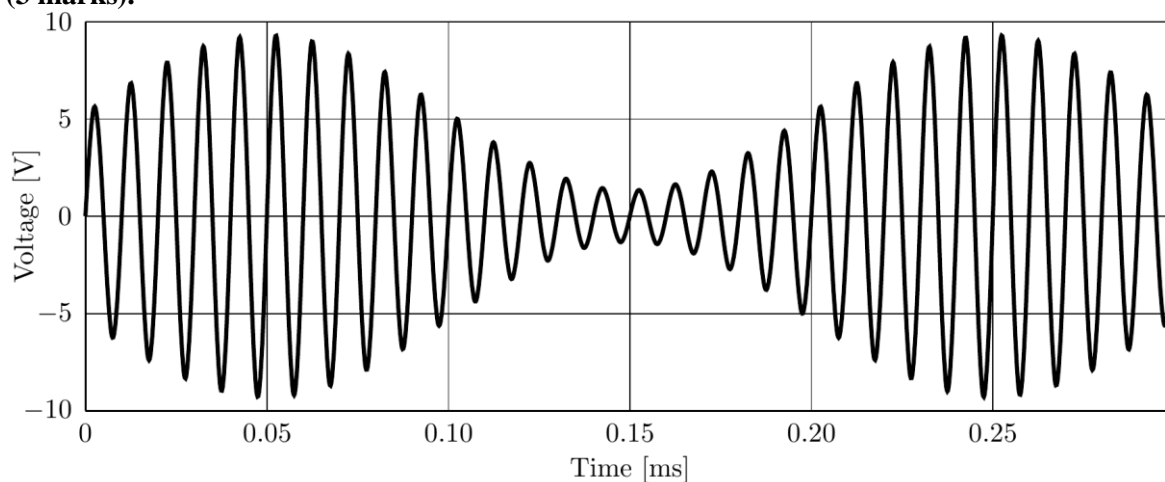


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On the axis below, a sine wave modulated AM radio signal has been drawn. This voltage signal is passed into the input of a peak detector.

Sketch on the axis below, three output voltage waveforms showing the effect of changing the time constant of the peak detector. Label your output waveforms “too big”, “too small” and “just right”.

(3 marks).



END OF PAPER