Examination Feedback for EEE403/6035 – High Speed Circuits Design Autumn Semester 2008-09

# Feedback for EEE403/6035 Session:2008-2009

<u>Feedback:</u> Please write simple statements about how well students addressed the exam paper in general and each individual question in particular including common problems/mistakes and areas of concern in the boxes provided below. Increase row height if necessary.

General Comments:		

#### Question 1:

Most students managed parts (a and b) reasonably well.

Nearly all of the students struggled with parts (c and d). They went wrong with part (c) because they didn't start the required proof from the equations of voltages and currents along a transmission line, instead most of them have re-arranged the given formula as a proof.

In part (d) the common mistake was the assumption of voltages at the source and load ends to be equal, while it is the power which is equal at those two points.

#### Question 2:

Most students managed to answer this question reasonably well.

## Question 3:

- (a) Many students didn't list the correct four rules, instead they went into detailed description of SFD without mentioning the rules.
- (b) Although the solution to this question is given exactly in the lecture notes, most of the students couldn't answer it correctly because the format of the question is different from what is given in the lecture notes. This shows the important of understanding the lectures rather than memorizing them.
- (c) All students managed to solve this part as it is given in the tutorials.

### Question 4:

- (a) Few students defined the unconditional stability correctly without plotting the required diagrams.
- (b) Nearly all students have answered this part correctly.
- (c) Again this question has been solved in the lectures but the majority of students didn't solve it correctly as they have used the design procedure for constant gains rather than that of maximum amplifier's gain.