EEE 117.

Electrical Circuits and Networks

First half ( thats TOZER)

- basic ideas of circuits and components and key act laws.

- d.c. circuits

- a.c. circuits

- phasor upresentation

- complex number representation.

- power

Ken Mitchell

- magnetic circuits

- inductance

- power.

- leal + reactive power

- three phase.

## Networks

- usually described by circuit diagrams

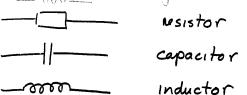
(or schematic diags.)

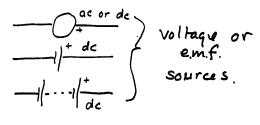
(or schematics)

-represents the connectivity of an assembly of electrical/

electronic components

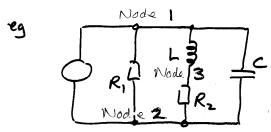
each different component has a symbol \_\_\_\_\_ told UK symbol







Components connected together by ideal wires



- a circuit "node" is where two or more components are connected together

( Node ! has 4 connections

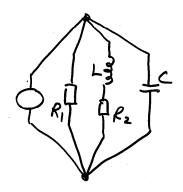
Node 2 has 4 connections

Node 3 has 2 connections

Simple node because it only has
a connections

Major nodes because they have
3 or more connections

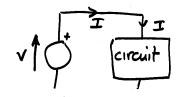
cct can be redrawn to make the nodes more obvious

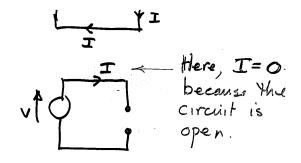


Current flows around a circuit

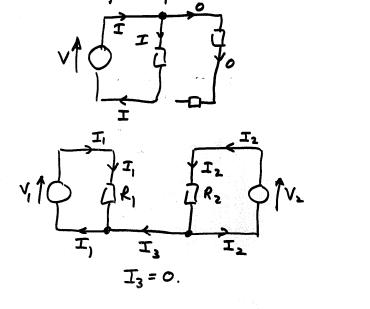
— it is driven by "electromotive force"

or e.m.f. The emf provides
a potential difference which
makes the electrons move.





If part of the circuit is open, no current will flow through the open part

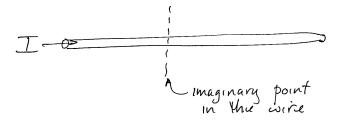


- Objective is to understand how circuits work so that one can be inventine in achieving design

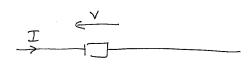
uinis.

### Current

- rate of passage of change through an imaginary point in a cct



 $I = \frac{dQ}{dE}$  at the maginary point.

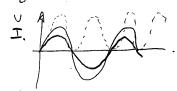


Power + Energy.

Work done = energy used =  $\int_{0}^{t} V. I.$ = V.I.t at dc.

power = vate of energy use  $= \frac{1}{t} \int_{0}^{t} VI. = VI \text{ at } dc.$ 

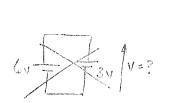
If V + I are functions of Time, ie  $V \Rightarrow V(E)$   $I \Rightarrow I(E)$   $E = \int_{0}^{E} V(E) I(E) dE.$ Power =  $\frac{1}{I} \int_{0}^{I} V(E) I(E) dE.$ 

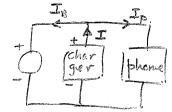


P = T (V(E) IE) dt. Dottom half has been chopped off

Voltage Sources

absorbing V A delivenergy.





Current Sources

- usually exist

as electronic

ccts withe

internal feedback

designed to

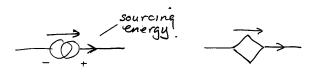
maintain a

constant I.

- usually operate over

a limited range of

terminal voltage.



Sinking or absorbing energy.

Resistors

I EN/UK symbol

US + for
east symbol

V = IR

R 15 an energy sink and it dissipates the energy it sinks

Size (physical) of the resistor determines its ability to dissipute

heat.

Power in resistine circuits

$$P = VI = V(\frac{V}{R}) = \frac{V^2}{R} W \left(if V = volts\right)$$

$$\begin{pmatrix} = \frac{V}{R} \end{pmatrix}$$

Preferred resistor values.

-preferred value series

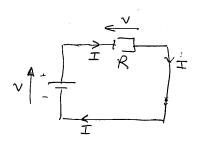
E6 -> 20% blerance.

E12 - 10% tolerance

E24 -> 5% tolerance.

look at E12.

E12 series 1, 1.2, 1.5, 1.8, 2.2, 2.7, 3.3, 3.9, 4.7, 5.6, 6.8, 8.2

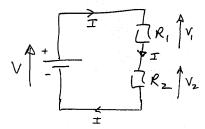


Sum of directed voltages around any closed loop in a cet must be zero

Current entering a point in the circuit must regular current beaving that point.

Kirchoff's laws

If there is more than one desistor in the circuit...

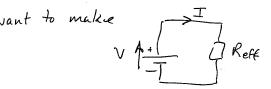


R, + Rz are in series because the same current must flow through them.

$$V_{1} = IR_{1} \qquad V_{2} = IK_{2}$$

$$V + (-V_{1}) + (-V_{2}) = 0$$
or 
$$V = V_{1} + V_{2} = IR_{1} + IR_{2}$$

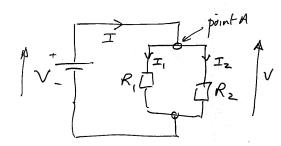
$$= I(R_{1} + R_{2}) \qquad 0$$



$$R_{eff} = \frac{V}{I} = R_1 + R_2$$
 (from eq. 11)

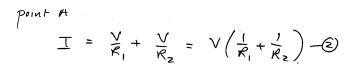
If more than two resistans are connected in series

Reff = Sum of values.

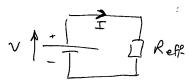


$$I_1 = \frac{\vee}{R_1}$$
  $I_2 = \frac{\vee}{R_2}$ 

using Kirchoff's current law.



to model This as

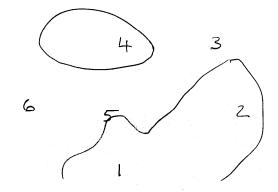


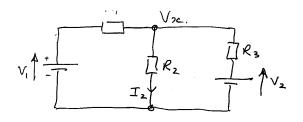
$$R_{eff} = \frac{V}{I} = \frac{V}{V(\frac{1}{R_1} + \frac{1}{R_2})} \quad \text{(using equ. 2)}$$

$$\therefore R_{eff} = \frac{1}{\frac{1}{R_1} + \frac{1}{R_2}}$$
or 
$$\frac{1}{R_{eff}} = \frac{1}{R_1} + \frac{1}{R_2}$$

if there are more than two nexistors in parallel

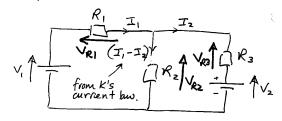
$$\frac{1}{R_{\text{eff}}} = \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3} + \dots + \frac{1}{R_N}$$





Methods of solution

(1) Application of Kirchoff's laws.



use Kirchoffs current law at each node to avoid creating redundant variables

use K's voltage law to sum voltages around the two loops.

$$V_1 - V_{R_1} - V_{R_2} = O - loop 1$$

expanding loop 1

$$V_1 - I_1 R_1 - (I_1 - I_2) R_2 = 0 - R_1$$

expanding loop 2

$$V_2 + I_2 R_3 - (I_1 - I_2) R_2 = 0 - \ell^2$$

Collecting terms

Collecting Terms

$$V_1 - I_1(R_1 + R_2) + I_2R_2 = 0 \ell I$$
  
 $V_2 + I_2(R_3 + R_2) - I_2R_2 = 0 \ell I$ 

using the substitution approach.

from 
$$l = I_2 = I_1(R_1 + R_2) - V_1$$
 $R_2$ 

in using 
$$(2 \ V_2 + \frac{I_1(R_1 + R_2) - V_1}{R_2} (R_3 + R_2) - I_1 R_2$$

$$= 0$$

$$V_{2}R_{2} + \left(I_{1}(R_{1}+R_{2})-V_{1}\right)\left(R_{3}+R_{2}\right)-I_{1}R_{2}^{2}$$

$$= 0.$$

$$V_{2}R_{2} + I_{1}(R_{1}+R_{2})\left(R_{3}+R_{2}\right)-V_{1}(R_{3}+R_{2})-I_{1}R_{2}^{2}$$

$$= 0.$$

$$V_{2}R_{2} + I_{1}\left(R_{1}R_{3}+R_{2}R_{3}+R_{2}R_{1}+R_{2}^{2}\right)-V_{1}\left(R_{3}+R_{2}\right)$$

$$-I_{1}R_{2}^{2} = 0.$$

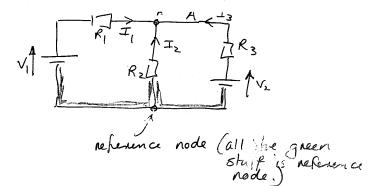
$$I_{1} = \frac{V_{1}(R_{3}+R_{2})-V_{2}R_{2}}{R_{1}R_{3}+R_{2}R_{2}+R_{2}R_{1}}$$

- substitute back in one of the loop equations to find Iz

Nodal Analysis

— aims to find node voltage w.r.t

a reference node



I let voltage at A w.r.t. reference

- label currents entering (or leaving)
- do a current sum at the node

collect terms together to end up in this case with.  $V_A = (a function of V_i, V_2 * R_s)$ 

$$\frac{V_1}{R_1} + \frac{V_2}{R_3} = V_A \left[ \frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3} \right].$$

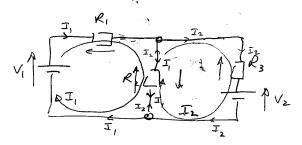
$$V_A = \frac{V_1}{R_1} + \frac{V_2}{R_3}$$

$$\frac{1}{R_1} + \frac{1}{R_2} + \frac{1}{R_3}$$

#### **EEE117 Lectures**

can be found easily once Un is known.

### LOOP ANALYSIS.



- define a current flowing around each loop of the circuit
- add up directed voltage drops around each loop.

$$-loop_{1} V_{1} - I_{1}R_{1} - (I_{1} - I_{2})R_{2} = 0$$
or  $V_{1} = I_{1}(R_{1} + R_{2}) - I_{2}R_{2}$ 

$$-\log^{2}(I_{2}-I_{1})R_{2}+I_{2}R_{3}+V_{2}=0$$

$$or_{-V_{2}}=-I_{1}R_{2}+I_{2}\left(R_{2}+R_{3}\right)$$

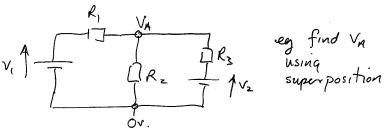
# Principle of superposition

The current in any branch of a circuit is linearly proportional to each of the sources driving the circuit

This means that each source can be considered in turn with all the

#### 2013-1014

other sources replaced by meir inhermal impedance (Or for a voltage source of va (ie open cct) for current source)



$$I = \frac{V_1}{R_1 + R_2 || R_3}$$

$$V_A = I(R_2 || R_3) = V_1 \frac{R_2 || R_3}{R_1 + R_2 || R_3}$$

$$\begin{array}{c|c}
R_1 & V_A & \exists \\
\hline
 & 1 & R_3 \\
R_2 & \hline
 & V_2
\end{array}$$

$$I = \frac{V_{2}}{R_{3} + (R_{1}||R_{2})}$$

$$V_{A} = I(R_{1}||R_{2}) = \frac{V_{2} \times (R_{1}||R_{2})}{R_{3} + (R_{1}||R_{2})}$$

### **EEE117 Lectures**

$$V_{A70T} = V_{R} \Big|_{due \, b_{V_{1}}} + V_{A} \Big|_{due \, b_{V_{2}}}$$

$$= V_{1} \frac{R_{2} || R_{3}}{R_{1} + (R_{2} || R_{3})} + V_{2} \frac{R_{1} || R_{2}}{R_{3} + (R_{1} || R_{2})}$$

Thevenin and Norton Eguivalent Ccts

Thevenin

this would model a battery.

Voc 1 Trint DRL VL.

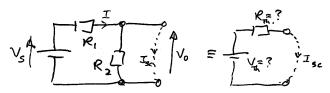
$$I = \frac{\sqrt{oe}}{\int_{int} + R_{L}}$$

$$V_{L} = IR_{L} = \frac{\sqrt{oc} R_{L}}{\int_{int} + R_{L}}$$

fint is the only unknown.

### 2013-1014

It I have a CCC



what is vo with no external load?  $V = V < \frac{R_2}{R_2}$ 

$$V_0 = V_5 \frac{R_2}{R_1 + R_2}$$

$$I = \frac{V_s}{R_1 + R_2}$$
  $V_o = IR_2 = \frac{V_s R_2}{R_1 + R_2}$ 

$$V_{Th} = V_0 = V_S \frac{R_2}{R_1 + R_2}$$

$$I_{se} = V_{s}$$

$$= V_{TH}$$

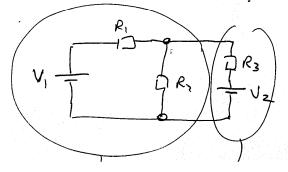
$$= V_{SR_{TH}}$$

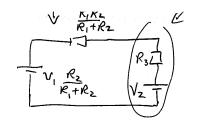
$$= V_{SR_{TH}}$$

$$R_{TH}(R_1 + R_2)$$

$$\frac{1}{R_{1}} = \frac{\sqrt{s} R_{2}}{R_{TH}(R_{1}+R_{2})}$$

$$R_{TH} = \frac{R_{1}R_{2}}{R_{1}+R_{2}}$$





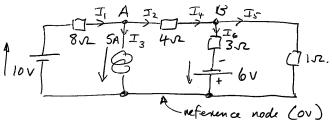


Therenin equivalent cet Norton equivalent cct.

short cet of  $p I = \frac{V_{Th}}{R_{Th}}$  short cet of  $p I = I_N$   $I_N = \frac{V_{Th}}{R_{Th}} \text{ for equivalence}$ open cet of  $p V = V_{Th}$  open cet of  $p V_0$  that  $p V_0$  is  $p V_0$ .  $I_N = \frac{V_{Th}}{R_{Th}} \text{ open cet of } p V_0$  that  $p V_0$  is  $p V_0$ .

 $V_{th} = \frac{V_{th} R_N}{R_{th}}$  for equivalence so  $R_{th} = R_N$ 

A more complicated cct with current source.



A nodal approach.

sum currents at node A

$$I_{1} = I_{2} + I_{3}$$

$$= n \text{ ferring}$$

$$\frac{10 - V_{A}}{8} = \frac{V_{A} - V_{B}}{4} + 5$$

Sum currents at node B.

$$I_{+} = I_{5} + I_{6}$$

$$V_{A} - V_{B} = V_{B} - 0 + V_{B} - (-6)$$

$$V_{B} - (-6)$$

$$V_{B} - (-6)$$

rearrange 1

$$|0 - V_A| = 2V_A - 2V_B + 8.5$$

$$= 2V_A - 2V_B + 40$$

$$-30 = 3V_A - 2V_B \qquad (1a)$$

rearrange 2

$$3V_A - 3V_B = 12V_B + 4V_B + 24$$

$$24 = 3V_A - 19V_B - 2a$$

$$(-30 = 3V_A - 2V_B)$$

$$+(-24 = -3V_A + 19V_B)$$

$$-54 = 0 + 17V_B$$

$$V_B = -\frac{54}{17} = -3.18V$$

Sub Vg in la.  

$$-30 = 3V_A - \left(-\frac{108}{17}\right)$$

$$-30 - \frac{108}{17} = 3V_A$$

$$V_A = -\frac{510 + 108}{17 \times 3} = -\frac{618}{51}$$

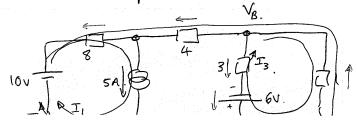
$$= -92.1 \text{ V}$$

Check ... using 2a.

$$24 = 3 \times (-12 \cdot 1) - 19 \times (-3 \cdot 18)$$

$$= -36 \cdot 3 + 60 \cdot 4$$

$$\approx 24$$



Loop ① 
$$I_1 = 5A$$
.  
loop ②  $10 = 8(I_1 + I_2) + 4I_2 + 1(I_2 + I_3)$   
loop ③  $3I_3 + 6 + (I_2 + I_3)1 = 0$ 

Mearrange loop 2  $10 = 40 + 13I_2 + I_3$   $-30 = 13I_3 + I_3$ 

rearrange loop 3.
$$-6 = I_3 + 4I_3$$

multiply loop 2 by (-4) and add to loop 3

sub. in loop 3  $-6 = -\frac{114}{4} + 4I_{3}$ 

$$-6 = -\frac{114}{51} + 4I_3$$

$$-\frac{306 + 1114}{51 \times 4} = I_3 = -\frac{192}{4 \times 51} = -0.941 \text{ A}$$

$$V_{B} = (I_{2} + I_{3}) |$$

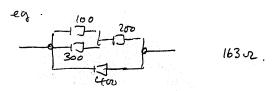
$$= (-2.24 * -0.94) |$$

$$= -3.18 V.$$

A bruef review of homework.

main points

- Not showing working.



$$R_{eff} = 400/1(200 + (100//300))$$
  
= 163

- Not finding voltage across or current the resistor of interest to calculate power.
- Abusing current sharing rules

$$V = I(R_{1}||R_{2})$$

$$= I(R_{1}||R_{2})$$

$$= I(R_{1}||R_{2})$$

$$= I(R_{1}||R_{2})$$

$$= I(R_{1}||R_{2})$$

$$= I(R_{1}||R_{2}||R_{2})$$

$$= I(R_{1}||R_{2}||R_{3})$$

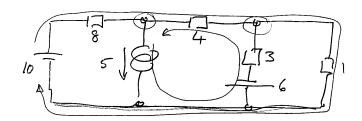
$$= I(R_{1}||R_{2}||R_{3})$$

$$= I(R_{2}||R_{3})$$

# - Younding  
.03A -> 30 mA or 
$$30 \times 10^{-3}$$
 A.  
 $1894V$   $1.89 \times 10^{3}$  V or  $1.89$  kV  
 $3mA = (.000003 \text{ A})$  not a good idea

If the answer is 2 it should be written as 2.00

- must draw a circuit if you are going to use algebraic variables like II, Iz, V3, etc.



- need as many equations as there are simple closed loops (3 in this case)

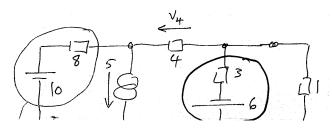
number of nodes, n = 3.

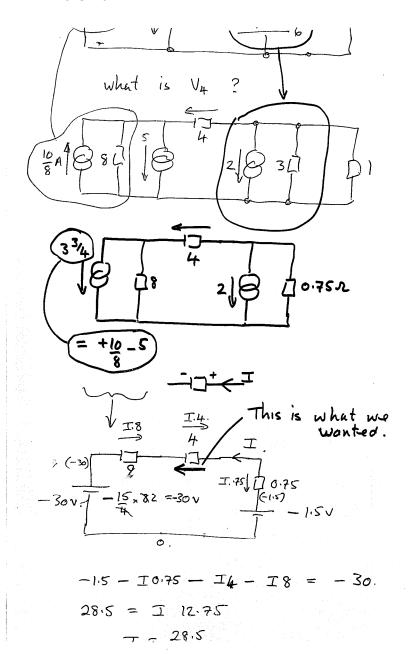
number of branches, b = 5

minimum number of bops agained

$$= (b-n)+l$$

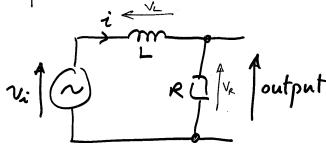
$$= (5-3)+l = 3$$





$$V_{4x} = - I.4 = -\frac{28.5}{12.75} \times 4$$
.

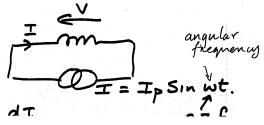
A quick background to the passive circuits lab.



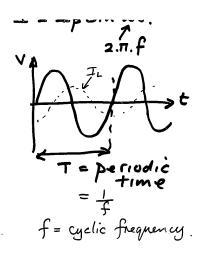
inductor governed by

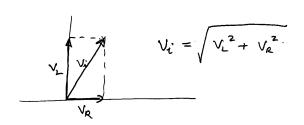
### V = L dI

let I be a sinusoidal forced current



I lags V by 90°



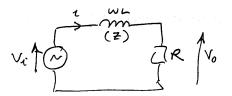


for a sinusoid the "impedance" of an inductor is

$$Z_L = \omega L = X_L$$
Inductive
Impedance

Inductive

Reactance



$$i = \frac{v_i}{Z_L + R} = \frac{v_i}{\sqrt{Z_L^2 + R^2}}$$

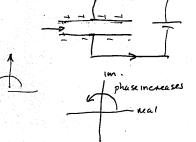
$$= \frac{v_i}{\sqrt{W_L^{22} + R^2}}$$

$$\frac{v}{i} = Z_{cet} = \sqrt{\omega^2 L^2 + R^2}$$

$$V_0 = iR = Vi \frac{R}{\sqrt{w^2L^2 + R^2}}$$

Circuits containing C

$$I = C \frac{dV}{dt}$$

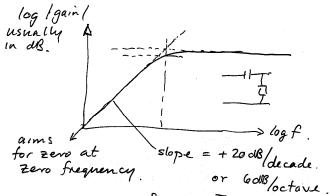


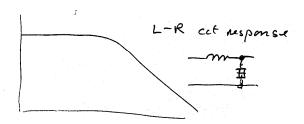
$$Z = \sqrt{R^2 + \left(\frac{1}{Wc}\right)^2}$$

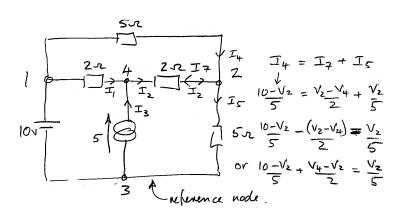
$$V_0 = i_2 R$$

$$i_2 = \frac{v_3}{2}$$

$$V_0 = \frac{V_1}{\sqrt{R^2 + (\frac{1}{w_c})^2}} \cdot R = \frac{V_1 R \omega wc}{\sqrt{1 + R^2 w^2 c^2}}$$







Vnode l=10v by inspection. — need node equations to find  $V_4+V_2$ Sum currents at node  $4: I_1+I_3=I_2$   $\frac{10-V_4}{2}+5=\frac{V_4-V_2}{2}$ or  $10-V_4+10=V_4-V_2$ or  $20=2V_4-V_3$ 

Sum currents at node 2:  $I_4 + I_2 = I_5$   $\frac{10 - V_2}{5} + \frac{V_4 - V_2}{2} = \frac{V_2}{5}$   $20 - 2V_2 + 5V_4 - 5V_2 = 2V_2$   $20 = -5V_4 + 9V_2$ 

Sub  $V_2$  from eqn. ① into ②  $20 = -5V_4 + 9(2V_4 - 20)$   $20 = 13V_4 - 180$ or  $V_4 = \frac{200}{13} = 15.4 \text{ V}.$ 

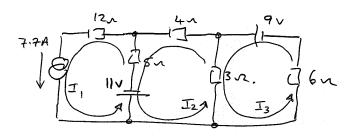
Using 
$$U = \frac{2 \times 200}{13} - V_2$$
.  

$$260 = 400 - 13V_2$$

$$-140 = -13V_2$$

$$V_2 = \frac{140}{13} = 10 \text{ and a bit.}$$

In 1s the current through top 50 from left to right. ( I4 in the cet diag about.)  $I_{1} (= I_{4}) = 10 - V_{2}$   $= 10 - \frac{140}{13}$   $= \frac{130 - 140}{5 \cdot 13} = -\frac{2}{13}$  = -0.154 A



I, = 7.7A

Sum voltages around loop Iz

$$11 - (I_2 - I_3) 3 - I_2 4 - (I_2 - I_1) 5$$
or 
$$11 - 12 I_2 + 3 I_3 + 38.5 = 0$$

or 
$$11 - 12I_2 + 3I_3 + 38.5 = 0$$
.  
Sum voltages around loop  $I_3$   
 $9 - (I_3 - I_2)3 - I_36 = 0$   
 $9 - 9I_3 : 2I_2 - 0$   
 $3 - 3I_3 + I_2 = 0$