Examination Feedback for EEE6394 - Compound Semiconductor Device Manufacture Spring Semester 2009-10

Feedback for EEE6394 Session: 2009-2010

<u>Feedback:</u> Please write simple statements about how well students addressed the exam paper in general and each individual question in particular including common problems/mistakes and areas of concern in the boxes provided below. Increase row height if necessary.

General Comments:

Students thoroughly enjoyed the course as evidenced by excellent attendance and level of engagement.

Question 1:

Good attempts at describing a compound semiconductor growth technique, which were generally factually correct. Marks were lost through poor referencing, reliance on only one text book, inadequate essay structure and poor figure captions. Brief examples of specific material systems were also poorly addressed.

Question 2:

Good attempts at reporting the data handling and extraction of important laser parameters. Marks were lost through poor report structure, important omissions such as introduction to the materials investigated and how they were prepared. Also of note was the poor choice of electroluminescence resolution.

Question 3:

Strong attendance and engagement in the practical sessions. Some students found it difficult to link photolithography theory to practice.