

EEE118: Electronic Devices and Circuits

Lecture VII

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Review

- 1 Finished looking at the **peak detector** circuit.
- 2 Considered the use of the **peak detector** in **AM radio demodulation**
- 3 Introduced and described the operation of the diode **voltage clamp** circuit. The clamp can be broken down into,
 - $0^\circ - \sim 180^\circ$ - Diode not conducting, negligible charging of C through R.
 - $180^\circ - \sim 270^\circ$ - Diode conducts C charges through D.
 - $270^\circ - \sim 360^\circ$ - Diode not conducting, small discharge current flows from C through R.
 - All following cycles - Diode briefly conducts around $\sim 270^\circ$ to recharge C
- 4 Connected a **peak detector** to a **voltage clamp** to form a **peak to peak detector**

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Review Continued

- Considered the **equilibrium condition** that defines how ideally the **peak to peak detector** acts as a **voltage doubler**
- Developed a three stage voltage doubler and looked at the voltage on the clamp and peak detector at each stage.
- Observed that the voltage doubler is often drawn as a ladder in which capacitors are the "edges" and diodes are the "rungs"

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Outline

- 1 Simulation Programs
 - SPICE
 - QsapecNG
- 2 Rectifiers: Linear Power Supplies
- 3 Single and Three Phase Supply
- 4 Half Wave Rectifier
- 5 Capacitive Smoothing
 - Choosing a Capacitor
- 6 Review
- 7 Bear

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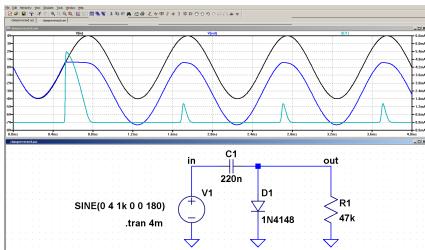
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Simulation Programs

SPICE

Simulation Program with Integrated Circuit Emphasis

An open source **numerical** circuit simulation tool that was developed at the University of California. Linear technology released LTSpice
<http://www.linear.com/designtools/software/#LTspice>
Example files from this course will be available on
<http://hercules.shef.ac.uk/eee/teach/resources/eee118/eee118.html>



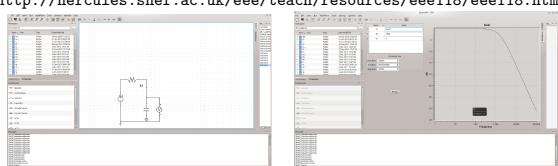
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Simulation Programs

QsapecNG

Qt-based Symbolic Analysis Program for Electric Circuits (New Generation)

An open source **analytical** (i.e. symbolic) linear circuit simulation tool that is being developed at the University of Florence
<http://qsapecng.sourceforge.net/>
Example files from this course will be available on
<http://hercules.shef.ac.uk/eee/teach/resources/eee118/eee118.html>



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Linear Power Supplies

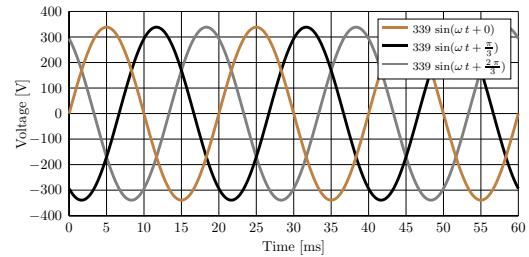
Rectification

Conversion of AC voltage into a unipolar voltage that is usually of the form of a DC component with a superimposed AC component.

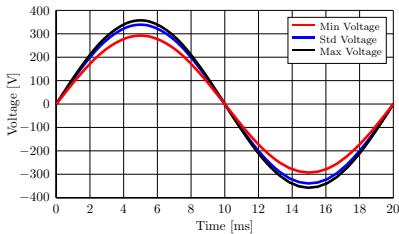
- The DC component is always the **average value** of the rectifier output voltage and the superimposed AC component, which is rarely sinusoidal, is called the **ripple voltage**.
- In this course the ripple voltage will always be the **peak to peak** value of the superimposed AC component.

Rectifier circuits are often divided into two categories **half wave** and **full wave**. In fact, full wave circuits can be looked at as two or more half wave circuits connected together.

Other descriptive terms applied to rectifier circuits are **single phase** and **three phase**. These terms relate to the nature of the AC power supply. Most medium/heavy industry (kilowatts and above) is supplied by a three phase AC power source while light industrial applications are more likely to be supplied by a single phase source. Domestic dwellings are supplied by a single phase source. **Only single phase circuits are examinable.**

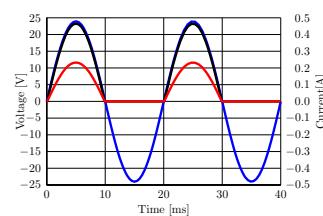
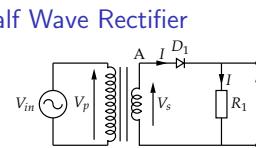


Single phase in the UK is 240 V RMS, 50 Hz. The standard gives 230 V $\pm 10\%$ at 50 Hz ± 0.5 Hz (BS 7671)^{1,2}. In practice 230 V is quoted so the UK conforms with western Europe, but since 240 V falls within the permissible limits the UK has not changed its generation voltage.



¹Kitcher, C., *Practical guide to inspection, testing and certification of electrical installations : conforms to 17th edition IEE Wiring Regulations (BS 7671:2008) and Part P of Building Regulations*, Oxford: Newnes, 2009, 621.31924 (K)

²<http://www.tlc-direct.co.uk/Book/1.1.htm>



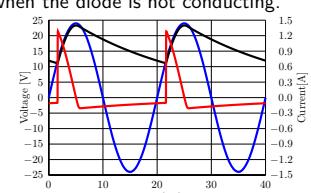
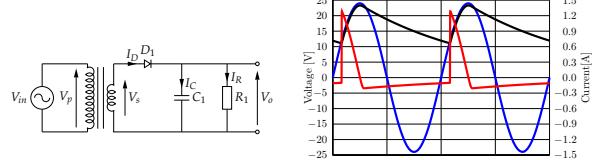
Red: I , Black: V_o , Blue: V_{in}

- The diode conducts when the voltage at A is more than 0.7 V positive with respect to B.
- Positive half-cycles of the secondary winding current pass through the diode. Negative half-cycles are blocked.
- If the orientation of the diode was reversed the negative half cycles would pass and the positive ones would be blocked.

- There is a limited range of power supply applications in which this wave shape is acceptable.
- Most electronic equipment requires a relatively smooth supply voltage that approximates continuous DC (e.g. a battery)
- V_o for the half-wave rectifier is unipolar (no negative part, only positive in this case).
- The average value (the DC component) is positive, but the ripple (AC component) is large.
- For the circuit to be useful as a DC power source for most electronic circuits, the output may be *smoothed* to reduce the amplitude of the AC component or "ripple voltage"
- There are various ways in which smoothing can be achieved. Each requires the storage of energy so that it can be redistributed more evenly across the cycle - essentially filling in the gaps.

Capacitive Smoothing

The simplest method is to use a capacitor in parallel with the load resistance to store charge when the diode is conducting, and deliver charge to the resistor when the diode is not conducting.

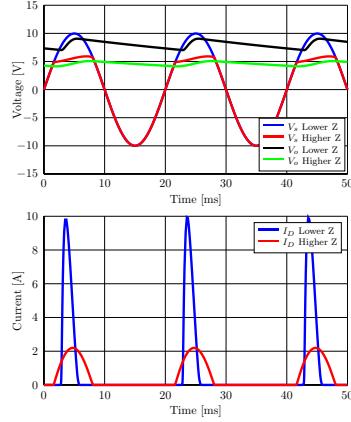


This is a peak detector circuit. C is charged in the vicinity of the peak of every positive half cycle and provides current for the load in between the positive peaks. Note the remaining ripple (somewhat exaggerated in the diagram) is a good approximation to a triangle - this will simplify calculations later.

The current waveform has large amplitude charging pulses of relatively short duration. The peak pulse amplitude can be many times greater than the load current. These may be troublesome for several reasons,

- 1 The rapid dI/dt can cause radiative interference problems (EMI) - more in EEE6010.
- 2 The external supply is delivering most of the energy used at one point in the cycle. This can set up impulsive mechanical loads in the utility machinery.
- 3 These pulses develop proportionately high $I^2 R$ losses, unnecessarily heating the circuit, and lowering efficiency.

Fortunately in most situations the series impedance associated with transformers and the utility supply mean that the impulsive currents are not as troublesome as they would be under ideal conditions.



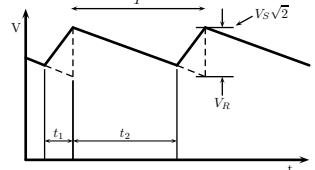
These graphs show voltage and current for the secondary V_s and the output V_o for a small transformer. The series impedance has been exaggerated somewhat in the graphs to make the effect clear. Note that the ideal secondary voltage is $V_s \sqrt{2} \sin(2\pi f_s t)$ neither of the shown V_s lines agree perfectly with the ideal, but the data representing the lower impedance situation comes quite close.

- The series impedance means that a large impulsive charging current is no longer possible.
- The resulting green output voltage waveform is somewhat lower in voltage than the nearly ideal black case. Because C_1 can not charge quickly enough the charging pulses are broader and smaller in amplitude - and area - to the ideal case.
- Note that the ripple voltage is still triangular in shape.
- Manufacturers do not give figures for the impedance of a transformer. If this information was available it would be of little value as the calculations involved are taxing. **Regulation** information is available, but it relates to the transformer only. Generally bigger VA leads to better regulation.
- The output voltage from a simple linear power supply with capacitor smoothing is poorly defined, will often be lower than ideal estimates and will be a function of the load resistance.

Choosing a Capacitor to Meet a Ripple Specification

Assumptions:

- 1 The ripple is triangular
- 2 The capacitor discharges throughout the charging cycle and charges instantaneously at the peak of each charging cycle
- 3 The transformer and power source are ideal (i.e. zero series impedance)
- 4 The load current is constant
- 5 Discharge occurs for the whole interval between charging peaks



Using the instantaneous charging model the voltage across C_1 reduces at a constant rate over the interval T as the load current I_R is drawn from C_1 .

$$Q = C V, I = \frac{dQ}{dt} = C \frac{dV}{dt} = C \frac{V_R}{T}$$

Since this is a half-wave rectifier $T = \frac{1}{f_s}$ where f_s is the supply frequency (UK: 50 Hz, USA: 60 Hz JPN: both!). If the load is purely resistive (as shown several slides ago) then,

$$I_L = \frac{\sqrt{2} V_s}{R_L}$$

For example

A half wave rectifier power supply is loaded by a power amplifier which is driving an AC machine. The load can modelled by a 5Ω resistance. Find the ripple voltage if the smoothing capacitor bank has a total capacitance of $68,000 \mu F$ and the transformer secondary voltage (V_s) is 80 V RMS.

Solution

Find the peak load current,

$$I_L = \frac{\sqrt{2} V_s}{R_L} = \frac{\sqrt{2} \cdot 80}{5} = 22.6274 \text{ A} \quad (1)$$

Find the time over which the smoothing capacitor is discharged (the period of the waveform)

$$T = \frac{1}{f_s} = \frac{1}{50} = 20 \text{ ms} \quad (2)$$

Transpose the capacitor equation $I = C (dV/dt)$ to obtain the solution.

$$I_L = C \frac{V_R}{T}, V_R = \frac{I_L T}{C} = \frac{22.6274 \cdot 0.02}{68 \times 10^{-3}} = 6.66 \text{ V} \quad (3)$$

In many cases this is an overestimate. But an overestimate is more desirable than an underestimate!

Review

- 1 Noted the existence of two useful - in terms of designing circuits and understanding them - programs for numerical and analytical simulation of electronic circuits.
 - LTSpice - Numerical simulation of linear and non-linear circuits
 - QsapecNG - Analytical (i.e. algebraic) simulation of linear circuits
- 2 Introduced the idea of linear power supplies
- 3 Briefly discussed some differences between single phase and three phase supplies
- 4 Considered the half wave rectifier in detail
- 5 Used a peak detector circuit to smooth the output voltage of the peak detector
- 6 Developed a simple model to find a suitable capacitor value for a power supply filter

