

Examination Feedback for EEE307/6023 – Power Electronics  
Spring Semester 2011-12

**Feedback for EEE307/6023 Session: 2011-2012**

**Feedback:** Please write simple statements about how well students addressed the exam paper in general and each individual question in particular including common problems/mistakes and areas of concern in the boxes provided below. Increase row height if necessary.

**General Comments:**

The students taking this paper seems less well prepared than in previous years, appearing to rely on spotting questions from previous papers to get through. The questions on this paper were very similar to previous papers, but the format was slightly different, and this seemed to cause problems with students who could not correctly interpret the question

**Question 1:**

Straightforward question on the boost converter which is the middle section in the diagram. Despite there being plenty of previous examples as to how the values were calculated, a number of students failed to get the correct values.

**Question 2:**

Question 2 was a H bridge inverter operating in quasi square mode. A number of students managed to misinterpret this as a phase controlled rectifier for some reason

**Question 3:**

Question on a phase controlled rectifier, despite explaining in some detail the differences between diodes and thyristors, a number of student failed to realize that a full diode bridge will also suffer from commutation volts drop. This question is very similar to one used before.

**Question 4:**

Typical snubber question, many people drew the wrong snubber, and explained it badly.