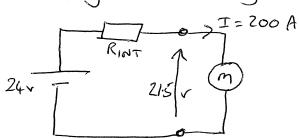
(a) Initially the battery is cornected to the starter roter:



The voltage drop across the internal resistance is:

VINT = 24-21.5 = 2.5V

Hence the internal resistance of the bothery is!

 $R_{INT} = \frac{V_{INT}}{I} = \frac{2.5}{200} = \frac{0.0125 \Lambda}{}$

The power denipoted is the bothery is then - PINT = IZRINT = 2002, 0.0125 = 500W

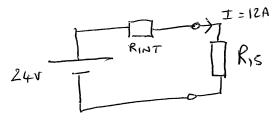
The total power supplied by the bodlery is:

PTOT = 24 × 200 = 4800 W

Hence the repter efficiency is:

Efficieny = PTOT - PLOSS X 100= 4800 - 500 = 89.6%.

(b) When the heater is first witched on it has a terestone of R15 and at the final temperature it will be RF.



$$\frac{V}{I} = R_{INT} + R_{IS} = \frac{24}{12} = 2\Lambda$$

Hence Ris = 2 - 0.0125 = 1.9881

Similarly at the final temperature:

$$R_F + R_{INT} = \frac{V}{I} = \frac{24}{9} = 2.667 \Omega$$

Hence RF = 2.667-0.6125 = 2.655a

Now
$$\frac{R_F}{R_{20}} = \frac{R_0}{R_0} \frac{(1+\alpha_0 T_F)}{(1+\alpha_0)}$$

$$\frac{2.655}{1.988} = \frac{\left(1 + 12.5 \times 10^{-3} T_F\right)}{\left(1 + 12.5 \times 10^{-3} \times 15\right)}$$

$$1.336 = (1 + 12.5 \times 10^{-3} T_F)$$

$$1.1875$$

(ii) At the final temperature the Power is $I^2R_F = 9^2 \times 2.655$ = 215 W

Hence the efficiency =
$$\frac{215}{24\times9} \times 100\% = \frac{99.5\%}{}$$

(iii) When both the heater and starter are operated together.

From part (a) Rm = 21.5 = 6.1675 12

Hence current is
$$\frac{24}{0.1175} = \frac{209.6A}{1.1175}$$

Since
$$\frac{V_P}{V_S} = \frac{N_P}{N_S}$$
 \Rightarrow $V_S = V_P \cdot \frac{N_S}{N_P} = \frac{200 \times S}{1} = \frac{1000 \text{ V}_{rns}}{1}$

$$I_s = \frac{V_s}{R_s} = \frac{1000}{40} = 25 A_{rms}$$

NOW
$$\frac{IP}{IS} = \frac{Ns}{Np} \implies IP = \frac{Ns Is}{NP} = \frac{5 \times 25}{1} = \frac{125 A cms}{1}$$

(ii) The recorday impedance is NOW:

$$R+j.2\pi fL = 40+j2\pi\times50\times0.1S = 40+j47.1\Omega$$
= 61.8 L49.7° L

Power dinipated in load = $I_s^2 \times 40 = 1618^2 \times 40 = 10.47 \text{ kW}$ (check $P = V_p I_p \cos \phi = 200 \times 80.9 \times \cos (-49.7) = 10.47 \text{ kW}$)

- (iii) The input power factor would be Cos (-49.7°) = 0.647 lagging

 The VA rating is 200 × 80.9 = 16.18 kVA
- (iv) Since Vms = 4.44f Nopman

The impedance of the cool is given by:

$$Z = R + j2\pi fL = 30 + j.2\pi.50.0.1$$

= $30 + j31.42 = 43.44 \angle 46.3^{\circ} \Omega$

(ii)The current flowing in the coil is:

$$T = \frac{VL0^{\circ}}{ZL0^{\circ}} = \frac{400L0^{\circ}}{43.44L46.3^{\circ}} = \frac{9.21L-46.3^{\circ}A}{43.44L46.3^{\circ}}$$

The real power is: (iii)

$$P = VICO \phi = 400 \times 9.21 \times cos 46.3^{\circ}$$

= $\frac{2545W}{}$

The power-factor is cosp = cos (-46:3) = 0.69 lagging

- FACTORY (i) VA rating = $\frac{\rho}{cep\phi} = \frac{160}{0.8} = \frac{200RVA}{0.860}$
 - (ii) KVAr rating = S sing = 200,0.6 = 120kVAr
- Magnétude of current = $\frac{S}{11} = \frac{200 \times 10^3}{3.3 \times 10^3} = 60.61 \text{ A}$

For the heating evens! (C)

For the motor load:

$$P_{12}^{2}$$
 S cos $\phi = 100 \times 10^{3} \times 0.75 = 75 \text{ kW}$
 Q_{12}^{2} S sin $\phi = 100 \times 10^{3} \times \sin(\cos^{-1}0.75) = 66.14 \text{ kVAr}$

(i) Therefore the total factory load is:

$$P_{T} = P_{F} + P_{L1} + P_{L2} = 160 + 50 + 75 = 285 kw$$

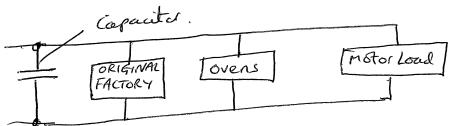
$$Q_{T} = Q_{F} + Q_{L1} + Q_{L2} = 120 + 0 + 66.14 = 186.14 \text{ kVAr}$$

$$Q_T = W_F \cdot q_L$$

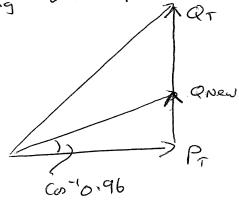
VA rating is $\int_{T_T}^{T_2} + Q_T^2 = \int_{Z_T}^{Z_T} \frac{340.4 \text{ kVA}}{2}$

(ii) Phase angle = aton Q_T = aton $\frac{186.14}{285}$ = 33.15° lagging

(d) (i) Capacitor is cornected a cross the total boad is prallel?



(ii) Adding the capacitar does not change P.T. Since P.J. needs to be 0.96 lagging - draw power triangle.



QNew = Pt ton (cos-10.96) = 285, ton 16.26°=83.12kVAr Here copacitor much provide:

Current drawn by the capacitor:

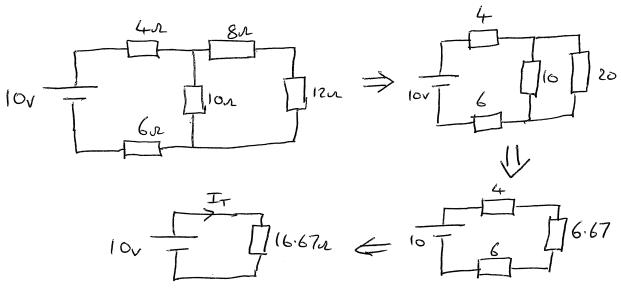
$$T_c = \frac{103.02 \times 10^3}{3300} = \frac{31.22 \text{ Arms}}{3}$$

Since
$$Q_c = \frac{V_c^2}{X_c} \Rightarrow X_c = \frac{V_c^2}{Q_c} = \frac{3300^2}{103.02 \times 10^6}$$

and
$$C = \frac{1}{2\pi f \times c} = \frac{30.1 \mu F}{2\pi .50 \times 105.7} = \frac{30.1 \mu F}{2\pi .50 \times 105.7}$$

(d)(iii) The peak voltage the capacitor reest withstead is $V_{DV} = 3300 \times \sqrt{2} = 4667 \text{ V}$

(a) First consider the 10 v Source:

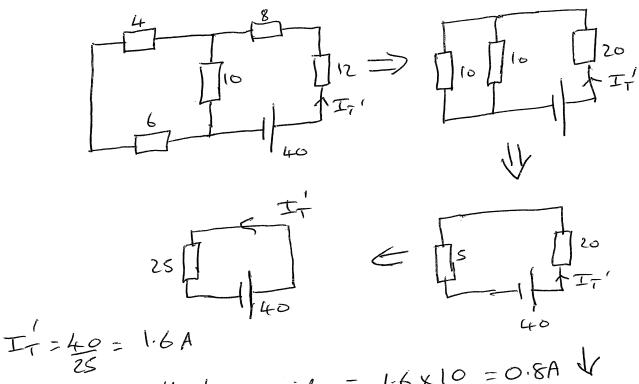


$$T_T = \frac{10}{16.67} = 0.6A$$

,. Current through the Or renistor is:

I trough the (Ost remoter is.)
$$T_{10a} = T_7 \cdot \frac{20}{(10+20)} = 0.6 \times \frac{2}{3} = 0.44 \text{ V}$$

Now comider the LOV Supply:



: Current through the los revistor = 1-6 × 10 = 0.8A V

: Total current through the 10st seriestor is:

(b) For the Therenin circult we need the open circult voltage. This is effectively the voltage across the 12st revistor. Using working from part (a).

For the lov source the current through the 12th remitor

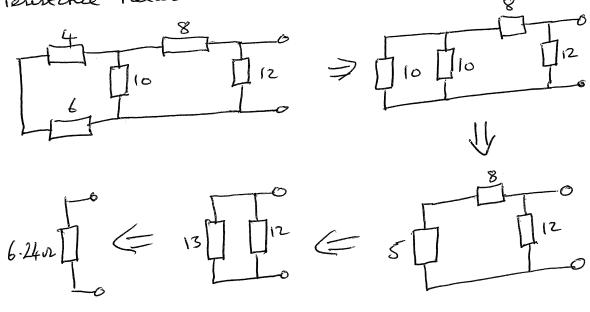
$$I_{12a} = I_T \times \frac{10}{30} = 0.2A \sqrt{}$$

For the 40v Source the current through the 12d remitor is equal to IT' = 1.6A 1

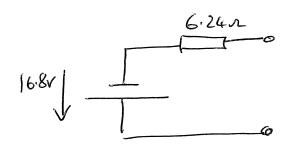
Hence the total current through the 12cr revistor is:

Here the Therenin voltage is 1.4 x 12 = 16.8VV

The resistance network is:



Hence the Therenin circult is:



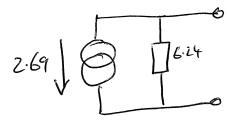
When the load is corrected the current is:

$$T_L = \frac{16.8}{(6.24+20)} = 0.64 A$$

Hence the power descripated is the load is !

(c) The Norton circuit can be found directly from the Therenin circuit.

$$I_N = \frac{E_T}{R_T} = \frac{16.8}{6.24} = 2.69A$$



- (d) At steady state $\frac{dI}{dt} = 0$ hence $V_{Li} = 0$ and C_i is fully charged. The certaint is therefore governed by the 2 resistors.
 - (i) Applying Kirchaf's land. (Use defined direction of Is)

$$T_s = -2A$$

= O.Z.J

The voltage across the capacitar is equal to the voltage across $R_2 = 20 \times 2 = 40v$

: Energy stored in the capacitor, C, = 1 CV2 = 1 x 1 x 10^3 x 402 = 0.8J

(iii) Total power denipoted in the would is: $P_7 = I_S^2 (R_1 + R_2) = 2^2, 30 = 1200$

EEE123 Solutions to Q4 to Q6 - 2014/15

4.

a) Load current is simply given by:

$$I_L = \frac{V_{Dc}}{R_L} = \frac{150}{60} = 2.5A$$

The load power is given by:

$$P_L = \frac{V_{DC}^2}{R_L} = \frac{150^2}{60} = 375W$$

Or alternatively,

$$P_L = I_L^2 R_L = 2.5^2 \times 60 = 375W$$

b) The minimum base current required to support this load is:

$$I_{B(min)} = \frac{I_L}{h_{FE}} = \frac{2.5}{125} = 20mA$$

This minimum value of base current is also given by:

$$I_{B(min)} = \frac{V_i - V_{BE}}{R_{B(max)}}$$

Rearranging yields:

$$R_{B(max)} = \frac{V_i - V_{BE}}{I_{B(min)}} = \frac{5 - 0.7}{0.02} = 215\Omega$$

- **c)** In practice, it would be wise to adopt value some 2-3 time smaller to allow for device variation, different operating conditions, including for example, temperature rise of the device.
- **d)** When the BJT transistor is fully turned on, (V_{ce}) is 1.15V. Hence the power loss in the transistor is simply:

$$P_{loss} = V_{ce}I_C = 1.15 \times 2.5 = 2.87W$$

e) For a MOSFET, the power loss is given by:

$$P_{loss} = I_L^2 R_{DS \ on}$$

Equating this to the loss for the BJT yields:

$$R_{DS\ on} < \frac{2.87}{2.5^2}$$

Hence

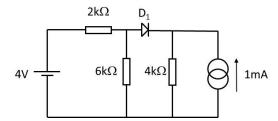
$$R_{DSon} < 0.46\Omega$$

f) Other advantages:

- Eliminates need for a base resistor
- Easier to drive in terms of current rating of control side electronics (notwithstanding transient)
- Lower loss on control side

5.

a)



Assume the diode is conducting and replace the diode with a +0.7V voltage supply with is positive at the anode. Replace the current source and its parallel resistance by a Thevenin equivalent and apply a Thevenin simplification to the voltage source and the $2k\Omega$ and $6k\Omega$ resistors.

For the voltage source and $2k\Omega$ and $6k\Omega$ resistors:

$$Open-circuit\ voltage\ =\ \left(\frac{6}{6+2}\right)\times 4=3V$$

Short – circuit current =
$$\frac{4}{2 \times 10^3} = 2mA$$

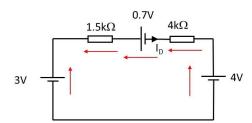
Hence
$$R_{TH} = 1.5k\Omega$$

For the current source and shunt resistor:

$$Open-circuit\ voltage\ =\ 1\times 10^{-3}\times 4\times 10^3=4V$$

$$Short - circuit current = 1mA$$

Hence
$$R_{TH} = 4k\Omega$$



$$3 - 1500I_D - 0.7 - 4000I_D - 4 = 0$$

Hence, I_D is given by:

$$I_D = \frac{-1.7}{5500} = -0.31 mA$$

Since this current is negative, then the original assumption was incorrect, and hence the diode is non-conducting in this circuit.

This can also be solved by nodal analysis.

b) The peak AC voltage is 162.6V

Including the two on-state diode voltage drop reduces this to 161V – neglecting these diode drops does not result in any meaningful difference in this case.

$$V_{ave} = \frac{2V_p}{\pi} = \frac{2 \times 161}{\pi} = 102.5V$$

c) It is necessary to calculate Vrms applied to the load (and not use Vave):

$$V_{rms} = \frac{V_p}{\sqrt{2}} = \frac{161}{\sqrt{2}} = 113.8V$$

(slightly lower than supply due to the on-state voltage drop)

Hence, average power delivered to load:

$$P_L = \frac{V_{rms}^2}{R_L} = \frac{113.8^2}{40} = 324W$$

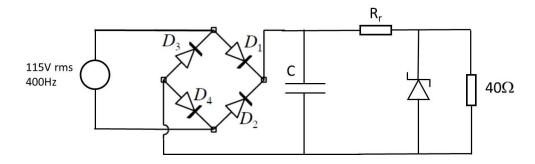
d) With a smoothed output (nominally around 161V), a slight overestimate of the load current is given by:

$$I_L = \frac{161}{40} = 4.02A$$

Assuming that the current falls linearly during the discharge period, then a slight overestimate of the capacitance is given by:

$$C_{min} = \frac{I}{dV/dt} \approx \frac{I}{\Delta V/\Delta t} \approx \frac{4.02 \times 0.00125}{6} = 0.839 mF$$

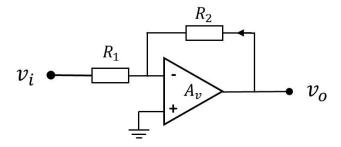
e)



The regulator resistor R_r drops the appropriate voltage between the capacitor voltage and the load which is determined by the total current flowing.

6.

a)



Assumptions:

- The op-amp gain $A_v \to \infty$
- The op-amp has infinite input resistance

In order to determine the effective gain of this circuit, we start by summing the currents into the v^- node:

$$i_i + i_f = 0$$

(since an op-amp has close to infinite input impedance, i.e. no current flows into the op-amp input)

This can be written as:

$$\frac{v_i - v^-}{R_1} + \frac{v_o - v^-}{R_2} = 0$$

But from the discussion above $v^- = 0$ (because it is a virtual earth) and so:

$$\frac{v_i}{R_1} + \frac{v_o}{R_2} = 0$$

Hence, the effective gain is given by:

$$\frac{v_o}{v_i} = -\frac{R_2}{R_1}$$

b) To get an input resistance of $10k\Omega$, then $R_1=10k\Omega$ and hence:

$$R_2 = -gain \times R_1 = 100 \times 10,000 = 1M\Omega$$

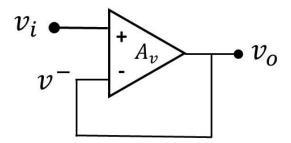
c) With a source impedance of $2k\Omega$, there is a potential divider action at play in setting v_i at the input stage. Specifically:

$$v_i = \frac{10,000}{10,000 + 2,000} v_s$$

Where v_s is the

Hence the actual voltage present at v_i is only 83.3% of the source voltage, i.e. a 16.7% error.

d) This can be remedied by including a unity buffer which has an input impedance close to that of the op-amp itself and a low output impedance (10s of Ω). The circuit diagram is:



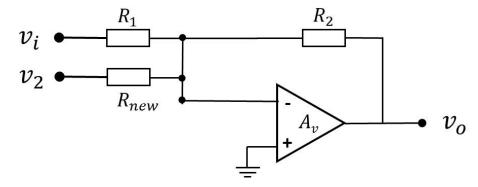
[Important to get correct polarity of input terminals]

e) With the finite op-amp gain:

$$\frac{v_o}{v_i} = -\frac{\frac{R_2}{R_1 + R_2}}{\frac{1}{A_v} + \frac{R_1}{R_1 + R_2}} = 99.8$$

i.e. 0.2% change in effective gain.

f) The modified circuit (which is in effect a summer) is:



Assuming R_2 remains fixed at 1M Ω , then the new resistor is 20k Ω to give a gain of -50.