

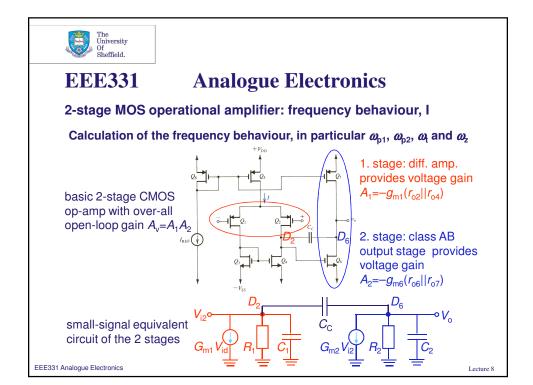
# **EEE331** Analogue Electronics

#### 8th lecture:

- operational amplifiers (Op-Amps), part II
  - frequency behaviour of 2-stage MOS Op-Amps
  - multi-stage Op-Amps
  - feedback theory

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Lecture 8





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2-stage MOS operational amplifier: frequency behaviour, II

Calculation of the frequency behaviour, in particular  $\omega_{b1}$ ,  $\omega_{b2}$ ,  $\omega_{1}$  and  $\omega_{2}$ 

1. stage: transconductance:  $G_{m1}=g_{m1}=g_{m2}$ output resistance:  $R_1 = r_{o2} || r_{o4}$ 

capacitance:  $C_1 = C_{DG2} + C_{DB2} + C_{DG4} + C_{DB4} + C_{GS6}$ 

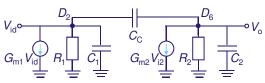
This is the total capacitance at the interface between the two stages.

2. stage: transconductance:  $G_{m2}=g_{m6}$ output resistance:  $R_2 = r_{o6} || r_{o7}$ 

 $C_2 = C_{DG6} + C_{DB6} + C_{DG7} + C_{DB7} + C_{L}$ capacitance:

This is the total capacitance at the output node of the Op Amp and usually dominated by the load capacitance  $C_L$ , while  $C_{DG6} << C_C$ .

small-signal equivalent circuit of the 2 stages



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2-stage MOS operational amplifier: frequency behaviour, III

Calculation of the frequency behaviour, in particular  $\omega_{\!_{p1}},\,\omega_{\!_{p2}},\,\omega_{\!_{l}}$  and  $\omega_{\!_{z}}$ 

equation for currents at node  $D_2$ : equation for currents at node  $D_6$ : solve  $2^{nd}$  equation for  $V_{i2}$ :

insert  $V_{i2}$  into 1<sup>st</sup> equation to get:

 $G_{\text{m1}}V_{\text{id}}+V_{\text{i2}}/R_1+sC_1V_{\text{i2}}+s\dot{C}_{\text{C}}(\dot{V}_{\text{i2}}-V_{\text{o}})=0$  $G_{\text{m2}}V_{\text{i2}}+V_{\text{o}}/R_{\text{2}}+sC_{\text{2}}V_{\text{o}}+sC_{\text{C}}(V_{\text{o}}-V_{\text{i2}})=0$  $V_{i2} = V_o (1/R_2 + sC_2 + sC_C)/(sC_C - G_{m2})$ 

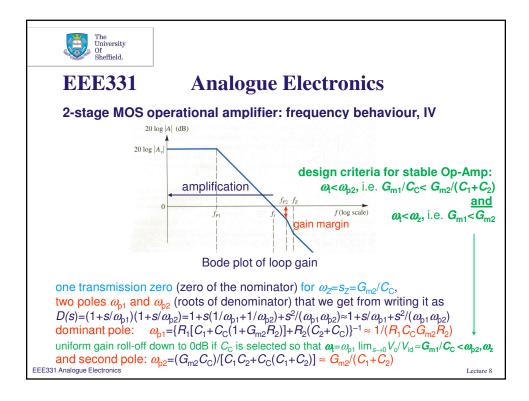
 $V_{\rm id}/V_{\rm o} = \{sC_{\rm C}/G_{\rm m1} - [(1/R_2 + sC_2 + sC_{\rm C})]\}$  $\times (1/R_1 + sC_1 + sC_C)]/[(sC_C - G_{m2})G_{m1}]$ 

then re-arrange to get voltage gain and sort according to powers of s:  $V_0/V_{id} = [G_{m1}(G_{m2} - sC_C)R_1R_2] / \{1 + s[C_1R_1 + C_2R_2 + C_C(R_1 + R_2 + G_{m2}R_1R_2)]$ 

 $+s^2 R_1 R_2 [C_1 C_2 + C_C (C_1 + C_2)]$ 

is a transfer function with  $A_{DC}=\lim_{s\to 0} V_o/V_{id}=(G_{m1}R_1)(G_{m2}R_2)$ , as expected, one transmission zero (zero of the nominator) for  $\omega_Z = S_Z = G_{m2}/C_C$ , two poles  $\omega_{p1}$  and  $\omega_{p2}$  (roots of denominator) that we get from writing it as  $D(s) = (1 + s/\omega_{p1})(1 + s/\omega_{p2}) = 1 + s(1/\omega_{p1} + 1/\omega_{p2}) + s^2/(\omega_{p1}\omega_{p2}) \approx 1 + 1 + s/\omega_{p1} + s^2/(\omega_{p1}\omega_{p2})$ dominant pole:  $\omega_{p1} = \{R_1[C_1 + C_C(1 + G_{m2}R_2)] + R_2(C_2 + C_C)\}^{-1} \approx 1/(R_1C_CG_{m2}R_2)$ 

note Miller enlargement of  $C_{\rm C}>>C_1$  in neg. feedback of  $2^{\rm nd}$  stage whose gain is  $G_{\rm m2}R_2$  and second pole:  $\omega_{\rm p2}=(G_{\rm m2}C_{\rm C})/[C_1C_2+C_{\rm C}(C_1+C_2)]\approx G_{\rm m2}/(C_1+C_2)$ 

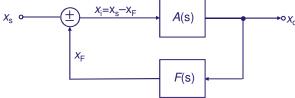




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formal feedback theory, I: loop gain and total gain



Consider a **non-inverting** amplifier where  $x_s$  can be either a voltage or a current signal to be amplified. A(s) denotes the -frequency dependent-amplification factor and F(s) the feedback. Then we have:

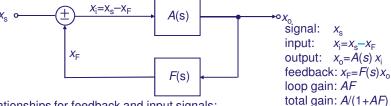
for large loop gain A(s)F(s)>>1:  $G\approx A/(AF)=1/F(s)$  depends on feedback, rather than on the actual amplifier. This is a useful concept for all systems with finite input and zero output resistance (i.e. no load effects).

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#### formal feedback theory, II



Relationships for feedback and input signals:

 $x_F = F(s)A(s)(x_S - x_F) -> x_F = AF/(1 + AF) x_S$  $x_i = x_s - F(s)A(s) x_i \rightarrow x_i = 1/(1 + AF) x_s$ 

is reduced in the amplifier with negative feedback (to almost zero for large negative feedback)

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#### formal feedback theory, III: gain de-sensitivity factor



calculation of the result of change of gain:

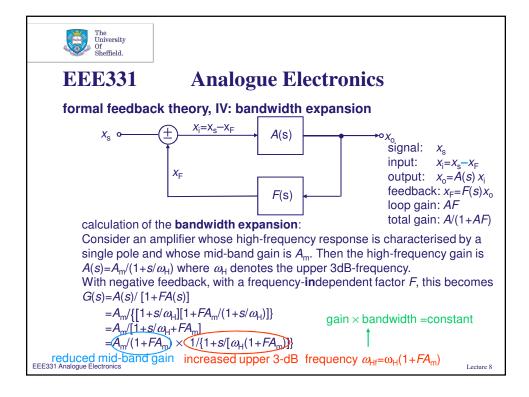
G=A/(1+AF)

-> 
$$dG/dA = [1 + AF - A(F + A dF/dA)] / (1 + AF)^2 = 1/(1 + AF)^2$$
  
=0 for constant F

 $-> 1/G dG/dA = 1/(1+AF)^2 (1+AF)/A = 1/(1+AF) 1/A$ 

-> dG/G = [1/(1+AF)] dA/A

The percentage change in total gain *G* due to variations in some circuit parameter is thus smaller than the percentage change in A by a factor of (1+AF). This is known as the **de-sensitivity factor.** 

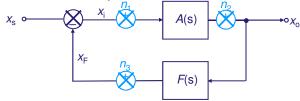




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formal feedback theory, V: influence of disturbances



consider influence of disturbances due to additive noise:

The effect will depend on where in the circuit the noise is added: output due to  $n_1$ :  $x_n = A(x_s - x_F + n_1) = A(x_s - Fx_n + n_1) = A/(1 + AF)x_s + A/(1 + AF)n_1$ 

output due to  $n_2$ :  $x_0 = A(x_s - x_F) + n_2 = A(x_s - Fx_0) + n_2 = A/(1 + AF)x_s + 1/(1 + AF)n_2$ 

output due to  $n_3$ :  $x_0 = A(x_s - x_F) = A(x_s - Fx_0 - n_3) = \underbrace{A/(1 + AF)x_s}_{\text{signal}} - \underbrace{A/(1 + AF)n_3}_{\text{noise}}$ 

Disturbance at the input and in the feedback loop are amplified by the same amount as the signal, disturbances at the output end are suppressed.

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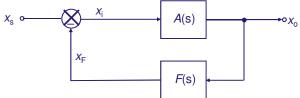
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formal feedback theory, VI: stability vs. oscillation



The closed-loop transfer function G(s)=A(s)/[1+A(s)F(s)] is a complex function for physical frequencies  $s=j\omega$ :  $G(j\omega)=A(j\omega)/[1+A(j\omega)\ F(j\omega)]$ , and so is the loop gain  $A(j\omega)\ F(j\omega)=|A(j\omega)\ F(j\omega)|$  exp  $j\phi(\omega)$  If the loop gain  $|AF|\ge 1$  for any frequency where the phase shift around the loop (i.e. between  $x_f$  and  $x_s$ ) is  $0^\circ$  or  $360^\circ$ , then the system will become unstable. The **inverting summer** which subtracts  $x_F$  from  $x_s$  at the input (i.e. negative feedback) **already introduces a 180^\circ phase shift** between these signals. Another  $180^\circ$  phase shift will make the feedback positive, and the circuit will start to oscillate. Hence, the **phase lag for stable amplification must be**  $0 < |\phi(\omega)| < 180^\circ$  for all frequencies with  $|A(\omega)F(\omega)| \ge 1$ , i.e. magnitude  $\ge 0$ dB.

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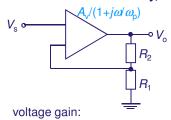
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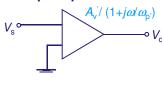


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formal feedback theory, VII: single-pole Op-Amps





$$G = V_{o}/V_{s} = \frac{A_{v}/(1+j\omega'\omega_{p})}{1+[A_{v}/(1+j\omega'\omega_{p})]} \frac{R_{1}/(R_{1}+R_{2})}{R_{1}/(R_{1}+R_{2})}$$

$$= A_{v}/[1+j\omega'\omega_{p}+A_{v}R_{1}/(R_{1}+R_{2})]$$

$$= A_{v}/[1+A_{v}R_{1}/(R_{1}+R_{2})+j\omega'\omega_{p}]$$

$$= \frac{A_{v}/[1+A_{v}R_{1}/(R_{1}+R_{2})]}{1+j\omega'\{\omega_{p}[1+A_{v}R_{1}/(R_{1}+R_{2})\}}$$

 $\equiv A_{\rm v}'/(1+j\omega/\omega_{\rm p'}) \qquad \text{where} \\ A_{\rm v}' = A_{\rm v}[1+A_{\rm v}R_1/(R_1+R_2)] \approx (R_1+R_2)/R_1 \\ \text{for } A_{\rm v}R_1/(R_1+R_2) >> 1 \\ \text{and} \\ \omega_{\rm p}' = \omega_{\rm p}[1+A_{\rm v}R_1/(R_1+R_2)] \\ \end{cases}$ 

Note again  $A_v' \omega_n = A_v \omega_n = \text{constant}$ 

=  ${A/(1+AF)}/{1+j\omega/[\omega_p(1+AF)]}$ =  $A/{(1+AF)(1+j\omega/[\omega_p(1+AF)])}$ 

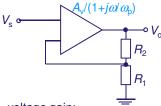
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formal feedback theory, VII: single-pole Op-Amps



voltage gain:

$$\begin{split} G &= V_{\text{o}}/V_{\text{s}} = \frac{A_{\text{v}}/(1+j\omega/\omega_{\text{p}})}{1+[A_{\text{v}}/(1+j\omega/\omega_{\text{p}})] \ R_{\text{1}}/(R_{\text{1}}+R_{\text{2}})} \\ &= A_{\text{v}}/[1+j\omega/\omega_{\text{p}}+A_{\text{v}}R_{\text{1}}/(R_{\text{1}}+R_{\text{2}})] \\ &= A_{\text{v}}/[1+A_{\text{v}}R_{\text{1}}/(R_{\text{1}}+R_{\text{2}})+j\omega/\omega_{\text{p}}] \\ &= \frac{A_{\text{v}}/[1+A_{\text{v}}R_{\text{1}}/(R_{\text{1}}+R_{\text{2}})]}{1+j\omega/\{\omega_{\text{p}}\ [1+A_{\text{v}}R_{\text{1}}/(R_{\text{1}}+R_{\text{2}})\}} \end{split}$$

 $= {A/(1+AF)}/{1+j\omega/[\omega_{p}(1+AF)]}$ = A/  $\{(1+AF)(1+j\omega/[\omega_p(1+AF)])\}$ 

advantages:

- · yields max. phase shift of 90°, i.e. single-pole Op-Amps with resistive feedback cannot oscillate and are always stable!
- · hence easy to use

disadvantages:

- low unity gain bandwidth (few MHz)
- poor slewing rates



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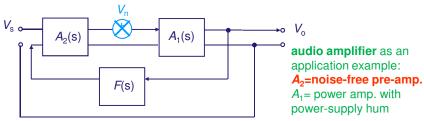
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# **Analogue Electronics**

formal feedback theory, VIII: noise reduction in 2-stage Op-Amps



signal-to-noise ratio of amplifier with input noise but without feedback:  $S/N=V_s/V_n$ 



signal-to-noise ratio of 2-stage amplifier with feedback where another noise-free stage precedes the noisy stage:

 $V_0 = V_s A_1 A_2 / (1 + A_1 A_2 F) + V_n A_1 / (1 + A_1 A_2 F) \rightarrow S / N = V_s A_2 / V_n$  is  $A_2$ -times larger

