

Q1 (i) Full wave bridge rectifier.

(ii) Diodes D_3 and D_1 conduct when B +ve w.r.t. A .

(iii) The capacitor maintains a voltage V_0 which is approximately V_p . The worst case condition for $D_3 + D_1$ is when V_{A-B} is at its +ve peak $\rightarrow D_2 + D_4$ are then conducting so $V_{\text{anode } D_4} = V_B + 0.7$ and $V_{\text{cathode } D_2} = V_A - 0.7$

$$\therefore \left. \begin{aligned} V_{D_3} &= V_A - 0.7 - V_B \\ V_{D_1} &= V_A - (V_B + 0.7) \end{aligned} \right\} = \text{reverse bias in both cases}$$

$V_A - V_B$ = peak voltage of V_s

$$\therefore \text{max reverse voltage} \approx V_s \sqrt{2} = \underline{495V}$$

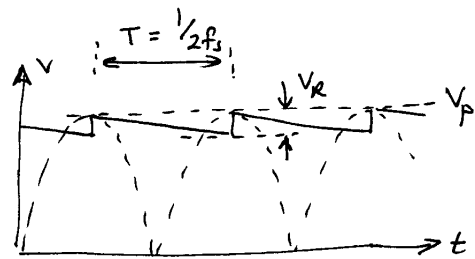
Same argument can be used for diodes 1 & 3 when V_B +ve w.r.t. V_A .

(iv)

$$(a) I = C \frac{dV}{dt}$$

$$0.35 = 68 \times 10^{-6} \times \frac{\Delta V}{10 \text{ ms}}$$

$$\text{or } \Delta V = V_R = 51.5V$$



$$\therefore V_0 \text{ dc} \approx V_p - \frac{V_R}{2} = (495 - 26)V = \underline{469V}$$

$$(b) V_R = 51.5V$$

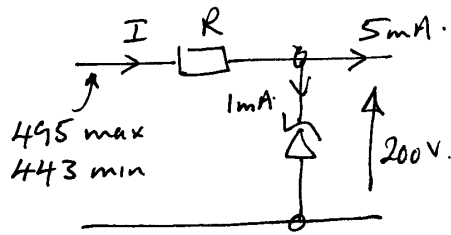
assumes - C discharges for whole charging interval
- 0.7V diode drops are negligible

$$(v) \text{ minimum input voltage} = V_p - V_R = 495 - 52 = 443V$$

$$I = 1mA + 5mA = \frac{443 - 200}{R}$$

$$6 \times 10^{-3} = \frac{243}{R}$$

or $R = 40.5 \text{ k}\Omega$.

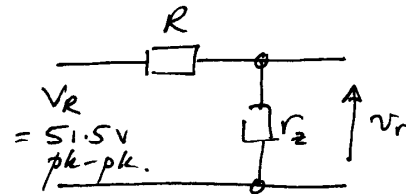


(vi) ripple equivalent circuit

$$V_r = V_R \frac{r_z}{R + r_z}$$

$$= 51.5 \times \frac{25}{40.5 \text{ k}\Omega + 25\Omega}$$

$$= 51.5 \times 617 \times 10^{-6} = \underline{\underline{32 \text{ mV}}}, \text{ pk-pk.}$$



(vii) Normal conditions for R

$$P = \frac{(V_{dc} - 200)^2}{R} = \frac{(469 - 200)^2}{40.5 \text{ k}\Omega} = \underline{\underline{1.8 \text{ W}}}$$

assumes that V_{dc} can be used to estimate power in R — not strictly accurate.

[If someone used $V_{i \text{ max}}$ to calculate P that would be acceptable.]

Normal conditions for D_z are $I_z = 0 \text{ mA}$ and $V_z = 200$

$$\therefore I_z = \frac{469 - 200}{R} - 5 \text{ mA} = 1.64 \text{ mA}$$

$$\therefore P_z = 200 \text{ V} \times 1.64 \text{ mA} = \underline{\underline{330 \text{ mW}}}$$

[again, if someone uses $V_{i \text{ max}}$, that would be acceptable

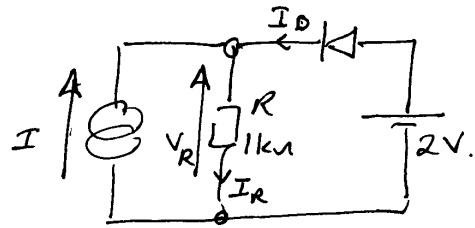
$$I_z = \frac{495 - 200}{R} - 5 \text{ mA} = 2.28 \text{ mA}$$

$$\therefore P_z = 200 \text{ V} \times 2.28 \text{ mA} = \underline{\underline{457 \text{ mW}}}]$$

Q2(a)(i) Diode will be on the point of conduction when $V_R = 1.3V$ and all of I goes through R .

ie when $I \times 1k\Omega = 1.3V$.

or $\underline{I = 1.3mA}$.



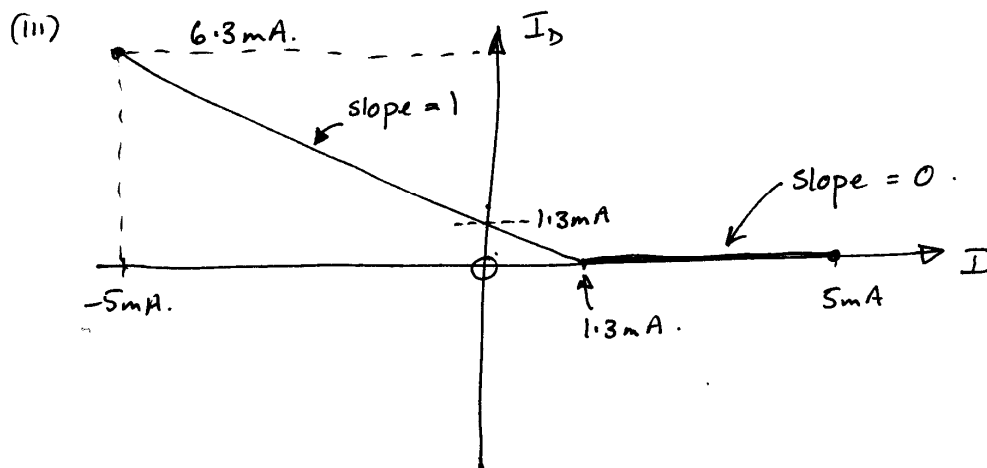
(ii) If $I > 1.3mA$, diode will be non-conducting
if $I < 1.3mA$, diode will conduct.

For $I = 5mA$, $\underline{I_D = 0}$ $V_D = 2 - 5mA \times 1k\Omega = \underline{-3V}$.

For $I = -5mA$, $I_D + (-5mA) = I_R = \frac{1.3V}{1k\Omega} = 1.3mA$.

$I_D = 1.3mA - (-5mA) = \underline{6.3mA}$.

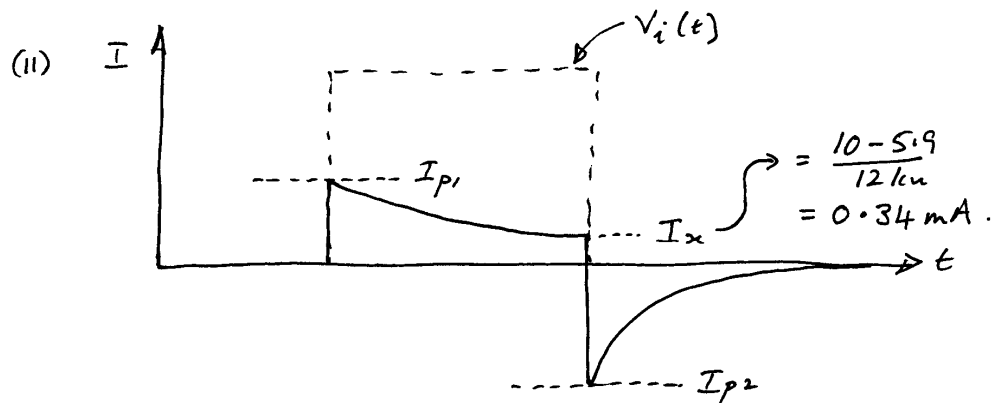
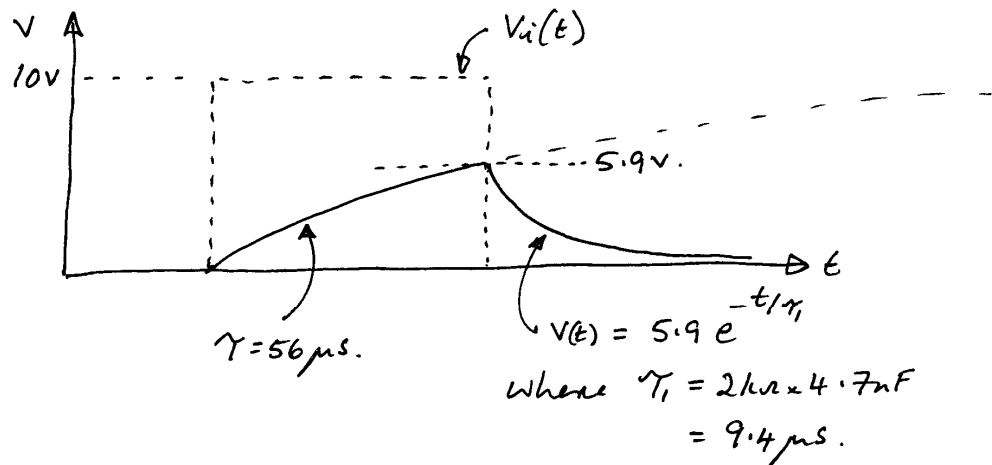
and $V_D = \underline{0.7V}$.



(b) (i) rising edge response is $V(t) = 10(1 - e^{-t/\tau})$

where $\tau = 12\text{ k}\Omega \times 4.7\text{ nF} = 56\text{ }\mu\text{s}$.

$\therefore V(50\text{ }\mu\text{s}) = 10(1 - e^{-50/56}) = \underline{\underline{5.9\text{ V}}}$



$I_{p1} = \frac{10\text{ V}}{12\text{ k}\Omega} = \underline{\underline{0.83\text{ mA}}}$ $I_{p2} = -\frac{5.9\text{ V}}{2\text{ k}\Omega} = \underline{\underline{-2.95\text{ mA}}}$

(iii) With $18\text{ k}\Omega$ in parallel with C , aiming level becomes $10 \times \frac{18\text{ k}\Omega}{18\text{ k}\Omega + 12\text{ k}\Omega} = 6\text{ V}$ and $\tau = (18\text{ k}\Omega // 12\text{ k}\Omega) \times 4.7\text{ nF} = 33.8\text{ }\mu\text{s}$.

$V(50\text{ }\mu\text{s}) = 6(1 - e^{-50/33.8}) = \underline{\underline{4.63\text{ V}}}$

Q3(a)(i) $I_C = I_L = \frac{24V}{6\Omega} = \underline{4A}$; $I_S = \underline{0A}$.
(V_{CEON} ignored).

(ii) in "on" state, $P_D = V_{CEON} \times I_C = 4A \times 0.2V = \underline{0.8W}$.

(iii) $I_{CON} = 4A$ so required $I_B = \frac{4A}{h_{FE}} = \frac{4A}{30} = 133mA$

$$I_{BON} = \frac{V_1 - V_{BEON}}{R_B} = \frac{10 - 0.7}{R_B} = 133mA$$

$$\text{or } R_B = \frac{9.3V}{0.133A} = \underline{70\Omega}.$$

(iv) immediately after switch off
 $I_C = 0$ $I_L = \text{unchanged}$

$$\therefore \underline{I_C = 0} \quad I_L = I_S = \underline{4A}.$$

(v) immediately after switch off

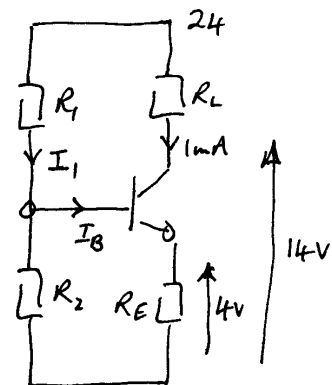
$$V_{CE} = 24 + I_S R_S + 0.7 = 24 + 48 + 0.7 = \underline{72.7V}.$$

(b)(i) Since $h_{FE} \gg 1$, $I_C \approx I_E$

$$\therefore R_E = \frac{4V}{1mA} = \underline{4k\Omega}.$$

$$R_L = \frac{24 - 14}{1mA} = \underline{10k\Omega}.$$

$$I_B = I_C / h_{FE} = \frac{1mA}{200} = 5\mu A.$$



$I_1 = \frac{24}{R_1 + R_2}$ must be at least $10 \times I_B$ in order to justify ignoring I_B — ie $I_1 = 50\mu A$.

$$\therefore R_1 + R_2 = 24V / 50\mu A = 480k\Omega$$

$$\text{and } \frac{24 \cdot R_2}{R_1 + R_2} = V_B = V_E + 0.7 = 4.7$$

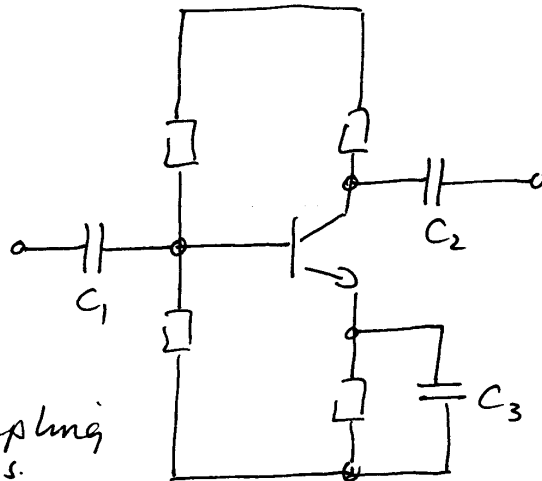
$$\text{So } 24R_2 = 4.7R_1 + 4.7R_2$$

$$\text{or } 19.3R_2 = 4.7R_1$$

$$\frac{R_1}{R_2} = \frac{19.3}{4.7} = 4.1$$

$$\therefore R_1 + \frac{R_1}{4.1} = 480\text{k}\Omega \rightarrow \begin{aligned} R_1 &= \underline{\underline{386\text{k}\Omega}} \\ R_2 &= \underline{\underline{94\text{k}\Omega}} \end{aligned}$$

(11)



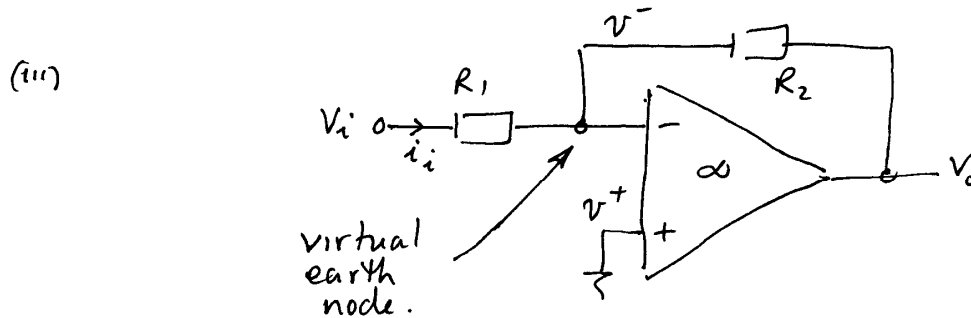
$C_1 + C_2$ are coupling capacitors.

C_3 is a decoupling capacitor.

Q4 (a) (i) "inverting amplifier"

(ii) $\frac{V_o}{V_i} = -\frac{R_2}{R_1}$

suitable values $R_2 = 10\text{ k}\Omega$ $R_1 = 1\text{ k}\Omega$.



The output voltage of the amplifier is given by $V_o = A_v (v^+ - v^-)$.

If $A_v \Rightarrow \infty$, $(v^+ - v^-) \approx 0$ for finite V_o and hence $v^+ \approx v^-$. If $v^+ = 0$ then $v^- \approx 0$ also i.e., $v^- = \text{virtual earth}$.

(iv) The input resistance of the circuit is R_1 (or whatever particular value has been chosen for R_1). Input resistance is defined as $\frac{V_i}{i_i}$ (in diag above)

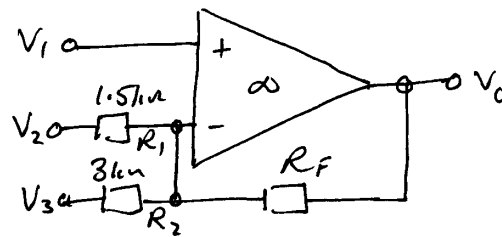
$$i_i = \frac{V_i - v^-}{R_1} = \frac{V_i - 0}{R_1} = \frac{V_i}{R_1}$$

$$\text{or } \frac{V_i}{i_i} = r_i = R_1$$

(b)

Amplifier requirements ...

- (i) Sinusoidal amplitude to be increased from 1.5V to 2.5V



\therefore gain of $\frac{2.5}{1.5} = 1.67$ is needed

- (ii) dc component of sinusoid to be changed from 0V at input to 2.5V at output — ie component of 2.5V to be added at output.

Gain first ...

$$\left. \frac{V_0}{V_1} \right|_{a.c.} = 1.67 = \frac{R_F + R_1 \parallel R_2}{R_1 \parallel R_2} = \frac{R_F + 1k\Omega}{1k\Omega}$$

$$\therefore \underline{R_F = 0.67k\Omega}$$

DC offset ---

amplifier is inverting for V_2 and inverting input is a virtual ground

$$\therefore \left. \frac{V_0}{V_2} \right|_{dc} = -\frac{R_F}{R_1} = -\frac{0.67k\Omega}{1.5k\Omega} = -0.44$$

$$\therefore \frac{2.5}{V_2} = -0.44 \quad \text{or} \quad V_2 = -\frac{2.5}{0.44} = \underline{\underline{-5.68V}}$$