

## CA LAB 12

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### Task1:

```
1 INP
2 OUT
3 CLA
4 OUT
5 HALT
6
```

EXECUTING...

Enter Inputs, the first of which must be an Integer: 3

Output: 3

Output: 0

EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

### Task2:

Name	Width	Data
AC	16	0
AR	12	-2048
DR	16	0
E	1	0
I	1	0
IR	16	-2048
PC	12	1
S	1	0

Name	Width	Data
AC	16	-5
AR	12	1
DR	16	0
E	1	0
I	1	0
IR	16	-8191
PC	12	4
S	1	-1

### Task3:

```
1 INP
2 INC
3 OUT
4 HALT
5
```

EXECUTING...

Enter Inputs, the first of which must be an Integer: 3

Output: 6

EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

### Task4:

```
1 INP
2 SPA
3 OUT
4 HALT
```

EXECUTING...

Enter Inputs, the first of which must be an Integer: 4

EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

EXECUTING...

Enter Inputs, the first of which must be an Integer: -4

Output: -4

EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

### Task5:

```
1 INP
2 SNA
3 OUT
4 HALT
```

EXECUTING...

Enter Inputs, the first of which must be an Integer: 3

Output: 3

EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

EXECUTING...

Enter Inputs, the first of which must be an Integer: -4

EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

