**Vulkan®** is a graphics and compute API consisting of procedures and functions to specify shader programs, compute kernels, objects, and operations involved in producing high-quality graphical images, specifically color images of three-dimensional objects. Vulkan is also a pipeline with programmable and state-driven fixed-function stages that are invoked by a set of specific drawing operations.

Specification and additional resources at www.khronos.org/vulkan





Color coded names as follows: function names and structure names [n.n.n] Indicates sections and text in the Vulkan API 1.1 Specification.

**P.#** Indicates a page in this reference guide for more information.

Indicates reserved for future use.

Command Function Pointers and Instances [3]

typedef void(VKAPI\_PTR\* PFN\_vkVoidFunction)(void);

Command Function Pointers [3.1]

PFN vkVoidFunction is:

VkResult vkCreateInstance(

VkInstance\* pInstance);

const void\* pNext;

PFN vkVoidFunction vkGetInstanceProcAddr(

PFN vkVoidFunction vkGetDeviceProcAddr(

VkDevice device, const char\* pName);

VkResult vkEnumerateInstanceVersion( uint32\_t\* pApiVersion);

typedef struct VkInstanceCreateInfo { VkStructureType sType; P.15

VkInstance instance, const char\* pName);

const VklnstanceCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12

VkInstanceCreateFlags flags; =0 const VkApplicationInfo; vint32\_t enabledLayerCount; const char\* const\* ppEnabledLayerNames; uint32\_t enabledExtensionCount;

pNext must either be NULL, or point to a valid structure which extends the base structure according to the valid usage rules of the base structure.

#### Return Codes [2.7.3]

Return codes are reported via VkResult return values.

#### **Success Codes**

Success codes are non-negative. VK SUCCESS VK NOT READY VK\_EVENT\_{SET, RESET} VK TIMEOUT VK\_SUBOPTIMAL\_KHR VK INCOMPLETE

#### **Error Codes**

Error codes are negative.

VK\_ERROR\_OUT\_OF\_{HOST, DEVICE}\_MEMORY
VK\_ERROR\_{INITIALIZATION, MEMORY\_MAP}\_FAILED

VK\_ERROR\_DEVICE\_LOST

VK\_ERROR\_{EXTENSION, FEATURE, LAYER}\_NOT\_PRESENT

VK ERROR INCOMPATIBLE DRIVER VK\_ERROR\_TOO\_MANY\_OBJECTS

VK\_ERROR\_FORMAT\_NOT\_SUPPORTED
VK\_ERROR\_FRAGMENTED\_POOL

VK\_ERROR\_OUT\_OF\_POOL\_MEMORY

VK\_ERROR\_INVALID\_EXTERNAL\_HANDLE

VK\_ERROR\_SURFACE\_LOST\_KHR
VK\_ERROR\_NATIVE\_WINDOW\_IN\_USE\_KHR

VK\_ERROR\_OUT\_OF\_DATE\_KHR

VK ERROR INCOMPATIBLE DISPLAY KHR

VkResult vkEnumeratePhysicalDevices(

void vkGetPhysicalDeviceProperties(

void vkGetPhysicalDeviceProperties2( VkPhysicalDevice physicalDevice,

VkStructureType sType; P.15

} VkPhysicalDeviceProperties2;

void\* pNext;

uint32\_t\* pPhysicalDeviceCount, VkPhysicalDevice\* pPhysicalDevices);

VkPhysicalDevice physicalDevice,
VkPhysicalDeviceProperties\* pProperties); P.14

VkPhysicalDeviceProperties2\* pProperties);

VkPhysicalDeviceProperties properties; P.14

VkPhysicalDeviceIDProperties P.14
VkPhysicalDeviceMaintenance3Properties P.14

VkPhysicalDeviceMultiviewProperties P.14
VkPhysicalDevicePointClippingProperties P.14

void vkGetPhysicalDeviceQueueFamilyProperties(

void vkGetPhysicalDeviceQueueFamilyProperties2(

uint32\_t queuecount; uint32\_t timestampValidBits; VkExtent3D minImageTransferGranularity; P.12

VK\_QUEUE\_X\_BIT where X is GRAPHICS, COMPUTE, TRANSFER, PROTECTED, SPARSE\_BINDING

VkQueue Family Properties 2\* pQueue Family Properties);

uint32\_t\* pQueueFamilyPropertyCount,

uint32\_t\* pQueueFamilyPropertyCount,

typedef struct VkQueueFamilyProperties {

typedef struct VkQueueFamilyProperties2 {

VkStructureType sType; P.15 void\* pNext; VkQueueFamilyProperties

VkPhysicalDevice physicalDevice,

pQueueFamilyProperties);

VkPhysicalDevice physicalDevice,

VkQueueFamilyProperties\*

VkQueueFlags queueFlags;

uint32\_t queueCount;

} VkQueueFamilyProperties;

VkPhysicalDeviceProtectedMemoryProperties P.15
VkPhysicalDeviceSubgroupProperties P.15

typedef struct VkPhysicalDeviceProperties2 {

pNext must be NULL or point to one of:

**Devices and Queues [4]** 

Physical Devices [4.1]

VkInstance instance,

#### Devices [4.2]

#### VkResult vkEnumeratePhysicalDeviceGroups(

VkInstance instance, uint32\_t\* pPhysicalDeviceGroupCount,

VkPhysicalDeviceGroupProperties pPhysicalDeviceGroupProperties);

typedef struct VkPhysicalDeviceGroupProperties { VkStructureType sType; P.15

void\* pNext; uint32\_t physicalDeviceCount;

VkPhysicalDevice physicalDevices[ VK\_MAX\_DEVICE\_GROUP\_SIZE];

VkBool32 subsetAllocation;

} VkPhysicalDeviceGroupProperties;

#### Device Creation [4.2.1]

VkResult vkCreateDevice(

VkPhysicalDevice physicalDevice,

const VkDeviceCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12

VkDevice\* pDevice);

typedef struct VkDeviceCreateInfo {

VkStructureType sType; P.15

const void\* pNext;

VkDeviceCreateFlags flags; =0 uint32\_t queueCreateInfoCount;

const VkDeviceQueueCreateInfo\* pQueueCreateInfos;

uint32\_t enabledLayerCount;
const char\* const\* ppEnabledLayerNames;
uint32\_t enabledExtensionCount;
const char\* const\* ppEnabledExtensionNames;
const VkPhysicalDeviceFeatures\* pEnabledFeatures;
P.14

} VkDeviceCreateInfo;

pNext must be NULL or point to one of:

VkDeviceGroupDeviceCreateInfo P.12

VkPhysicalDevice16BitStorageFeatures P.14
VkPhysicalDeviceFeatures P.14
VkPhysicalDeviceMultiviewFeatures P.14
VkPhysicalDeviceProtectedMemoryFeatures P.15
VkPhysicalDeviceSamplerYcbcrConversionFeatures P.15

VkPhysicalDeviceVariablePointerFeatures P.15

#### typedef struct VkDeviceGroupDeviceCreateInfo {

VkStructureType sType; 15

const void\* pNext;

uint32\_t physicalDeviceCount;

const VkPhysicalDevice\* pPhysicalDevices;

} VkDeviceGroupDeviceCreateInfo;

## **Device Destruction [4.2.4]**

void vkDestroyDevice(

VkDevice *device*, const VkAllocationCallbacks\* *pAllocator*); P.12

const char\* const\* ppEnabledExtensionNames; } VkInstanceCreateInfo;

} VkApplicationInfo;

#### void vkDestroyInstance(

VkInstance instance, const VkAllocationCallbacks\* pAllocator); P.12

#### Queues [4.3]

typedef struct VkDeviceQueueCreateInfo {

VkStructureType sType; P.15 const void\* pNext;

VkDeviceQueueCreateFlags flags;

uint32\_t queueFamilyIndex;

uint32\_t queueCount; const float\* pQueuePriorities; } VkDeviceQueueCreateInfo;

flags: VK\_DEVICE\_QUEUE\_CREATE\_PROTECTED\_BIT

void vkGetDeviceQueue(VkDevice device,

uint32\_t queueFamilyIndex, uint32\_t queueIndex, VkQueue\* pQueue);

void vkGetDeviceQueue2(VkDevice device, const VkDeviceQueueInfo2\* pQueueInfo,

VkQueue\* pQueue);

} VkDeviceQueueInfo2;

flags: VK\_DEVICE\_QUEUE\_CREATE\_PROTECTED\_BIT

## Command Buffers [5]

Also see Command Buffer Lifecycle diagram. P.5

#### Command Pools [5.2]

VkResult vkCreateCommandPool(

VkDevice device,

const VkCommandPoolCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkCommandPool\* pCommandPool);

#### typedef struct VkCommandPoolCreateInfo {

VkStructureType sType; P.15

const void\* pNext;

VkCommandPoolCreateFlags flags;

uint32\_t queueFamilyIndex; } VkCommandPoolCreateInfo;

flags: VK\_COMMAND\_POOL\_CREATE\_X\_BIT where X is PROTECTED, RESET\_COMMAND\_BUFFER, TRANSIENT

#### void vkTrimCommandPool(VkDevice device, VkCommandPool commandPool,

VkCommandPoolTrimFlags flags);

VkResult vkResetCommandPool( VkDevice device, VkCommandPool commandPool, VkCommandPoolResetFlags flags);

flags: VK\_COMMAND\_POOL\_RESET\_RELEASE\_-**RESOURCES BIT** 

void vkDestroyCommandPool( VkDevice device, VkCommandPool commandPool, const VkAllocationCallbacks\* pAllocator); [212]

Continued on next page > www.khronos.org/vulkan

} VkQueueFamilyProperties2;

queueFamilyProperties;

#### **Command Buffers (continued)**

#### Command Buffer Lifetime [5.3]

VkResult vkAllocateCommandBuffers(

VkDevice device, const VkCommandBufferAllocateInfo\* pAllocateInfo, VkCommandBuffer\* pCommandBuffers);

#### typedef struct VkCommandBufferAllocateInfo{

VkStructureType sType; P.15 const void\* pNext;

VkCommandPool commandPool; VkCommandBufferLevel level; uint32 t commandBufferCount;

} VkCommandBufferAllocateInfo;

VK COMMAND BUFFER LEVEL {PRIMARY, SECONDARY}

#### VkResult vkResetCommandBuffer(

VkCommandBuffer commandBuffer,

VkCommandBufferResetFlags flags);

VK\_COMMAND\_BUFFER\_RESET\_RELEASE\_RESOURCES\_BIT

void **vkFreeCommandBuffers(**VkDevice *device*, VkCommandPool *commandPool*, uint32\_t commandBufferCount, const VkCommandBuffer\* pCommandBuffers);

#### Command Buffer Recording [5.4]

VkResult vkBeginCommandBuffer( VkCommandBuffer commandBuffer, const VkCommandBufferBeginInfo\* pBeginInfo);

#### typedef struct VkCommandBufferBeginInfo{

VkStructureType sType; P.15

const void\* pNext;

VkCommandBufferUsageFlags flags; const VkCommandBufferInheritanceInfo\* pInheritanceInfo; } VkCommandBufferBeginInfo;

flags: VK\_COMMAND\_BUFFER\_USAGE\_X\_BIT where X is ONE\_TIME\_SUBMIT, RENDER\_PASS\_CONTINUE, SIMULTANEOUS USE

pNext must be NULL or point to:

VkDeviceGroupCommandBufferBeginInfo P.12

#### typedef struct VkCommandBufferInheritanceInfo {

VkStructureType sType; P.15 const void\* pNext; VkRenderPass renderPass;

uint32\_t *subpass*; VkFramebuffer *framebuffer*; VkBool32 occlusionQueryEnable;

VkQueryControlFlags queryFlags;

VkQueryPipelineStatisticFlags pipelineStatistics; P.15 } VkCommandBufferInheritanceInfo;

queryFlags: VK\_QUERY\_CONTROL\_PRECISE\_BIT

#### VkResult vkEndCommandBuffer(

VkCommandBuffer commandBuffer);

#### Command Buffer Submission [5.5]

VkResult vkQueueSubmit(

VkQueue queue, uint32\_t submitCount, const VkSubmitInfo\* pSubmits, VkFence fence);

#### typedef struct VkSubmitInfo{

VkStructureType sType; P.15 const void\* pNext; uint32\_t waitSemaphoreCount;

const VkSemaphore\* pWaitSemaphores; const VkPipelineStageFlags\* pWaitDstStageMask; P.15

uint32\_t commandBufferCount; const VkCommandBuffer\* pCommandBuffers;

uint32\_t signalSemaphoreCount; const VkSemaphore\* pSignalSemaphores;

} VkSubmitInfo:

pNext must be NULL or point to one of: VkDeviceGroupSubmitInfo P.12 VkProtectedSubmitInfo P.15

#### Secondary Command Buffer Execution [5.7]

void vkCmdExecuteCommands(

VkCommandBuffer commandBuffer, uint32\_t commandBufferCount, const VkCommandBuffer\* pCommandBuffers);

#### Command Buffer Device Mask [5.8]

void vkCmdSetDeviceMask(

VkCommandBuffer commandBuffer, uint32\_t deviceMask);

## Synchronization and Cache Control [6]

Fence status is always either signaled or unsignaled.

#### VkResult vkCreateFence(

VkDevice device, const VkFenceCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkFence\* pFence);

## typedef struct VkFenceCreateInfo {

VkStructureType sType; P.15 const void\* pNext;

VkFenceCreateFlags flags;

} VkFenceCreateInfo;

flags: VK\_FENCE\_CREATE\_SIGNALED\_BIT pNext must be NULL or point to:

VkExportFenceCreateInfo P.12

void vkDestroyFence(

VkDevice device, VkFence fence, const VkAllocationCallbacks\* pAllocator); P.12

#### VkResult vkGetFenceStatus(

VkDevice device, VkFence fence);

VkResult vkResetFences(VkDevice device, uint32\_t fenceCount, const VkFence\* pFences);

VkResult vkWaitForFences(VkDevice device, uint32\_t fenceCount, const VkFence\* pFences, VkBool32 waitAll, uint64\_t timeout);

#### Semaphores [6.4]

Semaphore status is always either signaled or unsignaled.

#### VkResult vkCreateSemaphore(

VkDevice device,

const VkSemaphoreCreateInfo\* pCreateInfo const VkAllocationCallbacks\* pAllocator, P112 VkSemaphore\* pSemaphore);

#### typedef struct VkSemaphoreCreateInfo {

VkStructureType sType; P.15 const void\* pNext; VkSemaphoreCreateFlags flags; =0

} VkSemaphoreCreateInfo;

pNext must be NULL or point to: VkExportSemaphoreCreateInfo P.12

#### void vkDestroySemaphore(

VkDevice device, VkSemaphore semaphore const VkAllocationCallbacks\* pAllocator); P.12

#### **Events [6.5]**

Events represent a fine-grained synchronization primitive that can be used to gauge progress through a sequence of commands executed on a queue.

VkResult vkCreateEvent( VkDevice device, const VkEventCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkEvent\* pEvent);

typedef struct VkEventCreateInfo { VkStructureType sType; P.15 const void\* pNext;

VkEventCreateFlags flags; = 0 } VkEventCreateInfo;

void vkDestroyEvent(VkDevice device, VkEvent event, const VkAllocationCallbacks\* pAllocator); P.12

#### VkResult vkGetEventStatus(

VkDevice device, VkEvent event);

## VkResult vk[Set, Reset]Event(

VkDevice device, VkEvent event);

## VkResult vkCmd[Set, Reset]Event(

VkCommandBuffer commandBuffer, VkEvent event, VkPipelineStageFlags stageMask); P.15

void vkCmdWaitEvents( VkCommandBuffer commandBuffer, uint32 t eventCount,

const VkEvent\* pEvents,
VkPipelineStageFlags srcStageMask, P.15 VkPipelineStageFlags dstStageMask, P.15

uint32 t memoryBarrierCount, const VkMemoryBarrier\* pMemoryBarriers, P.13 uint32\_t bufferMemoryBarrierCount,

const VkBufferMemoryBarrier\*

pBufferMemoryBarriers, P.12 uint32\_t imageMemoryBarrierCount, const VkImageMemoryBarrier\*

pImageMemoryBarriers); P.13

#### Version Number Macros [Appendix D]

The vulkan.h header defines C preprocessor macros that are described below. Version numbers are packed into integers.

#define VK\_VERSION\_MAJOR(version) \ ((uint32 t)(version) >> 22)

#define VK VERSION MINOR(version) \  $(((uint32_t)(version) >> 12) & 0x3ff)$ 

#define VK\_VERSION\_PATCH(version) \ ((uint32\_t)(version) & 0xfff)

#define VK\_API\_VERSION\_1\_0 VK\_MAKE\_VERSION(1, 0, 0)

#define VK\_API\_VERSION\_1\_1 VK\_MAKE\_VERSION(1, 1, 0)

#define VK\_MAKE\_VERSION(major, minor, patch) \ (((major) << 22) | ((minor) << 12) | (patch))

major: the major version number minor: the minor version number patch: the patch version number

#### #define VK\_HEADER\_VERSION patch-version

patch-version: synchronized with the patch version of the released specification

#### VK\_NULL\_HANDLE

This is a canonical invalid non-dispatchable handle. It is returned by certain object creation functions if creation fails, and can be passed as a parameter where permitted by VU

# **Notes**

#### Pipeline Barriers [6.6]

void vkCmdPipelineBarrier(

VkCommandBuffer commandBuffer, VkPipelineStageFlags srcStageMask, P.15 VkPipelineStageFlags dstStageMask, P.15 VkDependencyFlags dependencyFlags, P.15 VkDependencyFlags dependencyFlags, P.15 const VkMemoryBarrier\* pMemoryBarriers, P.13

uint32\_t bufferMemoryBarrierCount, const VkBufferMemoryBarrier\* pBufferMemoryBarriers, P.12 uint32\_t imageMemoryBarrierCount, const VkImageMemoryBarrier\*

plmageMemoryBarriers); P.13

## Wait Idle Operations [6.8]

VkResult vkQueueWaitIdle(VkQueue queue);

VkResult vkDeviceWaitIdle(VkDevice device);

#### Render Pass [7]

A render pass represents a collection of attachments, subpasses, and dependencies between the subpasses, and describes how the attachments are used over the course of the subpasses.

#### Render Pass Creation [7.1]

VkResult vkCreateRenderPass(VkDevice device, const VkRenderPassCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkRenderPass\* pRenderPass)

uint32 t subpassCount;

const VkSubpassDescription\* pSubpasses;

uint32\_t dependencyCount; const VkSubpassDependency\* pDependencies;

} VkRenderPassCreateInfo;

pNext must be NULL or point to one of: VkRenderPassInputAttachmentAspectCreateInfo

VkRenderPassMultiviewCreateInfo P1315

#### typedef struct VkAttachmentDescription {

VkAttachmentDescriptionFlags flags;

VkFormat format; [33]

VkSampleCountFlagBits samples; P.15

VkAttachmentLoadOp loadOp;

VkAttachmentStoreOp storeOp; VkAttachmentLoadOp stencilLoadOp; VkAttachmentStoreOp stencilStoreOp; VkImageLayout initialLayout; [2.13]

VkImageLayout finalLayout; 13
} VkAttachmentDescription;

flags: VK\_ATTACHMENT\_DESCRIPTION\_MAY\_ALIAS\_BIT

loadOp, stencilLoadOp: VK\_ATTACHMENT\_LOAD\_OP\_X where X is LOAD, CLEAR, DONT\_CARE

storeOp, stencilStoreOp: VK\_ATTACHMENT\_STORE\_OP\_X where X is STORE, DONT\_CARE

#### typedef struct VkSubpassDescription {

VkSubpassDescriptionFlags *flags*;

VkPipelineBindPoint pipelineBindPoint; P315 uint32\_t inputAttachmentCount;

 $const \ \overline{V} k Attachment Reference * \ pInput Attachments;$ 

uint32\_t colorAttachmentCount; const VkAttachmentReference\* pColorAttachments; const VkAttachmentReference\*

pResolveAttachments; const VkAttachmentReference\*

pDepthStencilAttachment; uint32\_t preserveAttachmentCount;

const uint32\_t\* pPreserveAttachments;

} VkSubpassDescription;

#### typedef struct VkAttachmentReference {

uint32\_t attachment; VkImageLayout layout; P.13

} VkAttachmentReference;

#### typedef struct VkSubpassDependency {

uint32\_t srcSubpass; uint32\_t dstSubpass;

VkPipelineStageFlags srcStageMask; P.15
VkPipelineStageFlags dstStageMask; P.12

VkAccessFlags srcAccessMask; P.12

VkAccessFlags dstAccessMask; P.12

VkDependencyFlags dependencyFlags; P1512

} VkSubpassDependency;

#### void vkDestroyRenderPass(VkDevice device,

VkRenderPass renderPass, const VkAllocationCallbacks\* pAllocator); P.12

#### Framebuffers [7.3]

VkResult vkCreateFramebuffer(VkDevice device, const VkFramebufferCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, 212 VkFramebuffer\* pFramebuffer);

#### typedef struct VkFramebufferCreateInfo {

VkStructureType sType; visconst void\* pNext;
VkFramebufferCreateFlags flags; = 0
VkRenderPass renderPass;
uint32\_t attachmentCount;

const VkImageView\* pAttachments;

uint32\_t width; uint32\_t height;

uint32\_t layers;

} VkFramebufferCreateInfo;

#### void vkDestroyFramebuffer(

VkDevice device, VkFramebuffer framebuffer, const VkAllocationCallbacks\* pAllocator); P.12

#### Render Pass Commands [7.4]

void vkCmdBeginRenderPass( VkCommandBuffer commandBuffer,

const VkRenderPassBeginInfo\* pRenderPassBegin,

VkSubpassContents contents);

contents: VK\_SUBPASS\_CONTENTS\_X where X is INLINE, SECONDARY COMMAND BUFFERS

typedef struct VkRenderPassBeginInfo {

VkStructureType sType; 2.15 const void\* pNext; VkRenderPass renderPass; VkFramebuffer framebuffer; VkRect2D renderArea; 2.15

uint32\_t clearValueCount; const VkClearValue\* pClearValues; P.12

} VkRenderPassBeginInfo; pNext must be NULL or point to:

VkDeviceGroupRenderPassBeginInfo P.12

#### void vkGetRenderAreaGranularity(

VkDevice device, VkRenderPass renderPass, VkExtent2D\* pGranularity); P.12

#### void vkCmdNextSubpass(

VkCommandBuffer commandBuffer, VkSubpassContents contents);

contents: VK\_SUBPASS\_CONTENTS\_X where X is INLINE, SECONDARY\_COMMAND\_BUFFERS

#### void vkCmdEndRenderPass(

VkCommandBuffer commandBuffer);

#### Pipelines [9]

#### Compute Pipelines [9.1]

Compute pipelines consist of a single static compute shader stage and the pipeline layout.

#### VkResult vkCreateComputePipelines(

VkDevice device, VkPipelineCache pipelineCache, uint32\_t createInfoCount,

const VkComputePipelineCreateInfo\* pCreateInfos, const VkAllocationCallbacks\* pAllocator, P.12 VkPipeline\* pPipelines);

typedef struct VkComputePipelineCreateInfo { VkStructureType sType; P.15

const void\* pNext;
VkPipelineCreateFlags flags;
VkPipelineShaderStageCreateInfo stage;
P.15

VkPipelineLayout layout; VkPipeline basePipelineHandle; int32\_t basePipelineIndex; } VkComputePipelineCreateInfo;

#### **Graphics Pipelines** [9.2]

VkResult vkCreateGraphicsPipelines(

VkDevice device, VkPipelineCache pipelineCache, uint32\_t createInfoCount, const VkGraphicsPipelineCreateInfo\* pCreateInfos,

const VkAllocationCallbacks\* pAllocator, P.12 VkPipeline\* pPipelines);

In VkGraphicsPipelineCreateInfo below, replace X with VkPipeline and replace Y with StateCreateInfo. For example, XVertexInputY would be VxPipelineVertexInputStateCreateInfo.

#### typedef struct VkGraphicsPipelineCreateInfo {

VkStructureType sType; P.15 const void\* pNext; VkPipelineCreateFlags flags; P.15

uint32\_t stageCount; const VkPipelineShaderStageCreateInfo\* pStages; P.15

const VKPpenineshaderstagecreatening pstage const XVertexInputY\* pVertexInputState; const XInputAssemblyY\* pInputAssemblyState; const XTessellationY\* pTessellationState; const XViewportY\* pViewportState;

const XRasterizationY\* pRosterizationState; const XMultisampleY\* pMultisampleState; const XDepthStencilY\* pDepthStencilState;

const XColorBlendY\* pColorBlendState; const XDynamicY\* pDynamicState;

VkPipelineLayout layout; VkRenderPass renderPass;

uint32\_t subpass; VkPipeline basePipelineHandle; int32\_t basePipelineIndex; } VkGraphicsPipelineCreateInfo;

#### Shaders [8]

#### Shader Modules [8.1]

VkResult vkCreateShaderModule(

VkDevice device,

const VkShaderModuleCreateInfo\* pCreateInfo, const VkShaderModuleCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkShaderModule\* pShaderModule);

#### typedef struct VkShaderModuleCreateInfo {

VkStructureType sType; P.15

const void\* pNext; VkShaderModuleCreateFlags flags; =0

size\_t codeSize; const uint32\_t\* pCode;

VkShaderModuleCreateInfo;

#### void vkDestroyShaderModule(

VkDevice device,

VkShaderModule shaderModule, const VkAllocationCallbacks\* pAllocator); P.12

#### Built-in Variables [14.6]

The built-in variables listed below are accessed in shaders by declaring the variable using a BuiltIn decoration

deciaring the variable using a <b>builtin</b> decoration.								
Decoration	Туре							
BaseInstance	Scalar 32-bit integer							
BaseVertex	Scalar 32-bit integer							
ClipDistance	Array of 32-bit floats							
CullDistance	Array of 32-bit floats							
DeviceIndex	Scalar 32-bit integer							
DrawIndex	Scalar 32-bit integer							
FragCoord	4-component vector of 32-bit floats							
FragDepth	Scalar 32-bit float							
FrontFacing	Scalar 32-bit integer							
GlobalInvocationID	3-component vector of 32-bit ints							
HelperInvocation	Scalar 32-bit integer							
InvocationID	Scalar 32-bit integer							
InstanceIndex	Scalar 32-bit integer							
Layer	Scalar 32-bit integer							
LocalInvocationID	3-component vector of 32-bit ints							
NumSubgroups	Scalar 32-bit integer							
NumWorkGroups	3-component vector of 32-bit ints							

PatchVertices Scalar 32-bit integer PointCoord 2-component vector of 32-bit floats PointSize Scalar 32-bit float value

Position 4-component vector of 32-bit floats PrimitiveID Scalar 32-bit integer SampleID Scalar 32-bit integer SampleMask Array of 32-bit integers

SamplePosition 2-component vector of float values SubgroupId Scalar 32-bit integer

Subgroup{Eq,Ge,Gt,Le,Lt}Mask 4-component vector of 32-bit ints Scalar 32-bit integer SubgroupLocalInvocationId

SubgroupSize Scalar 32-bit integer TessCoord 3-component vector of 32-bit floats TessLevelOuter Array of size 2 of 32-bit floats

TessLevelInner Array of size 4 of 32-bit floats VertexIndex 32-bit integer Scalar 32-bit integer ViewIndex

32-bit integer ViewportIndex WorkgroupSize 3-component vector of 32-bit ints WorkgroupID 3-component vector of 32-bit ints

typedef struct VkPipelineVertexInputStateCreateInfo { VkStructureType sType; P.15 const void\* pNext;

VkPipelineVertexInputStateCreateFlags flags; =0 uint32\_t vertexBindingDescriptionCount; const VkVertexInputBindingDescription\*

pVertexBindingDescriptions; uint32\_t vertexAttributeDescriptionCount; const VkVertexInputAttributeDescription\* pVertexAttributeDescriptions;

} VkPipelineVertexInputStateCreateInfo; typedef struct VkVertexInputBindingDescription {

uint32\_t binding; uint32\_t stride; VkVertexInputRate inputRate; } VkVertexInputBindingDescription;

> inputRate: . VK\_VERTEX\_INPUT\_RATE\_{VERTEX, INSTANCE}

> > Continued on next page > www.khronos.org/vulkan

# **Pipelines (continued)**

typedef struct VkVertexInputAttributeDescription { uint32\_t location; uint32\_t binding; VkFormat format; P.13 uint32 t offset;

typedef struct VkPipelineInputAssemblyStateCreateInfo {

VkPipelineInputAssemblyStateCreateFlags flags; = 0

topology: VK\_PRIMITIVE\_TOPOLOGY\_X where X is POINT\_LIST, LINE\_LIST, LINE\_STRIP, TRIANGLE\_LIST, TRIANGLE\_STRIP, TRIANGLE\_FAN,
LINE\_{LIST, STRIP}\_WITH\_ADJACENCY,
TRIANGLE\_{LIST, STRIP}\_WITH\_ADJACENCY, PATCH\_LIST

uint32\_t patchControlPoints;

VkPipelineTessellationDomainOriginStateCreateInfo P.15

VkStructureType sType; P.15 const void\* pNext;

VkPipelineViewportStateCreateFlags flags; = 0

uint32\_t viewportCount; const VkViewport\* pViewports; P.15

uint32\_t scissorCount; const VkRect2D\* pScissors; P.15 } VkPipelineViewportStateCreateInfo;

VkStructureType sType; P.15 const void\* pNext;

VkPipelineRasterizationStateCreateFlags flags; =0

VkBool32 rasterizerDiscardEnable;

VkPolygonMode polygonMode;

VkFrontFace frontFace; VkBool32 depthBiasEnable; float depthBiasConstantFactor; float depthBiasClamp;

float lineWidth;

VkPipelineRasterizationStateCreateInfo;

cullMode: VK\_CULL\_MODE\_X where X is NONE, FRONT\_BIT, BACK\_BIT, FRONT\_AND\_BACK

typedef struct VkPipelineMultisampleStateCreateInfo { VkStructureType *sType*; P.15

vkstducturerye 3rype; week const void\* pNext; vkPipelineMultisampleStateCreateFlags flags; =0 vkSampleCountFlagBits rasterizationSamples; P.15 vkBool32 sampleShadingEnable;

float minSampleShading; const VkSampleMask\* pSampleMask;

VkBool32 alphaToCoverageEnable; VkBool32 alphaToOneEnable; } VkPipelineMultisampleStateCreateInfo;

typedef struct VkPipelineDepthStencilStateCreateInfo { VkStructureType sType; P.15

const void\* pNext;

VkPipelineDepthStencilStateCreateFlags flags; = 0

VkBool32 depthTestEnable; VkBool32 depthWriteEnable;

VkCompareOp depthCompareOp; P.12
VkBool32 depthBoundsTestEnable;

VkBool32 stencilTestEnable;

VkStencilOpState front; VkStencilOpState back;

float minDepthBounds;

float maxDepthBounds;

} VkPipelineDepthStencilStateCreateInfo;

typedef struct VkStencilOpState {
 VkStencilOp failOp;
 VkStencilOp passOp;
 VkStencilOp depthFailOp;
 VkCompareOp compareOp;
 uint32\_t compareMask;
 uint32\_t writeMask;
 uint32\_t reference;
}VkStencilOpState;

} VkStencilOpState;

enum VkStencilOp: VK\_STENCIL\_OP\_X where X is KEEP, ZERO, REPLACE, INCREMENT\_AND\_{CLAMP, WRAP}, INVERT, DECREMENT\_AND\_{CLAMP, WRAP}

typedef struct VkPipelineColorBlendStateCreateInfo {

VkStructureType sType; P.15 const void\* pNext;

VkPipelineColorBlendStateCreateFlags flags; =0

VkBool32 logicOpEnable; VkLogicOp logicOp;

uint32\_t attachmentCount;

const VkPipelineColorBlendAttachmentState\* pAttachments;

float blendConstants[4];

} VkPipelineColorBlendStateCreateInfo;

logicOp: VK\_LOGIC\_OP\_X where X is CLEAR, AND, AND\_REVERSE, COPY, AND\_INVERTED, NO\_OP, XOR, OR, NOR, EQUIVALENT, INVERT, OR\_REVERSE COPY INVERTED, OR INVERTED, NAND, SET

blendOp: VK\_BLEND\_OP\_X where X is ADD, SUBTRACT, REVERSE\_SUBTRACT, MIN, MAX

colorWriteMask: VK\_COLOR\_COMPONENT\_X where X is R\_BIT, G\_BIT, B\_BIT, A\_BIT

typedef struct VkPipelineColorBlendAttachmentState { VkBool32 blendEnable;

VkBlendFactor srcColorBlendFactor; VkBlendFactor dstColorBlendFactor;

VkBlendOp colorBlendOp; P.12
VkBlendFactor srcAlphaBlendFactor; VkBlendFactor dstAlphaBlendFactor;

VkBlendOp alphaBlendOp; P.12 VkColorComponentFlags colorWriteMask; } VkPipelineColorBlendAttachmentState;

enum VkBlendFactor: VK\_BLEND\_FACTOR\_X where X is ZERO, ONE, SRC\_ALPHA\_SATURATE, [ONE\_MINUS\_]SRC\_COLOR, [ONE\_MINUS\_]DST\_COLOR, [ONE\_MINUS\_]DST\_ALPHA, [ONE\_MINUS\_]DST\_ALPHA,

[ONE\_MINUS\_]CONSTANT\_COLOR, [ONE\_MINUS\_]CONSTANT\_ALPHA,

[ONE\_MINUS\_]SRC1\_COLOR, [ONE\_MINUS\_]SRC1\_ALPHA

colorWriteMask.

VK\_COLOR\_COMPONENT\_X\_BIT where X is R, G, B, A

uint32\_t dynamicStateCount;

const VkDynamicState\* pDynamicStates; } VkPipelineDynamicStateCreateInfo;

pDynamicStates: Array of VK\_DYNAMIC\_STATE\_X where X is VIEWPORT, SCISSOR, LINE\_WIDTH, DEPTH\_BIAS, BLEND\_CONSTANTS, DEPTH\_BOUNDS, STENCIL\_REFERENCE, STENCIL\_COMPARE\_MASK, STENCIL\_WRITE\_MASK

Pipeline Destruction [9.3]

void vkDestroyPipeline(

VkDevice device, VkPipeline pipeline, const VkAllocationCallbacks\* pAllocator); P.12

Pipeline Cache [9.6]

Pipeline cache objects allow the result of pipeline construction to be reused between pipelines and between runs of an application.

VkResult vkCreatePipelineCache(VkDevice device, const VkPipelineCacheCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkPipelineCache\* pPipelineCache);

typedef struct VkPipelineCacheCreateInfo {

VkStructureType sType; P.15 const void\* pNext; VkPipelineCacheCreateFlags flags; = 0

size\_t initialDataSize; const void\* plnitialData;

} VkPipelineCacheCreateInfo;

VkResult vkMergePipelineCaches(VkDevice device, VkPipelineCache dstCache, uint32\_t srcCacheCount, const VkPipelineCache\* pSrcCaches);

VkResult vkGetPipelineCacheData(VkDevice device, VkPipelineCache pipelineCache size\_t\* pDataSize, void\* pData);

void vkDestroyPipelineCache(VkDevice device, VkPipelineCache pipelineCache, const VkAllocationCallbacks\* pAllocator); P.12

Pipeline Binding [9.8] void vkCmdBindPipeline(

VkCommandBuffer commandBuffer,

VkPipelineBindPoint pipelineBindPoint, P.13 VkPipeline pipeline);

void vkUnmapMemory( VkDevice device,

VkDeviceMemory memory); Lazily Allocated Memory [10.2.2]

If the memory object is allocated from a heap with the VK\_MEMORY\_PROPERTY\_LAZILY\_ALLOCATED\_BIT bit set, that object's backing memory may be provided by the implementation lazily.

void vkGetDeviceMemoryCommitment(

VkDevice device,

VkDeviceMemory memory, VkDeviceSize\* pCommittedMemoryInBytes);

Peer Memory Features [10.2.4]

void vkGetDeviceGroupPeerMemoryFeatures( VkDevice device, uint32\_t heapIndex, uint32\_t localDeviceIndex, uint32\_t remoteDeviceIndex, VkPeerMemoryFeatureFlags\* pPeerMemoryFeatures);

pPeerMemoryFeatures: VK\_PEER\_MEMORY\_FEATURE\_X where X is COPY\_SRC\_BIT, COPY\_DST\_BIT,

GENERIC\_SRC\_BIT, GENERIC\_DST\_BIT

} VkVertexInputAttributeDescription;

VkStructureType sType; P.15 const void\* pNext;

VkPrimitiveTopology topology; VkBool32 primitiveRestartEnable; } VkPipelineInputAssemblyStateCreateInfo;

typedef struct VkPipelineTessellationStateCreateInfo { VkStructureType sType; P.15

const void\* pNext; VkPipelineTessellationStateCreateFlags flags; =0

} VkPipelineTessellationStateCreateInfo;

pNext must be NULL or point to:

typedef struct VkPipelineViewportStateCreateInfo {

typedef struct VkPipelineRasterizationStateCreateInfo {

VkBool32 depthClampEnable;

VkCullModeFlags cullMode;

float depthBiasSlopeFactor;

polygonMode: VK\_POLYGON\_MODE\_{FILL, LINE, POINT}

frontFace: VK FRONT FACE [COUNTER ]CLOCKWISE

# Memory Allocation [10]

Device Memory [10.2]

Device memory is memory that is visible to the device. void vkGetPhysicalDeviceMemoryProperties(

VkPhysicalDevice physicalDevice, VkPhysicalDeviceMemoryProperties\*

pMemoryProperties); P.14

void vkGetPhysicalDeviceMemoryProperties2( VkPhysicalDevice physicalDevice, VkPhysicalDeviceMemoryProperties2\* pMemoryProperties);

typedef struct VkPhysicalDeviceMemoryProperties2 { VkStructureType sType; P.15

void\* pNext; **VkPhysicalDeviceMemoryProperties** memoryProperties; P.14
} VkPhysicalDeviceMemoryProperties2;

VkResult vkAllocateMemory(

VkDevice device, const VkMemoryAllocateInfo\* pAllocateInfo, const VkAllocationCallbacks\* pAllocator, №12
VkDeviceMemory\* pMemory);

typedef struct VkMemoryAllocateInfo { const VkMappedMemoryRange\* pMem VkStructureType sType; 15 const void\* pNext; vkDeviceSize\* allocationSize; uint32\_t memoryTypeIndex; typedef struct VkMappedMemoryRange { VkStructureType sType; 15 } VkMemoryAllocateInfo; pNext must be NULL or point to one of:

VkExportMemoryAllocateInfo P.12 VkMemoryAllocateFlagsInfo P.13
VkMemoryDedicatedAllocateInfo P.13 void vkFreeMemory(

VkDeviceMemory memory, const VkAllocationCallbacks\* pAllocator); P.12

Host Access to Device Memory Objects [10.2.1] Memory objects created with vkAllocateMemory are not directly host accessible. Memory objects created with memory property VK\_MEMORY\_PROPERTY\_HOST\_VISIBLE\_BIT are considered mappable. Memory objects must be mappable in order to be successfully mapped on the host.

VkResult vkMapMemory( VkDevice device, VkDeviceMemory memory, VkDeviceSize offset, VkDeviceSize size,

VkMemoryMapFlags flags, =0 void\*\* ppData);

VkResult vkFlushMappedMemoryRanges( VkDevice device, uint32\_t memoryRangeCount,

const VkMappedMemoryRange\* pMemoryRanges); VkResult vkInvalidateMappedMemoryRanges( VkDevice device, uint32\_t memoryRangeCount,

const VkMappedMemoryRange\* pMemoryRanges); VkStructureType sType; P.15

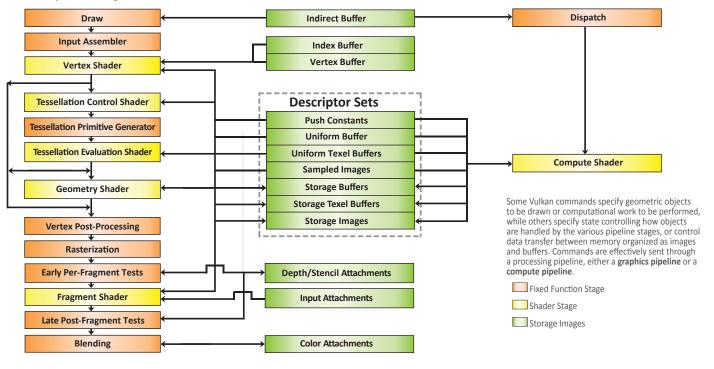
const void\* pNext; VkDeviceMemory memory; VkDeviceSize offset; VkDeviceSize size;

} VkMappedMemoryRange;

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#### Vulkan Pipeline Diagram [9]



## Command Buffer Lifecycle [5.1]

A command buffer is always in one of the five states shown below:

The state when a command buffer is first allocated. The command buffer may be reset back to this state from any of the executable, recording, or invalid states. Command buffers in the initial state can only be moved to recording, or freed.

#### **Recording state**

vkBeginCommandBuffer changes the state from initial to recording. Once in the recording state, **vkCmd\*** commands can be used to record to the command buffer.

#### **Executable state**

vkEndCommandBuffer moves a command buffer state from recording to executable

Executable command buffers can be submitted, reset, or recorded to another command buffer.

#### Pending state

Queue submission changes the state from executable to pending, in which applications must not attempt to modify the command buffer in any way. The state reverts back to executable when current executions complete, or to invalid.

#### Invalid state

Some operations will transition the command buffer into the invalid state, in which it can only be reset or freed.

#### Allocate Initia Reset Reset Begin Recording Invalid Invalidate Completion with One Time Submit End Completion Pending Executable Submission

#### Resource Creation [11]

Buffers represent linear arrays of data which are used for various purposes by binding them to a graphics or compute pipeline via descriptor sets or via certain commands, or by directly specifying them as parameters to certain commands.

#### VkResult vkCreateBuffer(

VkDevice device, const VkBufferCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkBuffer\* pBuffer);

typedef struct VkBufferCreateInfo {

VkStructureType sType; P.15

const void\* pNext; VkBufferCreateFlags flags;

VkDeviceSize size;

VkBufferUsageFlags usage; P.12

VkSharingMode sharingMode; P.15

uint32\_t queueFamilyIndexCount; const uint32\_t\* pQueueFamilyIndices;

} VkBufferCreateInfo;

VK\_BUFFER\_CREATE\_SPARSE\_X\_BIT where X is ALIASED, BINDING, PROTECTED, RESIDENCY

pNext must be NULL or point to:

VkExternalMemoryBufferCreateInfo P.12

#### void vkDestroyBuffer(

VkDevice device, VkBuffer buffer,

const VkAllocationCallbacks\* pAllocator); P.12

#### **Buffer Views [11.2]**

A buffer view represents a contiguous range of a buffer and a specific format to be used to interpret the data.

#### VkResult vkCreateBufferView(

VkDevice device, const VkBufferViewCreateInfo\* pCreateInfo,

const VkAllocationCallbacks\* pAllocator, P.12

VkBufferView\* pView);

typedef struct VkBufferViewCreateInfo {

VkStructureType sType; [215]
const void\* pNext;
VkBufferViewCreateFlags flags; = 0

VkBuffer buffer; VkFormat format; P.13

VkDeviceSize offset; VkDeviceSize range;

} VkBufferViewCreateInfo;

#### void vkDestroyBufferView(

VkDevice device,

VkBufferView bufferView,

const VkAllocationCallbacks\* pAllocator); P.12

#### Images [11.3]

Images represent multidimensional (up to 3) arrays of data which can be used for various purposes by binding them to the graphics or compute pipeline via descriptor sets, or by directly specifying them as parameters to certain commands.

#### VkResult vkCreateImage(

VkDevice device,

const VkImageCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12

VkImage\* plmage);

typedef struct **VkImageCreateInfo** { VkStructureType *sType*; P.15

const void\* pNext;

VkImageCreateFlags flags; P.13

VkImageType imageType; P.13

VkFormat format; P.13 VkExtent3D extent; PP.12

uint32\_t mipLevels; uint32\_t arrayLayers;

VkSampleCountFlagBits samples; P.15

VkImageTiling tiling; P.13

VkImageUsageFlags usage; P.13

VkSharingMode sharingMode; P.15

uint32\_t queueFamilyIndexCount; const uint32\_t\* pQueueFamilyIndices; VkImageLayout; P.13

} VkImageCreateInfo;

pNext must be NULL or point to:

VkExternalMemoryImageCreateInfo P.13

# typedef struct VkImageSwapchainCreateInfoKHR { VkStructureType sType; P.15

const void\* pNext; VkSwapchainKHR swapchain;

VkImageSwapchainCreateInfoKHR;

#### void vkGetImageSubresourceLayout(

VkDevice device, VkImage image, const VkImageSubresource\* pSubresource, VkSubresourceLayout\* pLayout);

# typedef struct VkImageSubresource {

VkImageAspectFlags aspectMask; P.13 uint32\_t mipLevel;

uint32\_t arrayLayer; } VkImageSubresource;

#### Continued on next page > typedef struct VkComponentMapping {

#### Resource Creation (continued)

typedef struct VkSubresourceLayout {

VkDeviceSize offset;

VkDeviceSize size;

VkDeviceSize rowPitch;

VkDeviceSize arrayPitch;

VkDeviceSize depthPitch;

} VkSubresourceLayout;

void **vkDestroyImage**( VkDevice *device*, VkImage *image*,

const VkAllocationCallbacks\* pAllocator); P.12

#### Image Views [11.5]

Image objects are not directly accessed by pipeline shaders for reading or writing image data. Instead, image views representing contiguous ranges of the image subresources and containing additional metadata are used for that purpose.

# VkResult vkCreateImageView( VkDevice device,

const VkAllocationCallbacks\* pAllocator, P.12

VkImageView\* pView);

#### typedef struct VkImageViewCreateInfo {

VkStructureType sType; P.15 const void\* pNext;

VkImageViewCreateFlags flags; =0

VkImage image; VkImageViewType viewType;

VkFormat format; [2]:3
VkComponentMapping components;
VkImageSubresourceRange subresourceRange; [2]:3 } VkImageViewCreateInfo;

viewType: VK\_IMAGE\_VIEW\_TYPE\_X where X is 1D, 2D, 3D, CUBE, 1D\_ARRAY, 2D\_ARRAY, CUBE\_ARRAY

pNext must be NULL or point to one of:

VkImageViewUsageCreateInfo P.13

VkSamplerYcbcrConversionInfo P.15

#### VkComponentSwizzle r;

VkComponentSwizzle g;

VkComponentSwizzle b;

VkComponentSwizzle a; } VkComponentMapping;

enum VkComponentSwizzle: VK COMPONENT SWIZZLE X where X is IDENTITY, ZERO, ONE, R, G, B, A

#### void vkDestroyImageView(VkDevice device,

VkImageView imageView,

const VkAllocationCallbacks\* pAllocator); P.12

#### Resource Memory Association [11.6]

Resources are initially created as virtual allocations with no backing memory. Device memory is allocated separately and then associated with the resource.

#### void vkGetBufferMemoryRequirements(

VkDevice device,

VkBuffer buffer,

VkMemoryRequirements\* pMemoryRequirements); P.13

## void vkGetBufferMemoryRequirements2(VkDevice device,

const VkBufferMemoryRequirementsInfo2\* plnfo, VkMemoryRequirements2\* pMemoryRequirements); P.13

#### typedef struct VkBufferMemoryRequirementsInfo2 {

VkStructureType sType; P.15 const void\* pNext;

VkBuffer buffer;

} VkBufferMemoryRequirementsInfo2;

#### void vkGetImageMemoryRequirements(

VkDevice *device*, VkImage *image*, VkMemoryRequirements\* *pMemoryRequirements*); [213]

#### void vkGetImageMemoryRequirements2(VkDevice device, const VkImageMemoryRequirementsInfo2\* pInfo,

VkMemoryRequirements2\* pMemoryRequirements); P.13

#### typedef struct VkImageMemoryRequirementsInfo2 {

VkStructureType sType; P.15

const void\* pNext;

VkImage *image*;
} VkImageMemoryRequirementsInfo2;

pNext must be NULL or point to:

VkImagePlaneMemoryRequirementsInfo P.13

## VkResult vkBindBufferMemory(VkDevice device, VkBuffer buffer, VkDeviceMemory memory, VkDeviceSize memoryOffset);

VkResult vkBindBufferMemory2(VkDevice device, uint32 t bindInfoCount, const VkBindBufferMemoryInfo\* pBindInfos);

# 

VkDeviceMemory memory; VkDeviceSize memoryOffset;

} VkBindBufferMemoryInfo;

pNext must be NULL or point to:

VkBindBufferMemoryDeviceGroupInfo P.12

## VkResult vkBindImageMemory(VkDevice device, VkImage image, VkDeviceMemory memory, VkDeviceSize memoryOffset);

VkResult vkBindImageMemory2(VkDevice device,

uint32\_t bindInfoCount, const VkBindImageMemoryInfo\* pBindInfos);

# typedef struct VkBindImageMemoryInfo {

VkStructureType sType; P.15 const void\* pNext;

VkImage *image*; VkDeviceMemory *memory*;

VkDeviceSize memoryOffset;

} VkBindImageMemoryInfo;

pNext must be NULL or point to one of:

VkBindImageMemoryDeviceGroupInfo
VkBindImagePlaneMemoryInfo
P.12

#### Samplers [12]

VkSampler objects encapsulate the state of an image sampler which is used by the implementation to read image data and apply filtering and other transformations for the shader.

#### VkResult vkCreateSampler(

VkDevice device,

const VkSamplerCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12

## VkSampler\* pSampler);

typedef struct VkSamplerCreateInfo {

VkStructureType sType; P.I.S const void\* pNext; VkSamplerCreateFlags flags; = 0 VkFilter magFilter; VkFilter minFilter;

VkSamplerMipmapMode *mipmapMode*; VkSamplerAddressMode *addressModeU*;

VkSamplerAddressMode addressModeV

VkSamplerAddressMode addressModeW;

float mipLodBias; VkBool32 anisotropyEnable;

float maxAnisotropy; VkBool32 compareEnable;

VkCompareOp; P.12 float minLod; float maxLod;

VkBorderColor borderColor;

VkBool32 unnormalizedCoordinates; } VkSamplerCreateInfo;

magFilter, minFilter: VK\_FILTER\_NEAREST, VK\_FILTER\_LINEAR

mipmapMode:

VK SAMPLER MIPMAP MODE {NEAREST, LINEAR}

borderColor: VK\_BORDER\_COLOR\_{FLOAT, INT}\_X where X is TRANSPARENT BLACK, OPAQUE BLACK,

OPAQUE WHITE

addressMode{U, V, W}:
VK\_SAMPLER\_ADDRESS\_MODE\_X where X is REPEAT,
MIRRORED\_REPEAT, MIRROR\_CLAMP\_TO\_EDGE, CLAMP\_TO\_EDGE, CLAMP\_TO\_BORDER

pNext must be NULL or point to:

VkSamplerYcbcrConversionInfo P.15

## void vkDestroySampler(

VkDevice device,

VkSampler sampler, const VkAllocationCallbacks\* pAllocator); P.12

## Sampler Y'C<sub>B</sub>C<sub>R</sub> Conversion [12.1]

## VkResult vkCreateSamplerYcbcrConversion(

const VkSamplerYcbcrConversionCreateInfo\*

pCreateInfo,

VkDevice device,

const VkAllocationCallbacks\* pAllocator, P.12 VkSamplerYcbcrConversion\* pYcbcrConversion);

# void vkDestroySamplerYcbcrConversion(VkDevice device, VkSamplerYcbcrConversion ycbcrConversion, const VkAllocationCallbacks\* pAllocator); [212]

## Resource Descriptors [13]

An opaque data structure representing a shader resource e.g., a buffer view, image view, sampler, or combined image sampler.

## Descriptor Set Layout [13.2.1]

VkResult vkCreateDescriptorSetLayout(

VkDevice device, const VkDescriptorSetLayoutCreateInfo\* pCreateInfo,P12 const VkAllocationCallbacks\* pAllocator, P.12

## VkDescriptorSetLayout\* pSetLayout);

const VkDescriptorSetLayoutCreateInfo\* pCreateInfo, P1512

#### typedef struct VkDescriptorSetLayoutSupport {

VkStructureType sType; P.15 void\* pNext; VkBool32 supported;

## void vkGetDescriptorSetLayoutSupport(

VkDevice device,

VkDescriptorSetLayoutSupport\* pSupport);

} VkDescriptorSetLayoutSupport;

#### void vkDestroyDescriptorSetLayout(

VkDevice device,

VkDescriptorSetLayout descriptorSetLayout, const VkAllocationCallbacks\* pAllocator); P.12

## Pipeline Layouts [13.2.2]

VkResult vkCreatePipelineLayout(

VkDevice device, const VkPipelineLayoutCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkPipelineLayout\* pPipelineLayout);

typedef struct VkPipelineLayoutCreateInfo {
 VkStructureType sType; P.15

const void\* pNext; VkPipelineLayoutCreateFlags flags; =0

uint32\_t setLayoutCount;

const VkDescriptorSetLayout\* pSetLayouts; uint32\_t pushConstantRangeCount; const VkPushConstantRange\* pPushConstantRanges;

} VkPipelineLayoutCreateInfo;

# VkStructureType sType; [315] const void\* pNext; VkFormat format; [213] VkSamplerYcbcrModelConversion ycbcrModel;

typedef struct VkSamplerYcbcrConversionCreateInfo {

VkSamplerYcbcrRange ycbcrRange;

VkComponentMapping components; VkChromaLocation xChromaOffset;

VkChromaLocation yChromaOffset;

VkFilter chromaFilter; VkBool32 forceExplicitReconstruction;

# } VkSamplerYcbcrConversionCreateInfo;

VkSamplerYcbcrModelConversion:
VK\_SAMPLER\_YCBCR\_MODEL\_CONVERSION\_X where X is {RGB, YCBCR}\_IDENTITY, YCBCR\_{709, 601, 2020}

VkSamplerYcbcrRange:
VK\_SAMPLER\_YCBCR\_RANGE\_ITU\_{FULL, NARROW}

VkChromaLocation: VK\_CHROMA\_LOCATION\_{COSITED\_EVEN, MIDPOINT}

VK\_FILTER\_{NEAREST, LINEAR}

#### typedef struct VkPushConstantRange { VkShaderStageFlags stageFlags; P.15 uint32\_t offset; uint32\_t size;

} VkPushConstantRange;

void vkDestroyPipelineLayout( VkDevice device, VkPipelineLayout pipelineLayout, const VkAllocationCallbacks\* pAllocator); P12

## Allocation of Descriptor Sets [13.2.3]

VkResult vkCreateDescriptorPool( VkDevice device, const VkDescriptorPoolCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkDescriptorPool\* pDescriptorPool);

## typedef struct VkDescriptorPoolCreateInfo {

uint32\_t maxSets; uint32\_t poolSizeCount; const VkDescriptorPoolSize\* pPoolSizes; } VkDescriptorPoolCreateInfo;

## Resource Descriptors (continued)

flags: VK\_DESCRIPTOR\_POOL\_CREATE\_FREE\_DESCRIPTOR\_SET\_BIT

#### typedef struct VkDescriptorPoolSize { VkDescriptorType type; P.12

uint32\_t descriptorCount; } VkDescriptorPoolSize;

#### void vkDestroyDescriptorPool(

VkDevice device,

VkDescriptorPool descriptorPool, const VkAllocationCallbacks\* pAllocator); P.12

#### VkResult vkAllocateDescriptorSets(

VkDevice device, const VkDescriptorSetAllocateInfo\* pAllocateInfo, VkDescriptorSet\* pDescriptorSets);

#### typedef struct VkDescriptorSetAllocateInfo {

VkStructureType sType; P.15 const void\* pNext; VkDescriptorPool descriptorPool; uint32\_t descriptorSetCount; const VkDescriptorSetLayout\* pSetLayouts;

} VkDescriptorSetAllocateInfo;

## VkResult vkFreeDescriptorSets(

VkDevice device,

VkDescriptorPool descriptorPool, uint32\_t descriptorSetCount, const VkDescriptorSet\* pDescriptorSets);

#### VkResult vkResetDescriptorPool(

VkDevice device, VkDescriptorPool descriptorPool, VkDescriptorPoolResetFlags flags);

#### Descriptor Set Updates [13.2.4]

#### void vkUpdateDescriptorSets(

VkDevice device, uint32\_t descriptorWriteCount, const VkWriteDescriptorSet\* pDescriptorWrites, uint32\_t descriptorCopyCount, const VkCopyDescriptorSet\* pDescriptorCopies);

vkDescriptorType descriptorType; [312]
const VkDescriptorImageInfo\* plmageInfo;
const VkDescriptorBufferInfo\* pBufferInfo; const VkBufferView\* pTexelBufferView;

} VkWriteDescriptorSet;

## Queries [16]

#### Query Pools [16.1]

#### VkResult vkCreateQueryPool(

VkDevice device,

const VkQueryPoolCreateInfo\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkQueryPool\* pQueryPool);

#### typedef struct VkQueryPoolCreateInfo {

peder struct vkQueryPoolcreateinio {

VkStructureType sType; P15

const void\* pNext;

VkQueryPoolCreateFlags flags; ■ 0

VkQueryType queryType;

uint32\_t queryCount;

VkQueryPipelineStatisticFlags pipelineStatistics; P15 } VkQueryPoolCreateInfo;

queryType: VK QUERY TYPE OCCLUSION, VK\_QUERY\_TYPE\_PIPELINE\_STATISTICS, VK\_QUERY\_TYPE\_TIMESTAMP

#### void vkDestroyQueryPool(

VkDevice device,

VkQueryPool queryPool, const VkAllocationCallbacks\* pAllocator); P.12

Query Operation [16.2] void vkCmdResetQueryPool( VkCommandBuffer commandBuffer, VkQueryPool queryPool,

uint32\_t firstQuery, uint32\_t queryCount);

void **vkCmdBeginQuery(**VkCommandBuffer commandBuffer,
VkQueryPool queryPool, uint32\_t entry, VkQueryControlFlags flags); flags: VK\_QUERY\_CONTROL\_PRECISE\_BIT

#### typedef struct VkDescriptorImageInfo {

VkSampler sampler; VkImageView imageView;

VkImageLayout imageLayout; P.13

} VkDescriptorImageInfo;

#### typedef struct VkDescriptorBufferInfo {

VkBuffer buffer; VkDeviceSize offset; VkDeviceSize range; } VkDescriptorBufferInfo;

typedef struct VkCopyDescriptorSet {
 VkStructureType sType; P.15
 const void\* pNext; VkDescriptorSet srcSet;
 uint32\_t srcBinding;

uint32\_t srcArrayElement;

VkDescriptorSet dstSet;

uint32\_t dstBinding;

uint32\_t dstArrayElement; uint32\_t descriptorCount;

} VkCopyDescriptorSet;

## Descriptor Set Updates with Templates [13.2.6]

VkResult vkCreateDescriptorUpdateTemplate(

VkDevice device, const VkDescriptorUpdateTemplateCreateInfo\* pCreateInfo,

const VkAllocationCallbacks\* pAllocator, P.12

VkDescriptorUpdateTemplate<sup>5</sup> pDescriptorUpdateTemplate);

# typedef struct VkDescriptorUpdateTemplateCreateInfo { VkStructureType sType; P.15

void\* pNext;

VkDescriptorUpdateTemplateCreateFlags flags;
uint32\_t descriptorUpdateEntryCount;
const VkDescriptorUpdateTemplateEntry\*

pDescriptorUpdateEntries;

VkDescriptorUpdateTemplateType templateType; VkDescriptorSetLayout descriptorSetLayout;

VkPipelineBindPoint pipelineBindPoint; P1315

VkPipelineLayout pipelineLayout; uint32\_t set; =0

} VkDescriptorUpdateTemplateCreateInfo;

VKPipelineBindPoint: VK PIPELINE BIND POINT X

where X is GRAPHICS, COMPUTE templateType: VK\_DESCRIPTOR\_UPDATE\_TEMPLATE\_-

TYPE DESCRIPTOR SET

#### typedef struct VkDescriptorUpdateTemplateEntry {

uint32\_t dstBinding; uint32\_t dstArrayElement;

uint32\_t descriptorCount; vkDescriptorType descriptorType; size\_t offset; size\_t stride; } VkDescriptorUpdateTemplateEntry;

#### void vkCmdEndQuery(

VkCommandBuffer commandBuffer,

VkQueryPool queryPool,

uint32\_t query);

#### VkResult vkGetQueryPoolResults(

VkDevice device,

VkQueryPool queryPool,

uint32\_t firstQuery, uint32\_t queryCount,

size\_t dataSize,

void\* pData,

VkDeviceSize stride,

VkQueryResultFlags flags);

flags: VK\_QUERY\_RESULT\_X\_BIT where X is 64, WAIT, WITH\_AVAILABILITY, PARTIAL

void **vkCmdCopyQueryPoolResults(** VkCommandBuffer *commandBuffer*,

VkQueryPool queryPool, uint32\_t firstQuery, uint32\_t queryCount, VkBuffer dstBuffer, VkDeviceSize dstOffset,

VkDeviceSize stride,

VkQueryResultFlags flags); flags: VK\_QUERY\_RESULT\_X\_BIT where X is 64, WAIT, WITH\_AVAILABILITY, PARTIAL

Timestamp Queries [16.5] void vkCmdWriteTimestamp(

VkCommandBuffer commandBuffer,

VkPipelineStageFlagBits pipelineStage, P.15
VkQueryPool queryPool,

uint32\_t query);

#### void vkDestroyDescriptorUpdateTemplate(

VkDevice device,

VkDescriptorUpdateTemplate

descriptorUpdateTemplate,
const VkAllocationCallbacks\* pAllocator); P.12

#### void vkUpdateDescriptorSetWithTemplate(

VkDevice device,

VkDescriptorSet descriptorSet,

VkDescriptorUpdateTemplate

descriptorUpdateTemplate,
const void\* pData);

#### **Descriptor Set Binding [13.2.7]**

void vkCmdBindDescriptorSets(

VkCommandBuffer commandBuffer, VkPipelineBindPoint pipelineBindPoint, P1315

The pipeline layout defines shader push constants which are updated via Vulkan commands rather than via writes to

## Clear Commands [17]

const VkImageSubresourceRange\* pRanges); P.13

VK\_IMAGE\_LAYOUT\_TRANSFER\_DST\_OPTIMAL,

VkCommandBuffer commandBuffer, uint32\_t attachmentCount, const VkClearAttachment\* pAttachments,

typedef struct VkClearRect {

## } VkClearRect;

typedef struct VkClearAttachment {

uint32\_t colorAttachment; VkClearValue clearValue; P.12

## } VkClearAttachment;

#### Filling Buffers [17.4]

VkCommandBuffer commandBuffer, VkBuffer dstBuffer, VkDeviceSize dstOffset,

void **vkCmdUpdateBuffer**( VkCmmandBuffer *commandBuffer*, VkBuffer *dstBuffer*, VkDeviceSize *dstOffset*,

VkPipelineLayout layout, P.15

uint32\_t firstSet, uint32\_t descriptorSetCount, const VkDescriptorSet\* pDescriptorSets, uint32\_t dynamicOffsetCount, const uint32\_t\* pDynamicOffsets);

Push Constant Updates [13.2.8]

memory or copy commands.

#### void vkCmdPushConstants(

VkCommandBuffer commandBuffer, VkPipelineLayout layout, P.15 VkShaderStageFlags stageFlags, P.15

uint32\_t offset, uint32\_t size, const void\* pValues);

## Outside a Render Pass Instance [17.1]

void **vkCmdClearColorImage(** VkCommandBuffer commandBuffer,

VkImage image, VkImageLayout imageLayout, P.13 const VkClearColorValue\* pColor, P.12

uint32\_t rangeCount,

imaaeLavout:

VK\_IMAGE\_LAYOUT\_TRANSFER\_DST\_OPTIMAL,
VK\_IMAGE\_LAYOUT\_GENERAL.

## VK IMAGE LAYOUT SHARED PRESENT KHR

void **vkCmdClearDepthStencilImage**( VkCommandBuffer *commandBuffer*,

VkImage image, VkImageLayout imageLayout, P.13 const VkClearDepthStencilValue\* pDepthStencil, P.12

uint32\_t rangeCount, const VkImageSubresourceRange\* pRanges); P.13

## VK\_IMAGE\_LAYOUT\_GENERAL

Inside a Render Pass Instance [17.2] void vkCmdClearAttachments(

# uint32\_t rectCount, const VkClearRect\* pRects);

VkRect2D rect; P.15 uint32\_t baseArrayLayer; uint32\_t layerCount;

## VkImageAspectFlags aspectMask; P13

void vkCmdFillBuffer(

#### VkDeviceSize size, uint32\_t data);

**Updating Buffers [17.5]** 

VkDeviceSize dataSize, const void\* pData);

#### **Drawing Commands** [19]

#### void vkCmdBindIndexBuffer(

VkCommandBuffer commandBuffer, VkBuffer buffer, VkDeviceSize offset, VkIndexType indexType); indexType: VK INDEX TYPE UINT{16, 32}

#### void vkCmdDraw(

VkCommandBuffer commandBuffer, uint32\_t vertexCount, uint32\_t instanceCount, uint32\_t firstVertex, uint32\_t firstInstance);

#### void vkCmdDrawIndexed(

VkCommandBuffer commandBuffer, uint32\_t indexCount, uint32\_t instanceCount, uint32\_t firstIndex, int32\_t vertexOffset, uint32 t firstInstance);

void vkCmdDrawIndirect( VkCommandBuffer commandBuffer, VkBuffer buffer, VkDeviceSize offset, uint32\_t drawCount, uint32 t stride);

#### typedef struct VkDrawIndirectCommand {

uint32\_t vertexCount; uint32\_t instanceCount; uint32\_t firstVertex; uint32\_t firstInstance; } VkDrawIndirectCommand;

void **vkCmdDrawIndexedIndirect**( VkCommandBuffer *commandBuffer*, VkBuffer *buffer*, VkDeviceSize *offset*, uint32\_t drawCount, uint32\_t stride);

#### typedef struct VkDrawIndexedIndirectCommand {

uint32\_t indexCount; uint32\_t instanceCount; uint32\_t firstIndex; int32\_t vertexOffset; uint32\_t firstInstance;

} VkDrawIndexedIndirectCommand;

#### Fixed-Function Vertex Postprocessing [23]

## Controlling the Viewport [23.5]

void vkCmdSetViewport(

VkCommandBuffer commandBuffer, uint32\_t firstViewport, uint32\_t viewportCount, const VkViewport\* pViewports); P.15

#### Rasterization [24]

## **Basic Line Segment Rasterization [24.6]**

void vkCmdSetLineWidth( VkCommandBuffer commandBuffer, float lineWidth):

#### Depth Bias [24.7.3]

void vkCmdSetDepthBias( VkCommandBuffer commandBuffer, float depthBiasConstantFactor, float depthBiasClamp, float depthBiasSlopeFactor);

#### Framebuffer: Blend Factors [26.1.1]

#### void vkCmdSetBlendConstants(

Sparse Resources [28]

VkCommandBuffer commandBuffer, const float blendConstants[4]);

Sparse Image Format Properties [28.7.3]

VkImageType type, P.13
VkSampleCountFlagBits samples, P.15

VkImageUsageFlags usage, P.13

VkSparseImageFormatFlags flags;

VkImageTiling tiling, [11]

uint32\_t\* pPropertyCount,

void vkGetPhysicalDeviceSparseImageFormatProperties( VkPhysicalDevice physicalDevice, VkFormat format, P.13

VkSparseImageFormatProperties\* pProperties);

typedef struct VkSparselmageFormatProperties {
 VkImageAspectFlags aspectMask; P.13
 VkExtent3D imageGranularity; P12

#### Copy Commands [18]

#### Copying Data Between Buffers [18.2]

void vkCmdCopyBuffer(

VkCommandBuffer commandBuffer, VkBuffer srcBuffer, VkBuffer dstBuffer, uint32\_t regionCount, const VkBufferCopy\* pRegions);

typedef struct **VkBufferCopy** {
 VkDeviceSize *srcOffset*; VkDeviceSize *dstOffset*; VkDeviceSize size; VkBufferCopy;

#### Copying Data Between Images [18.3]

void vkCmdCopyImage( VkCommandBuffer commandBuffer, Vkimage srcimage, VkimageLayout srcimageLayout, P.13 Vkimage dstimage, VkimageLayout dstimageLayout, P.13 uint32 t regionCount, const VkImageCopy\* pRegions);

#### typedef struct VkImageCopy {

VkImageSubresourceLayers srcSubresource; P.13
VkOffset3D srcOffset; P14

VkImageSubresourceLayers dstSubresource; P.13
VkOffset3D dstOffset; P.13

VkExtent3D extent; P12 VkImageCopy;

#### Copying Data Between Buffers and Images [18.4]

void vkCmdCopyBufferToImage(

VkCommandBuffer commandBuffer, VkBuffer srcBuffer, VkImage dstimage VkImageLayout dstImageLayout, P.13 uint32\_t regionCount, const VkBufferImageCopy\* pRegions);

#### Vertex Input Description [20.2]

#### void vkCmdBindVertexBuffers(

VkCommandBuffer commandBuffer, uint32\_t firstBinding, uint32\_t bindingCount, const VkBuffer\* pBuffers const VkDeviceSize\* pOffsets);

#### Fragment Operations [25]

#### Scissor Test [25.2]

void vkCmdSetScissor( VkCommandBuffer commandBuffer, uint32\_t firstScissor, uint32\_t scissorCount, const VkRect2D\* pScissors); P.15

#### Depth Bounds Test [25.8]

void vkCmdSetDepthBounds(

VkCommandBuffer commandBuffer, float minDepthBounds, float maxDepthBounds);

#### Stencil Test [25.9]

void vkCmdSetStencilCompareMask(

VkCommandBuffer commandBuffer VkStencilFaceFlags faceMask, uint32\_t compareMask);

#### void vkCmdSetStencilWriteMask(

VkCommandBuffer commandBuffer, VkStencilFaceFlags faceMask, uint32 t writeMask);

#### void vkCmdSetStencilReference(

VkCommandBuffer commandBuffer, VkStencilFaceFlags faceMask,

uint32 t reference);

faceMask: VK\_STENCIL\_FACE\_{FRONT, BACK}\_BIT, VK\_STENCIL\_FRONT\_AND\_BACK

# flags: VK\_SPARSE\_IMAGE\_FORMAT\_X where X is SINGLE\_MIPTAIL\_BIT, ALIGNED\_MIP\_SIZE\_BIT, NONSTANDARD\_BLOCK\_SIZE\_BIT

void vkGetPhysicalDeviceSparseImageFormatProperties2( VkPhysicalDevice physicalDevice, const VkPhysicalDeviceSparseImageFormatInfo2\*

pFormatInfo, uint32\_t\* pPropertyCount, VkSparseImageFormatProperties2\* pProperties);

## typedef struct VkSparseImageFormatProperties2 {

VkStructureType sType; P.15

void\* pNext;

VkSparseImageFormatProperties properties; } VkSparseImageFormatProperties2;

# void **vkCmdCopyImageToBuffer(** VkCommandBuffer *commandBuffer*,

Vklmage srclmage, VklmageLayout srclmageLayout, P.13

VkBuffer dstBuffer,

uint32\_t regionCount const VkBufferImageCopy\* pRegions);

#### typedef struct VkBufferImageCopy { VkDeviceSize bufferOffset;

uint32\_t bufferRowLength; uint32\_t bufferImageHeight;

VklmageSubresourceLayers imageSubresource; P.13
VkOffset3D imageOffset; P.14
VkExtent3D imageExtent; P.12

} VkBufferImageCopy;

## Image Copies With Scaling [18.5]

void **vkCmdBlitImage**( VkCommandBuffer commandBuffer, VkImage srcImage, VkImageLayout srcImageLayout, P.13 Vklmage dstlmage, VklmageLayout dstlmageLayout, P.13 uint32 t regionCount, const VkImageBlit\* pRegions,

VkFilter filter); filter: VK\_FILTER\_NEAREST, VK\_FILTER\_LINEAR

#### typedef struct VkImageBlit {

VkImageSubresourceLayers srcSubresource; P.13 VkOffset3D srcOffsets[2]; P14 VkImageSubresourceLayers dstSubresource; P.13

VkOffset3D dstOffsets[2]; P.13 } VkImageBlit;

## Resolving Multisample Images [18.6]

void vkCmdResolveImage( VkCommandBuffer commandBuffer,

VkImage srcImage, VkImageLayout srcImageLayout, P.13

Vklmage dstlmage, VklmageLayout dstlmageLayout, P.13

uint32 t regionCount, const VkImageResolve\* pRegions);

#### typedef struct VkImageResolve {

VkImageSubresourceLayers srcSubresource; P.13
VkOffset3D srcOffset; P14

VkImageSubresourceLayers dstSubresource; P.13
VkOffset3D dstOffset; P14
VkExtent3D extent; P12

VkImageResolve;

## Dispatching Commands [27]

#### void vkCmdDispatch(

VkCommandBuffer commandBuffer, uint32\_t groupCountX, uint32 t groupCountY uint32\_t groupCountZ);

## void vkCmdDispatchIndirect(

VkCommandBuffer commandBuffer, VkBuffer buffer,

VkDeviceSize offset);

## typedef struct VkDispatchIndirectCommand {

uint32\_t x; uint32 t y; uint32\_t z;

} VkDispatchIndirectCommand;

void vkCmdDispatchBase( VkCommandBuffer commandBuffer, uint32\_t baseGroupX, uint32\_t baseGroupY, uint32\_t baseGroupZ, uint32\_t groupCountX, uint32\_t groupCountY, uint32\_t groupCountZ);

# typedef struct VkPhysicalDeviceSparseImageFormatInfo2 { VkStructureType sType; P.15 const void\* pNext; VkFormat format, P.13

VkImageType type, P.13
VkSampleCountFlagBits samples, P.15 VkImageUsageFlags usage, P.13

VkImageTiling tiling, P.13 VkPhysicalDeviceSparseImageFormatInfo2;

#### Sparse Resource Memory Requirements [28.7.5]

void vkGetImageSparseMemoryRequirements(

VkDevice device, VkImage image, uint32\_t\* pSparseMemoryRequirementCount, VkSparseImageMemoryRequirements\* pSparseMemoryRequirements)

Continued on next page >

} VkSparseImageFormatProperties;

#### Sparse Resources (continued)

typedef struct VkSparseImageMemoryRequirements { VkSparseImageFormatProperties formatProperties; uint32\_t imageMipTailFirstLod; VkDeviceSize imageMipTailSize; VkDeviceSize imageMipTailOffset; VkDeviceSize imageMipTailStride; } VkSparseImageMemoryRequirements;

#### void vkGetImageSparseMemoryRequirements2( VkDevice device,

const VkImageSparseMemoryRequirementsInfo2\* pInfo, uint32 t\* pSparseMemoryRequirementCount, VkSparseImageMemoryRequirements2\* pSparseMemoryRequirements);

#### typedef struct VkImageSparseMemoryRequirementsInfo2 {

VkStructureType *sType*; P.15 const void\* *pNext*; VkImage image; } VkImageSparseMemoryRequirementsInfo2;

typedef struct VkSparseImageMemoryRequirements2 { VkStructureType sType; P.15 VkSparseImageMemoryRequirements

memoryRequirements;

} VkSparseImageMemoryRequirements2;

#### **Binding Resource Memory [28.7.6]**

typedef struct VkBindSparseInfo { VkStructureType sType; 1.15 const void\* pNext; uint32\_t waitSemaphoreCount; const VkSemaphore\* pWaitSemaphores; uint32\_t bufferBindCount; const VkSparseBufferMemoryBindInfo\* pBufferBinds; uint32\_t imageOpaqueBindCount; const VkSparseImageOpaqueMemoryBindInfo\* plmageOpaqueBinds; uint32\_t imageBindCount;

const VkSparseImageMemoryBindInfo\* plmageBinds; uint32\_t signalSemaphoreCount; const VkSemaphore\* pSignalSemaphores; } VkBindSparseInfo;

pNext must be NULL or point to: VkDeviceGroupBindSparseInfo P.12

## typedef struct VkSparseBufferMemoryBindInfo {

VkBuffer buffer; uint32 t bindCount; const VkSparseMemoryBind\* pBinds; P.15 } VkSparseBufferMemoryBindInfo;

typedef struct VkSparseImageOpaqueMemoryBindInfo { VkImage image uint32\_t bindCount; const VkSparseMemoryBind\* pBinds; P.15

} VkSparseImageOpaqueMemoryBindInfo;

typedef struct VkSparselmageMemoryBindInfo { VkImage image;

uint32\_t bindCount; const VkSparseImageMemoryBind\* pBinds; } VkSparseImageMemoryBindInfo;

# typedef struct VkSparseImageMemoryBind { VkImageSubresource subresource;

VkOffset3D offset; P1 VkExtent3D extent; P12 VkDeviceMemory memory; VkDeviceSize memoryOffset; VkSparseMemoryBindFlags flags; } VkSparseImageMemoryBind;

flags: VK SPARSE MEMORY BIND METADATA BIT

#### VkResult vkQueueBindSparse(

VkQueue queue, uint32 t bindInfoCount, const VkBindSparseInfo\* pBindInfo, VkFence fence);

#### Window System Integration (WSI) [29]

#### Android Platform [29.2.1]

#### VkResult vkCreateAndroidSurfaceKHR(

VkInstance instance, const VkAndroidSurfaceCreateInfoKHR\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12
VkSurfaceKHR\* pSurface);

typedef struct VkAndroidSurfaceCreateInfoKHR {

VkStructureType sType; P.15 const void\* pNext; VkAndroidSurfaceCreateFlagsKHR *flags*; =0 struct ANativeWindow\* *window*; } VkAndroidSurfaceCreateInfoKHR;

#### Wayland Platform [29.2.3]

#### VkResult vkCreateWaylandSurfaceKHR(

VkInstance instance, const VkWaylandSurfaceCreateInfoKHR\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkSurfaceKHR\* pSurface);

typedef struct VkWaylandSurfaceCreateInfoKHR {

VkStructureType sType; P.15 const void\* pNext; VkWaylandSurfaceCreateFlagsKHR flags; =0 struct wl\_display\* display; struct wl\_surface\* surface; } VkWaylandSurfaceCreateInfoKHR;

#### Win32 Platform [29.2.4]

#### VkResult vkCreateWin32SurfaceKHR(

VkInstance instance, const VkWin32SurfaceCreateInfoKHR\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkSurfaceKHR\* pSurface);

HINSTANCE hinstance; HWND hwnd; } VkWin32SurfaceCreateInfoKHR;

#### XCB Platform [29.2.5]

#### VkResult vkCreateXcbSurfaceKHR(

VkInstance instance, const VkXcbSurfaceCreateInfoKHR\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkSurfaceKHR\* pSurface);

#### typedef struct VkXcbSurfaceCreateInfoKHR {

VkStructureType sType; P.15 const void\* pNext; VkXcbSurfaceCreateFlagsKHR flags; =0 xcb connection t\* connection; xcb window t window; } VkXcbSurfaceCreateInfoKHR;

#### Xlib Platform [29.2.6]

#### VkResult vkCreateXlibSurfaceKHR(

VkInstance instance, const VkXlibSurfaceCreateInfoKHR\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkSurfaceKHR\* pSurface);

## typedef struct VkXlibSurfaceCreateInfoKHR {

VkStructureType sType; P.15 const void\* pNext; VkXlibSurfaceCreateFlagsKHR flags; =0 Display\* dpy; Window window; } VkXlibSurfaceCreateInfoKHR;

#### Platform-Independent Information [29.2.7]

void vkDestroySurfaceKHR( VkInstance instance, VkSurfaceKHR surface, const VkAllocationCallbacks\* pAllocator); P.12

#### Display Enumeration [29.3.1]

VkResult vkGetPhysicalDeviceDisplayPropertiesKHR( VkPhysicalDevice physicalDevice, uint32\_t\* pPropertyCount, VkDisplayPropertiesKHR\* pProperties);

typedef struct VkDisplayPropertiesKHR {
 VkDisplayKHR display; const char\* displayName;
 VkExtent2D physicalDimensions; P12
 VkExtent2D physicalResolution; P12 VkSurfaceTransformFlagsKHR supportedTransforms; P.15 VkBool32 planeReorderPossible; VkBool32 persistentContent;

} VkDisplayPropertiesKHR; VkResult vkGetPhysicalDeviceDisplayProperties2KHR (

VkPhysicalDevice physicalDevice, uint32\_t\* pPropertyCount, VkDisplayProperties2KHR\* pProperties);

typedef struct VkDisplayProperties2KHR {

VkStructureType sType; P.15 void\* pNext; VkDisplayPropertiesKHR displayProperties; } VkDisplayProperties2KHR;

#### **Display Planes**

# VkResult vkGetPhysicalDeviceDisplayPlanePropertiesKHR(

VkPhysicalDevice physicalDevice, uint32\_t\* pPropertyCount, VkDisplayPlanePropertiesKHR\* pProperties);

typedef struct VkDisplayPlanePropertiesKHR { VkDisplayKHR currentDisplay; uint32\_t currentStackIndex;

#### } VkDisplayPlanePropertiesKHR; VkResult vkGetPhysicalDeviceDisplayPlaneProperties2KHR (

VkPhysicalDevice physicalDevice, uint32\_t\* pPropertyCount, VkDisplayPlaneProperties2KHR\* pProperties);

#### typedef struct VkDisplayPlaneProperties2KHR { VkStructureType sType; P.15

void\* pNext; VkDisplayPlanePropertiesKHR displayPlaneProperties; } VkDisplayPlaneProperties2KHR;

VkResult vkGetDisplayPlaneSupportedDisplaysKHR( VkPhysicalDevice physicalDevice, uint32\_t planeIndex, uint32\_t\* pDisplayCount, VkDisplayKHR\* pDisplays);

#### **Display Modes**

#### VkResult vkGetDisplayModePropertiesKHR(

VkPhysicalDevice physicalDevice, VkDisplayKHR display, uint32\_t\* pPropertyCount, VkDisplayModePropertiesKHR\* pProperties);

#### typedef struct VkDisplayModePropertiesKHR {

VkDisplayModeKHR displayMode; VkDisplayModeParametersKHR parameters; } VkDisplayModePropertiesKHR;

## typedef struct VkDisplayModeParametersKHR {

VkExtent2D visibleRegion; P12 uint32 t refreshRate; } VkDisplayModeParametersKHR;

#### VkResult vkGetDisplayModeProperties2KHR (

VkPhysicalDevice physicalDevice, VkDisplayKHR display, uint32\_t\* pPropertyCount, VkDisplayModeProperties2KHR\* pProperties);

#### typedef struct VkDisplayModeProperties2KHR {

VkStructureType sType; P.15 void\* pNext;

VkDisplayModePropertiesKHR displayModeProperties; } VkDisplayModeProperties2KHR;

VkResult vkCreateDisplayModeKHR(
VkPhysicalDevice physicalDevice, VkDisplayKHR display, const VkDisplayModeCreateInfoKHR\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12
VkDisplayModeKHR\* pMode);

#### typedef struct VkDisplayModeCreateInfoKHR {

VkStructureType sType; 215 const void\* pNext; VkDisplayModeCreateFlagsKHR flags; =0 VkDisplayModeParametersKHR parameters; } VkDisplayModeCreateInfoKHR;

#### VkResult vkGetDisplayPlaneCapabilitiesKHR(

VkPhysicalDevice physicalDevice, VkDisplayModeKHR mode, uint32\_t planeIndex, VkDisplayPlaneCapabilitiesKHR\* pCapabilities);

## typedef struct VkDisplayPlaneCapabilitiesKHR

VkDisplayPlaneAlphaFlagsKHR supportedAlpha; VkOffset2D minSrcPosition; P14 VkOffset2D maxSrcPosition; P14 VkExtent2D minSrcExtent; P12 VkExtent2D maxSrcExtent; P12 VkOffset2D minDstPosition; P14 VkOffset2D maxDstPosition; P14 VkExtent2D minDstExtent; P12 VkExtent2D maxDstExtent; P12 } VkDisplayPlaneCapabilitiesKHR;

#### VkResult vkGetDisplayPlaneCapabilities2KHR (

VkPhysicalDevice physicalDevice, const VkDisplayPlaneInfo, VkDisplayPlaneCapabilities2KHR\* pCapabilities);

#### typedef struct VkDisplayPlaneInfo2KHR { VkStructureType sType; P15

const void\* pNext; VkDisplayModeKHR mode; uint32\_t planeIndex; } VkDisplayPlaneInfo2KHR;

## typedef struct VkDisplayPlaneCapabilities2KHR {

VkStructureType sType; P.15 void\* pNext; VkDisplayPlaneCapabilitiesKHR capabilities; } VkDisplayPlaneCapabilities2KHR;

#### WSI (continued)

#### Display Surfaces [29.3.2]

## VkResult vkCreateDisplayPlaneSurfaceKHR(

VkInstance instance,

const VkDisplaySurfaceCreateInfoKHR\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkSurfaceKHR\* pSurface);

typedef struct VkDisplaySurfaceCreateInfoKHR {
 VkStructureType sType; P.15
 const void\* pNext;
 VkDisplaySurfaceCreateFlagsKHR flags; =0
 VkDisplayModeKHR displayMode;
 uint32 t planeIndex; uint32 t planeStackIndex;
 VkSurfaceTransformFlagBitsKHR transform; P.15
 float alahataInha; float globalAlpha;

VkDisplayPlaneAlphaFlagBitsKHR alphaMode; VkExtent2D imageExtent; P12

} VkDisplaySurfaceCreateInfoKHR;

alphaMode: VK\_DISPLAY\_PLANE\_ALPHA\_X\_BIT\_KHR where X is OPAQUE, GLOBAL, PER\_PIXEL, PER\_PIXEL\_PREMULTIPLIED

#### Querying for WSI Support [29.4]

VkResult vkGetPhysicalDeviceSurfaceSupportKHR( VkPhysicalDevice physicalDevice, uint32\_t queueFamilyIndex, VkSurfaceKHR surface, VkBool32\* pSupported);

#### Wayland Platform Querying [29.4.3]

VkBool32

vkGetPhysicalDeviceWaylandPresentationSupportKHR(

VkPhysicalDevice physicalDevice, uint32\_t queueFamilyIndex, struct wl\_display\* display);

#### Win32 Platform Querying [29.4.4]

VkBool32

vkGetPhysicalDeviceWin32PresentationSupportKHR(

VkPhysicalDevice physicalDevice, uint32\_t queueFamilyIndex);

#### XCB Platform Querying [29.4.5]

VkBool32

#### vkGetPhysicalDeviceXcbPresentationSupportKHR(

VkPhysicalDevice physicalDevice, uint32 t queueFamilyIndex, xcb\_connection\_t\* connection, xcb\_visualid\_t visual\_id);

#### Xlib Platform Querying [29.4.6]

#### vk Get Physical Device X lib Presentation Support KHR (

VkPhysicalDevice physicalDevice, uint32\_t queueFamilyIndex, Display\* dpy, VisualID visualID);

#### Surface Queries [29.5]

## VkResult vkGetPhysicalDeviceSurfaceCapabilitiesKHR( VkPhysicalDevice physicalDevice, VkSurfaceKHR surface, VkSurfaceCapabilitiesKHR\* pSurfaceCapabilities); P.15

VkResult vkGetPhysicalDeviceSurfaceCapabilities2KHR( VkPhysicalDevice physicalDevice, const VkPhysicalDeviceSurfaceInfo2KHR\* pSurfaceInfo, P.15 VkSurfaceCapabilities2KHR\* pSurfaceCapabilities);

#### typedef struct VkSurfaceCapabilities2KHR {

VkStructureType sType; P.15 void\* *pNext*;

VkSurfaceCapabilitiesKHR surfaceCapabilities; P.15 } VkSurfaceCapabilities2KHR;

pNext must be NULL or point to: VkSharedPresentSurfaceCapabilitiesKHR

# typedef struct VkSharedPresentSurfaceCapabilitiesKHR { VkStructureType sType; P.15

void\* pNext;

VklmageUsageFlags sharedPresentSupportedUsageFlags; P.13 } VkSharedPresentSurfaceCapabilitiesKHR;

## VkResult vkGetPhysicalDeviceSurfaceFormatsKHR(

VkPhysicalDevice physicalDevice, VkSurfaceKHR surface, uint32 <u>t</u>\* *pSurfaceFormatCount*, VkSurfaceFormatKHR\* *pSurfaceFormats*); P.15

VkResult vkGetPhysicalDeviceSurfaceFormats2KHR( VkPhysicalDevice physicalDevice, const VkPhysicalDeviceSurfaceInfo2KHR\* pSurfaceInfo, 2.15 uint32\_t\* pSurfaceFormatCount, VkSurfaceFormat2KHR\* pSurfaceFormats);

## typedef struct VkSurfaceFormat2KHR {

VkStructureType sType; P.15 void\* pNext; VkSurfaceFormatKHR surfaceFormat; P.15 } VkSurfaceFormat2KHR:

VkResult vkGetPhysicalDeviceSurfacePresentModesKHR( VkPhysicalDevice physicalDevice, VkSurfaceKHR surface, uint32\_t\* pPresentModeCount, VkPresentModeKHR\* pPresentModes);

pPresentModes: VK\_PRESENT\_MODE\_X\_KHR where X is IMMEDIATE, MAILBOX, FIFO, FIFO\_RELAXED, SHARED\_DEMAND\_REFRESH, SHARED\_CONTINUOUS\_REFRESH

#### Device Group Queries [29.6]

#### VkResult vkGetDeviceGroupPresentCapabilitiesKHR(

VkDevice device, VkDeviceGroupPresentCapabilitiesKHR\* pDeviceGroupPresentCapabilities);

## typedef struct VkDeviceGroupPresentCapabilitiesKHR {

VkStructureType sType; P15 const void\* pNext; uint32\_t presentMask[VK\_MAX\_DEVICE\_GROUP\_SIZE];

VkDeviceGroupPresentModeFlagsKHR modes; P.12 } VkDeviceGroupPresentCapabilitiesKHR;

VkResult vkGetDeviceGroupSurfacePresentModesKHR( VkDevice device, VkSurfaceKHR surface, VkDeviceGroupPresentModeFlagsKHR\* pModes); 212

#### VkResult vkGetPhysicalDevicePresentRectanglesKHR(

VkPhysicalDevice physicalDevice, VkSurfaceKHR surface, uint32\_t\* pRectCount, VkRect2D\* pRects); P.15

#### WSI Swapchain [29.7]

#### VkResult vkGetSwapchainStatusKHR(

VkDevice device,

VkSwapchainKHR swapchain);

#### VkResult vkCreateSwapchainKHR(

VkDevice device, const VkSwapchainCreateInfoKHR\* pCreateInfo, const VkAllocationCallbacks\* pAllocator, P.12 VkSwapchainKHR\* pSwapchain);

typedef struct VkSwapchainCreateInfoKHR {
 VkStructureType sType; PIS
 const void\* pNext;
 VkSwapchainCreateFlagsKHR flags;
 VkSurfaceKHR surface; uint32 t minImageCount;
 VkFormat imageFormat; PIS
 VkColorSpaceKHR imageColorSpace;

VkExtent2D imageExtent; P12

uint32\_t imageArrayLayers;

VkImageUsageFlags imageUsage; P.13 VkSharingMode imageSharingMode; P.15

VKSnaringMode ImagesnaringMode; [215]
uint32\_t queueFamilyIndexCount;
const uint32\_t\* pQueueFamilyIndices;
VKSurfaceTransformFlagBitsKHR preTransform; [215]
VKCompositeAlphaFlagBitsKHR compositeAlpha; [212]
VKPresentModeKHR presentMode;
VKBool32 clipped; VKSwapchainKHR oldSwapchain;
VKSwapchainGreateInfoKHR.

} VkSwapchainCreateInfoKHR;

pNext: may point to struct:

VkDeviceGroupSwapchainCreateInfoKHR

flags: VK\_SWAPCHAIN\_CREATE\_X\_KHR where X is ŠPLIT\_INSTANCE\_BIND\_REGIONS, PROTECTED

colorSpace: VK\_COLOR\_SPACE\_SRGB\_NONLINEAR\_KHR presentMode: VK PRESENT MODE X KHR

where X is IMMEDIATE, MAILBOX, FIFO, FIFO\_RELAXED, DEMAND\_REFRESH, CONTINUOUS\_REFRESH

# typedef struct VkDeviceGroupSwapchainCreateInfoKHR {

VkStructureType sType; P.15 const void\* pNext;

VkDeviceGroupPresentModeFlagsKHR modes; } VkDeviceGroupSwapchainCreateInfoKHR;

modes: VK\_DEVICE\_GROUP\_PRESENT\_MODE\_X\_BIT\_KHR where X is LOCAL, REMOTE, SUM, LOCAL\_MULTI\_DEVICE

#### void vkDestroySwapchainKHR(

VkDevice device, VkSwapchainKHR swapchain, const VkAllocationCallbacks\* pAllocator); P.12

#### VkResult vkCreateSharedSwapchainsKHR(

VkDevice device, uint32\_t swapchainCount, const VkSwapchainCreateInfoKHR\* pCreateInfos, const VkAllocationCallbacks\* pAllocator, P.12 VkSwapchainKHR\* pSwapchains);

VkResult **vkGetSwapchainImagesKHR**( VkDevice *device*, VkSwapchainKHR *swapchain*, uint32\_t\* pSwapchainImageCount, VkImage\* pSwapchainImages);

#### VkResult vkAcquireNextImageKHR(

VkDevice device, VkSwapchainKHR swapchain, uint64 t timeout, VkSemaphore semaphore, VkFence fence, uint32\_t\* plmageIndex);

#### VkResult vkAcquireNextImage2KHR(

VkDevice device, const VkAcquireNextImageInfoKHR\* pAcquireInfo, uint32\_t\* plmageIndex);

#### typedef struct VkAcquireNextImageInfoKHR {

VkStructureType *sType*; P.15 const void\* *pNext*; VkSwapchainKHR swapchain; uint64\_t timeout; VkSemaphore semaphore; VkFence fence; uint32\_t deviceMask;

} VkAcquireNextImageInfoKHR;

#### VkResult vkQueuePresentKHR(

VkQueue queue,

const VkPresentInfoKHR\* pPresentInfo);

#### typedef struct VkPresentInfoKHR {

pNext must be NULL or point to one of:

VkDeviceGroupPresentInfoKHR, VkDisplayPresentInfoKHR, or VkPresentRegionsKHR

## typedef struct VkDeviceGroupPresentInfoKHR {

VkStructureType sType; [215] const void\* pNext; uint32\_t swapchainCount;

const uint32 <u>t</u>\* *pDeviceMasks*; VkDeviceGroupPresentModeFlagBitsKHR *mode*;

} VkDeviceGroupPresentInfoKHR;

mode: VK\_DEVICE\_GROUP\_PRESENT\_MODE\_X\_BIT\_KHR where X is REMOTE, SUM, LOCAL, LOCAL\_MULTI\_DEVICE

#### typedef struct VkDisplayPresentInfoKHR {

VkStructureType sType; P.15 const void\* pNext; VkRect2D srcRect; P.15

VkRect2D dstRect; P.15 VkBool32 persistent; } VkDisplayPresentInfoKHR;

typedef struct VkPresentRegionsKHR {

VkStructureType sType; P.15 const void\* pNext; uint32\_t swapchainCount; const VkPresentRegionKHR\* pRegions;

## typedef struct VkPresentRegionKHR {

uint32\_t rectangleCount; const VkRectLayerKHR\* pRectangles; } VkPresentRegionKHR;

#### typedef struct VkRectLayerKHR { VkOffset2D offset; P.14

VkExtent2D extent; P.12 uint32\_t layer; } VkRectLayerKHR;

} VkPresentRegionsKHR;

#### VkBool32 persistent; } VkDisplayPresentInfoKHR;

## **Extended Functionality**

#### Layers [30.1]

VkResult vkEnumerateInstanceLayerProperties( uint32\_t\* pPropertyCount, VkLayerProperties\* pProperties);

VkResult vkEnumerateDeviceLayerProperties( VkPhysicalDevice physicalDevice, uint32\_t\* pPropertyCount, VkLayerProperties\* pProperties);

typedef struct VkLayerProperties {
 char layerName [VK\_MAX\_EXTENSION\_NAME\_SIZE]; uint32\_t specVersion; uint32\_t implementationVersion; char description [VK\_MAX\_DESCRIPTION\_SIZE]; } VkLayerProperties;

#### Extensions [30.2]

# VkResult vkEnumerateInstanceExtensionProperties( const char\* pLayerName, uint32\_t\* pPropertyCount, VkExtensionProperties\* pProperties);

#### VkResult vkEnumerateDeviceExtensionProperties( VkPhysicalDevice physicalDevice,

const char\* pLayerName, uint32\_t\* pPropertyCount, VkExtensionProperties\* pProperties);

> Continued on next page > www.khronos.org/vulkan

#### **Extended Functionality (continued)**

typedef struct VkExtensionProperties { char extensionName [VK\_MAX\_EXTENSION\_NAME\_SIZE]; uint32\_t specVersion, } VkExtensionProperties;

#### Additional Buffer Capabilities [31.5]

void vkGetPhysicalDeviceExternalBufferProperties( VkPhysicalDevice physicalDevice, const VkPhysicalDeviceExternalBufferInfo\* pExternalBufferInfo, VkExternalBufferProperties\* pExternalBufferProperties);

#### typedef struct VkPhysicalDeviceExternalBufferInfo {

VkStructureType sType; P.15 const void\* pNext; VkBufferCreateFlags flags; P.12 VkBufferUsageFlags usage; P.12
VkExternalMemoryHandleTypeFlagBits handleType; P.12 } VkPhysicalDeviceExternalBufferInfo;

typedef struct VkExternalBufferProperties { VkStructureType sType; P.15

void\* pNext; VkExternalMemoryProperties externalMemoryProperties; [2.13] } VkExternalBufferProperties;

#### **Optional Semaphore Capabilities [31.6]**

void vkGetPhysicalDeviceExternalSemaphoreProperties( VkPhysicalDevice *physicalDevice*, const VkPhysicalDeviceExternalSemaphoreInfo\* pExternalSemaphoreInfo, VkExternalSemaphoreProperties\* pExternalSemaphoreProperties);

handleType; P.13
} VkPhysicalDeviceExternalSemaphoreInfo;

enum VkExternalSemaphoreHandleTypeFlagBits: VK\_EXTERNAL\_SEMAPHORE\_HANDLE\_TYPE\_X\_BIT where X is OPAQUE\_FD, OPAQUE\_WIN32[\_KMT], D3D12\_FENCE, SYNC\_FD

#### typedef struct VkExternalSemaphoreProperties {

VkStructureType sType; P.15 void\* pNext;

VkExternalSemaphoreHandleTypeFlags exportFromImportedHandleTypes; P.13 VkExternalSemaphoreHandleTypeFlags compatibleHandleTypes;

VkExternalSemaphoreFeatureFlags externalSemaphoreFeatures; } VkExternalSemaphoreProperties;

VkExternalSemaphoreFeatureFlagBits: VK\_EXTERNAL\_SEMAPHORE\_FEATURE\_X\_BIT where X is EXPORTABLE, IMPORTABLE

#### Optional Fence Capabilities [31.7]

void vkGetPhysicalDeviceExternalFenceProperties( VkPhysicalDevice physicalDevice, const VkPhysicalDeviceExternalFenceInfo\* pExternalFenceInfo, VkExternalFenceProperties\* pExternalFenceProperties);

typedef struct VkPhysicalDeviceExternalFenceInfo {

VkStructureType sType; P.15 const void\* pNext;

VkExternalFenceHandleTypeFlagBits handleType; P.12 } VkPhysicalDeviceExternalFenceInfo;

typedef struct VkExternalFenceProperties {

VkStructureType sType; P.15 void\* *pNext*; VkExternalFenceHandleTypeFlags exportFromImportedHandleTypes; P.12

VkExternalFenceHandleTypeFlags compatibleHandleTypes; P.12

} VkExternalFenceProperties;

enum VkExternalFenceFeatureFlagBits: VK\_EXTERNAL\_FENCE\_FEATURE\_X\_BIT where X is EXPORTABLE, IMPORTABLE

#### Features, Limits, and Formats [31]

#### Features [31.1]

void vkGetPhysicalDeviceFeatures( VkPhysicalDevice *physicalDevice*, VkPhysicalDeviceFeatures\* *pFeatures*); P14

#### void vkGetPhysicalDeviceFeatures2(

VkPhysicalDevice physicalDevice, VkPhysicalDeviceFeatures2\* pFeatures); P.14

#### Format Properties [31.3.2]

void vkGetPhysicalDeviceFormatProperties( VkPhysicalDevice physicalDevice, VkFormat format, P.13 VkFormatProperties\* pFormatProperties);

typedef struct VkFormatProperties {

VkFormatFeatureFlags linearTilingFeatures; VkFormatFeatureFlags optimalTilingFeatures; VkFormatFeatureFlags bufferFeatures; VkFormatProperties;

enum VkFormatFeatureFlagBits:

VK\_FORMAT\_FEATURE\_X\_BIT where X is SAMPLED\_IMAGE, STORAGE\_IMAGE[\_ATOMIC], SAMPLED\_IMAGE, STORAGE\_IMAGE\_ATOMIC],
UNIFORM\_TEXEL\_BUFFER,
STORAGE\_TEXEL\_BUFFER\_ATOMIC],
VERTEX\_BUFFER, COLOR\_ATTACHMENT[\_BLEND],
DEPTH\_STENCIL\_ATTACHMENT,
SAMPLED\_IMAGE\_FILTER\_LINEAR, DISJOINT,
BLIT\_{SRC, DST}, TRANSFER\_{SRC, DST},
{MIDPOINT, COSITED}\_CHROMA\_SAMPLES,
and VK\_FORMAT\_FEATURE\_SAMPLED\_IMAGE\_YCBCR\_CONVERSION\_X where X is LINEAR\_FILTER,
SEPARATE\_RECONSTRUCTION\_BUTER. SEPARATE\_RECONSTRUCTION\_FILTER,

#### void vkGetPhysicalDeviceFormatProperties2(

VkPhysicalDevice physicalDevice, VkFormat format, P.13 VkFormatProperties2\* pFormatProperties);

typedef struct VkFormatProperties2 { VkStructureType sType; P.15 void\* pNext;

VkFormatProperties formatProperties; } VkFormatProperties2;

#### Additional Image Capabilities [31.4]

VkResult vkGetPhysicalDeviceImageFormatProperties(

VkPhysicalDevice physicalDevice, VkFormat format, P.13 VkImageType type, P.13
VkImageTiling tiling, P.13
VkImageUsageFlags usage, P.13

VkImageCreateFlags flags, P.13
VkImageFormatProperties\* plmageFormatProperties);

# typedef struct VkImageFormatProperties { VkExtent3D maxExtent; P12

uint32\_t maxMipLevels; uint32\_t maxArrayLayers; VkSampleCountFlags sampleCounts; P.15 VkDeviceSize maxResourceSize; } VkImageFormatProperties;

#### VkResult vkGetPhysicalDeviceImageFormatProperties2(

VkPhysicalDevice *physicalDevice*, const VkPhysicalDeviceImageFormatInfo2\* plmageFormatInfo,

typedef struct VkImageFormatProperties2 {

VkStructureType sType; P.15 void\* pNext;

VkImageFormatProperties imageFormatProperties;

} VkImageFormatProperties2;

pNext must be NULL or point to: VkExternalImageFormatProperties P.12

VkSamplerYcbcrConversionImageFormatProperties P.15

#### typedef struct VkPhysicalDeviceImageFormatInfo2 {

VkStructureType sType; P.15 const void\* pNext; VkFormat format, P.13 VkImageType type, 113
VkImageTiling tiling, 123
VkImageUsageFlags usage, 123
VkImageCreateFlags flags, 215
} VkPhysicalDeviceImageFormatInfo2;

pNext must be NULL or point to:

VkPhysicalDeviceExternalImageFormatInfo P.14

VkImageFormatProperties2\* plmageFormatProperties); CHROMA\_RECONSTRUCTION\_EXPLICIT,
CHROMA\_RECONSTRUCTION\_EXPLICIT\_FORCEABLE

**Notes** 

#### **Structures and Enumerations**

This section contains an alphabetic reference to types enums and structs referenced in multiple places on preceding pages.

#### enum VkAccessFlagBits:

VK\_ACCESS\_X\_BIT where X is INDIRECT\_COMMAND\_READ, INDEX\_READ, VERTEX\_ATTRIBUTE\_READ, UNIFORM READ, INPUT ATTACHMENT READ, SHADER [READ, WRITE] COLOR\_ATTACHMENT\_[READ, WRITE],
DEPTH\_STENCIL\_ATTACHMENT\_[READ, WRITE], TRANSFER\_[READ, WRITE], HOST\_[READ, WRITE], MEMORY\_[READ, WRITE]

#### typedef struct VkAllocationCallbacks {

void\* *pUserData*; PFN\_vkAllocationFunction pfnAllocation; PFN\_vkReallocationFunction pfnReallocation; PFN\_vkFreeFunction pfnFree;
PFN\_vkInternalAllocationNotification
pfnInternalAllocation;
PFN\_vkInternalFreeNotification pfnInternalFree; } VkAllocationCallbacks; typedef void\* (VKAPI\_PTR\* PFN\_vkAllocationFunction)(

## void\* pUserData,

size\_t size, size t alignment,

VkSystemAllocationScope allocationScope);

## typedef void\* (VKAPI\_PTR\* PFN\_vkReallocationFunction)(

void\* pUserData, void\* pOriginal, size\_t size, size t alignment, VkSystemAllocationScope allocationScope);

typedef void (VKAPI\_PTR\* PFN\_vkFreeFunction)(

#### void\* pUserData, void\* pMemory);

typedef void (

VKAPI PTR\* PFN vkInternalAllocationNotification)( void\* pUserData, size t*size,* 

VkInternalAllocationType allocationType, VkSystemAllocationScope allocationScope);

#### typedef void (

VKAPI\_PTR\* **PFN\_vkInternalFreeNotification**)( void\* *pUserData*,

size t size, VkInternalAllocationType allocationType, VkSystemAllocationScope allocationScope);

VK\_INTERNAL\_ALLOCATION\_TYPE\_EXECUTABLE

 ${\it allocationScope:} \ {\it VK\_SYSTEM\_ALLOCATION\_SCOPE\_X} \ {\it where} \\ {\it X is COMMAND, OBJECT, CACHE, DEVICE, INSTANCE}$ 

# typedef struct VkBindBufferMemoryDeviceGroupInfo {

VkStructureType sType; PIS const void\* pNext; uint32\_t deviceIndexCount; const uint32\_t\* pDeviceIndices; } VkBindBufferMemoryDeviceGroupInfo;

#### typedef struct VkBindImageMemoryDeviceGroupInfo {

VkStructureType sType; P.15 const void\* pNext; uint32\_t deviceIndexCount; const uint32\_t\* pDeviceIndices; uint32\_t splitInstanceBindRegionCount; const VkRect2D\* pSplitInstanceBindRegions; P.15 } VkBindImageMemoryDeviceGroupInfo;

#### typedef struct VkBindImagePlaneMemoryInfo {

VkStructureType sType; P.15 const void\* pNext; VkImageAspectFlagBits planeAspect; [2.13]
} VkBindImagePlaneMemoryInfo;

#### enum VkBlendOp:

VK\_BLEND\_OP\_ADD,
VK\_BLEND\_OP\_SUBTRACT, VK\_BLEND\_OP\_REVERSE\_SUBTRACT, VK\_BLEND\_OP\_MIN, VK\_BLEND\_OP\_MAX

#### enum VkBufferCreateFlagBits:

VK\_BUFFER\_CREATE\_SPARSE\_BINDING\_BIT, VK\_BUFFER\_CREATE\_SPARSE\_RESIDENCY\_BIT, VK\_BUFFER\_CREATE\_SPARSE\_ALIASED\_BIT, VK\_BUFFER\_CREATE\_PROTECTED\_BIT

## typedef struct VkBufferMemoryBarrier {

VkStructureType sType; P.15 const void\* pNext; VkAccessFlags srcAccessMask; P.12 VkAccessFlags dstAccessMask; P.12 uint32\_t srcQueueFamilyIndex; uint32 t dstQueueFamilyIndex;

VkBuffer buffer; VkDeviceSize offset; VkDeviceSize size; } VkBufferMemoryBarrier;

#### enum VkBufferUsageFlagBits:

VK BUFFER USAGE X BIT where X is TRANSFER\_SRC, TRANSFER\_DST, UNIFORM TEXEL BUFFER, STORAGE TEXEL BUFFER, UNIFORM BUFFER, STORAGE BUFFER, INDEX BUFFER, VERTEX\_BUFFER, INDIRECT\_BUFFER

#### typedef union VkClearColorValue {

float float32[4] int32\_t int32[4]; uint32\_t uint32[4]; } VkClearColorValue;

#### typedef struct VkClearDepthStencilValue {

float depth; uint32 t stencil; } VkClearDepthStencilValue;

# typedef union VkClearValue { VkClearColorValue color; P.12

VkClearDepthStencilValue depthStencil; P.12 } VkClearValue;

#### enum VkCompareOp:

VK\_COMPARE\_OP\_X where X is NEVER, LESS, EQUAL, LESS\_OR\_EQUAL, GREATER, NOT EQUAL GREATER\_OR\_EQUAL, **ALWAYS** 

#### enum VkCompositeAlphaFlagBitsKHR:

VK\_COMPOSITE\_ALPHA\_X\_BIT\_KHR where X is OPAQUE, PRE\_MULTIPLIED, POST\_MULTIPLIED, INHERIT

#### enum VkDependencyFlagBits:

VK\_DEPENDENCY\_BY\_REGION\_BIT, VK\_DEPENDENCY\_DEVICE\_GROUP\_BIT, VK\_DEPENDENCY\_VIEW\_LOCAL\_BIT

#### enum VkDescriptorType:

VK DESCRIPTOR TYPE X where X is SAMPLER, COMBINED\_IMAGE\_SAMPLER, SAMPLED\_IMAGE, STORAGE\_IMAGE, UNIFORM TEXEL BUFFER, STORAGE\_TEXEL\_BUFFER, UNIFORM\_BUFFER[\_DYNAMIC], STORAGE BUFFER[ DYNAMIC], INPUT\_ATTACHMENT

#### typedef struct VkDescriptorSetLayoutBinding {

uint32\_t binding; VkDescriptorType descriptorType; P.12 uint32\_t descriptorCount; VkShaderStageFlags stageFlags; P.15 const VkSampler\* plmmutableSamplers; } VkDescriptorSetLayoutBinding;

## typedef struct VkDescriptorSetLayoutCreateInfo {

VkStructureType sType; P.15 const void\* pNext; VkDescriptorSetLayoutCreateFlags flags; uint32\_t bindingCount; const VkDescriptorSetLayoutBinding\* pBindings; } VkDescriptorSetLayoutCreateInfo;

#### typedef struct VkDeviceGroupBindSparseInfo {

VkStructureType sType; P.15 const void\* pNext; uint32\_t resourceDeviceIndex; uint32\_t memoryDeviceIndex; } VkDeviceGroupBindSparseInfo;

typedef struct VkDeviceGroupCommandBufferBeginInfo {
 VkStructureType sType; P.15
 const void\* pNext; uint32 t deviceMask; } VkDeviceGroupCommandBufferBeginInfo;

# typedef struct VkDeviceGroupDeviceCreateInfo {

VkStructureType sType; P.15 const void\* pNext; uint32\_t physicalDeviceCount; const VkPhysicalDevice\* pPhysicalDevices;

} VkDeviceGroupDeviceCreateInfo;

#### enum VkDeviceGroupPresentModeFlagBitsKHR:

VK\_DEVICE\_GROUP\_PRESENT\_MODE\_X\_BIT\_KHR where X is LOCAL, REMOTE, SUM, LOCAL\_MULTI\_DEVICE

const VkRect2D\* pDeviceRenderAreas; P.15 } VkDeviceGroupRenderPassBeginInfo;

#### typedef struct VkDeviceGroupSubmitInfo {

VkStructureType sType; P.15 const void\* pNext; uint32\_t waitSemaphoreCount; const uint32\_t\* pWaitSemaphoreDeviceIndices; uint32\_t commandBufferCount; const uint32\_t\* pCommandBufferDeviceMasks; uint32\_t signalSemaphoreCount; const uint32\_t\* pSignalSemaphoreDeviceIndices; } VkDeviceGroupSubmitInfo;

#### typedef struct VkExportFenceCreateInfo {

VkStructureType sType; P.15 const void\* pNext; VkExternalFenceHandleTypeFlags handleTypes; P.12 } VkExportFenceCreateInfo;

# typedef struct **VkExportMemoryAllocateInfo** { VkStructureType *sType*; P.15

const void\* pNext; VkExternalMemoryHandleTypeFlags handleTypes; P.12 } VkExportMemoryAllocateInfo;

#### typedef struct VkExportSemaphoreCreateInfo {

VkStructureType *sType*; P.15 const void\* pNext;

VkExternalSemaphoreHandleTypeFlags handleTypes; P.13 } VkExportSemaphoreCreateInfo;

# typedef struct VkExtent2D { uint32\_t width; uint32\_t height;

} VkExtent2D:

#### typedef struct VkExtent3D {

uint32\_t width; uint32\_t height; uint32\_t depth; } VkExtent3D;

## enum VkExternalFenceHandleTypeFlagBits:

VK EXTERNAL FENCE HANDLE TYPE X BIT where X is OPAQUE FD, OPAQUE\_WIN32, OPAQUE\_WIN32\_KMT, SYNC\_FD

typedef struct **VkExternalImageFormatProperties** { VkStructureType *sType*; P.15 void\* pNext; **VkExternalMemoryProperties** externalMemoryProperties; P13 } VkExternalImageFormatProperties;

#### typedef struct VkExternalMemoryBufferCreateInfo {

VkStructureType sType; P.15 const void\* pNext; VkExternalMemoryHandleTypeFlags handleTypes; P.12 } VkExternalMemoryBufferCreateInfo;

## enum VkExternalMemoryFeatureFlagBits:

VK\_EXTERNAL\_MEMORY\_FEATURE\_X\_BIT where X is DEDICATED ONLY, **EXPORTABLE IMPORTABLE** 

#### enum VkExternalMemoryHandleTypeFlagBits:

VK\_EXTERNAL\_MEMORY\_HANDLE\_TYPE\_X\_BIT where X is OPAQUE\_FD, OPAQUE\_WIN32, OPAQUE\_WIN32\_KMT, D3D11\_TEXTURE D3D11\_TEXTURE\_KMT, D3D12 HEAP, D3D12\_RESOURCE

## Structures and Enumerations (continued)

typedef struct VkExternalMemoryImageCreateInfo { VkStructureType sType; P.15 const void\* pNext;

VkExternalMemoryHandleTypeFlags handleTypes; P.12

VkExternalMemoryImageCreateInfo;

typedef struct VkExternalMemoryProperties { VkExternalMemoryFeatureFlags

externalMemoryFeatures; P.12 VkExternalMemoryHandleTypeFlags exportFromImportedHandleTypes; P12 VkExternalMemoryHandleTypeFlags compatibleHandleTypes; P12

} VkExternalMemoryProperties;

#### enum VkExternalSemaphoreHandleTypeFlagBits:

VK\_EXTERNAL\_SEMAPHORE\_HANDLE\_TYPE\_X\_BIT where X is OPAQUE\_FD, OPAQUE\_WIN32, OPAQUE\_WIN32\_KMT, D3D12\_FENCE, SYNC\_FD

#### enum VkFormat:

VK FORMAT\_X where X is UNDEFINED, R4G4\_UNORM\_PACK8, R4G4B4A4\_UNORM\_PACK16, B4G4R4A4\_UNORM\_PACK16, R5G6B5\_UNORM\_PACK16, B5G6R5\_UNORM\_PACK16 R5G5B5A1\_UNORM\_PACK16 B5G5R5A1\_UNORM\_PACK16, A1R5G5B5\_UNORM\_PACK16, ATRSGSBS\_UNDRM\_PACKT6,
R8\_[UNORM, SNORM, USCALED],
R8\_[SSCALED, UINT, SINT, SRGB],
R8G8\_[UNORM, SNORM, USCALED],
R8G8BS\_[SSCALED, UINT, SINT, SRGB],
R8G8BS\_[UNORM, SNORM, USCALED],
R8G8BS\_[SSCALED, UINT, SINT, SRGB],
B8G8R8\_[UNORM, SNORM, USCALED], B8G8R8 [SSCALED, UINT, SINT, SRGB] R8G8B8A8\_[UNORM, SNORM, USCALED], R8G8B8A8\_[SSCALED, UINT, SINT, SRGB], B8G8R8A8\_[UNORM, SNORM, USCALED], B8G8R8A8\_[SSCALED, UINT, SINT, SRGB] A8B8G8R8\_[UNORM, SNORM, USCALED]\_PACK32, A8B8G8R8\_[SSCALED, UINT, SINT, SRGB]\_PACK32, A2R10G10B10 [UNORM, SNORM, USCALED] PACK32, A2R10G10B10 [SSCALED, UINT, SINT] PACK32, A2B10G10R10 [UNORM, SNORM, USCALED] PACK32, AZBIJGIJORIO\_[UNORM, SNORM, USCALED] P AZBIJGIJORIO\_[SSCALED, UINT, SINT]\_PACK32, R16\_[UNORM, SNORM, USCALED], R16\_[SSCALED, UINT, SINT, SFLOAT], R16616\_[UNORM, SNORM, USCALED], R16616\_[SSCALED, UINT, SINT, SFLOAT], R16616B16\_[UNORM, SNORM, USCALED], R16616B16\_[SSCALED, UINT, SINT, SFLOAT], R16616B16A16 [UNORM, SNORM, USCALED], R16G16B16A16 [SSCALED, UINT, SINT, SFLOAT], R32 [UINT, SINT, SFLOAT], R32G32\_[UINT, SINT, SFLOAT] R32G32B32\_[UINT, SINT, SFLOAT] R32G32B32A32\_[UINT, SINT, SFLOAT], R64\_[UINT, SINT, SFLOAT], R64\_[UIN1, SIN1, SFLOAT], R64G64\_[UINT, SINT, SFLOAT], R64G64B64\_[UINT, SINT, SFLOAT], R64G64B64A64\_[UINT, SINT, SFLOAT], B10G11R11\_UFLOAT\_PACK32, E5B9G9R9\_UFLOAT\_PACK32, X8\_D24\_UNORM\_PACK32, D32\_SFLOAT[\_S8\_UINT], S8 UINT, D[16, 24] UNORM S8 UINT, BC1\_[RGB, RGBA]\_UNORM\_BLOCK, BC1\_[RGB, RGBA]\_SRGB\_BLOCK, BC2\_[UNORM, SRGB]\_BLOCK, BC3\_[UNORM, SRGB]\_BLOCK BC4\_[UNORM, SRGB]\_BLOCK, BC5\_[UNORM, SRGB]\_BLOCK, BC5\_[UNORM, SRGB]\_BLOCK,
BC6H\_[UFLOAT, SFLOAT]\_BLOCK,
BC7\_[UNORM, SRGB]\_BLOCK,
ETC2\_R8G8B8\_[UNORM, SRGB]\_BLOCK,
ETC2\_R8G8B8A1\_[UNORM, SRGB]\_BLOCK,
ETC2\_R8G8B8A8\_[UNORM, SRGB]\_BLOCK,
EAC\_R11\_[UNORM, SRGB]\_BLOCK, EAC\_R11\_[UNORM, SRGB]\_BLOCK, EAC\_R11G11\_[UNORM, SRGB]\_BLOCK, ASTC\_4x4\_[UNORM, SRGB]\_BLOCK, ASTC\_5x4\_[UNORM, SRGB]\_BLOCK, ASTC\_5x5\_[UNORM, SRGB]\_BLOCK, ASTC\_6x5\_[UNORM, SRGB]\_BLOCK, ASTC\_6x6\_[UNORM, SRGB]\_BLOCK,

ASTC\_10x5\_[UNORM, SRGB]\_BLOCK,
ASTC\_10x6\_[UNORM, SRGB]\_BLOCK,
ASTC\_10x8\_[UNORM, SRGB]\_BLOCK,
ASTC\_10x10\_[UNORM, SRGB]\_BLOCK,
ASTC\_12x10\_[UNORM, SRGB]\_BLOCK,
ASTC\_12x12\_[UNORM, SRGB]\_BLOCK,
ASTC\_12x12\_[UNORM, SRGB]\_BLOCK, G8B8G8R8\_422\_UNORM, B8G8R8G8\_422\_UNORM, G8\_B8\_R8\_3PLANE\_420\_UNORM, G8\_B8R8\_2PLANE\_{420, 422}\_UNORM G8\_B8\_R8\_3PLANE\_{422, 444}\_UNORM, R10X6\_UNORM\_PACK16, R10X6\_UNORM\_PACK16,
R10X6G10X6\_UNORM\_2PACK16,
R10X6G10X6B10X6A10X6\_UNORM\_4PACK16,
G10X6B10X6G10X6R10X6\_422\_UNORM\_4PACK16,
B10X6G10X6R10X6G10X6\_422\_UNORM\_4PACK16,
G10X6\_B10X6\_R10X6\_3PLANE\_420\_UNORM\_3PACK16,
G10X6\_B10X6\_R10X6\_3PLANE\_420\_UNORM\_3PACK16,
G10X6\_B10X6\_R10X6\_3PLANE\_422\_UNORM\_3PACK16,
G10X6\_B10X6R10X6\_2PLANE\_422\_UNORM\_3PACK16,
G10X6\_B10X6\_R10X6\_3PLANE\_444\_UNORM\_3PACK16, R12X4 UNORM PACK16, R12X4G12X4\_UNORM\_2PACK16, R12X4G12X4B12X4A12X4\_UNORM\_4PACK16, 612X4B12X4G12X4R12X4 422 UNORM 4PACK16, B12X4G12X4R12X4G12X4 422 UNORM 4PACK16, G12X4 B12X4 R12X4 3PLANE 420 UNORM 3PACK16, G12X4 B12X4 R12X4 3PLANE 420 UNORM 3PACK16, G12X4 B12X4R12X4 2PLANE 420, 422 UNORM 3PACK16, G12X4\_B12X4R12X4\_ZPLANE\_{420, 422}\_UNORM\_3PACK16, G12X4\_B12X4\_R12X4\_3PLANE\_422\_UNORM\_3PACK16, G12X4\_B12X4\_R12X4\_3PLANE\_444\_UNORM\_3PACK16, G16B16G16R16\_422\_UNORM, B16G16R16G16\_422\_UNORM, G16\_B16\_R16\_3PLANE\_{420, 422, 444}\_UNORM, G16\_B16R16\_ZPLANE\_{420, 422}\_UNORM

#### enum VkImageAspectFlagBits:

VK\_IMAGE\_ASPECT\_X\_BIT where X is COLOR, DEPTH, STENCIL, METADATA, PLANE\_[0,1,2]

#### enum VkImageCreateFlagBits:

VK\_IMAGE\_CREATE\_X\_BIT where X is SPARSE {BINDING, RESIDENCY, ALIASED}, MUTABLE\_FORMAT, {CUBE, 2D\_ARRAY, TEXEL\_VIEW}\_COMPATIBLE, ALIAS, BIND SFR, EXTENDED\_USAGE, PROTECTED, DISJOINT

#### enum VklmageLayout:

VK\_IMAGE\_LAYOUT\_X where X is UNDEFINED, GENERAL, PREINITIALIZED, COLOR\_ATTACHMENT\_OPTIMAL, COLOR ATTACHMENT\_OPTIMAL,
DEPTH\_ATTACHMENT\_STENCIL\_READ\_ONLY\_OPTIMAL,
DEPTH\_READ\_ONLY\_STENCIL\_ATTACHMENT\_OPTIMAL,
DEPTH\_STENCIL\_ATTACHMENT\_OPTIMAL,
DEPTH\_STENCIL\_READ\_ONLY\_OPTIMAL,
SHADER\_READ\_ONLY\_OPTIMAL,
SHADER\_READ\_ONLY\_OPTIMAL,
DEPTH\_READ\_ONLY\_STENCIL\_ATTACHMENT\_OPTIMAL,
DEPTH\_ATTACHMENT\_STENCIL\_READ\_ONLY\_OPTIMAL,
VK\_IMAGE\_LAYOUT\_PRESENT\_SRC\_KHR,
VK\_IMAGE\_LAYOUT\_SHARED\_PRESENT\_KHR VK\_IMAGE\_LAYOUT\_SHARED\_PRESENT\_KHR

NOTE: For the functions vkCmdCopyImage vk Cmd Copy Buffer ToI mage, vk Cmd Copy Image To Buffer,vkCmdBlitImage, and vkCmdResolveImage, the enum VkImageLayout for the following parameters may be:

srcImageLayout: VK\_IMAGE\_LAYOUT\_GENERAL, VK\_IMAGE\_LAYOUT\_TRANSFER\_SRC\_OPTIMAL,
VK\_IMAGE\_LAYOUT\_SHARED\_PRESENT\_KHR

dstimageLayout: VK\_IMAGE\_LAYOUT\_GENERAL,
VK\_IMAGE\_LAYOUT\_TRANSFER\_DST\_OPTIMAL,
VK\_IMAGE\_LAYOUT\_SHARED\_PRESENT\_KHR

typedef struct VkImageMemoryBarrier {
 VkStructureType sType; P.15
 const void\* pNext;
 VkAccessFlags srcAccessMask; P.12
 VkAccessFlags dstAccessMask; P.12
 VkImageLayout oldLayout; P.13 VklmageLayout newLayout; P.13 uint32 t srcQueueFamilyIndex; uint32\_t dstQueueFamilyIndex;

VkImage image; VkImageSubresourceRange subresourceRange; P3313 } VkImageMemoryBarrier;

# typedef struct VkImagePlaneMemoryRequirementsInfo { VkStructureType sType; P.15

const void\* pNext; VkImageAspectFlagBits planeAspect; P333
} VkImagePlaneMemoryRequirementsInfo; typedef struct VkImageSubresourceLayers { VkImageAspectFlags aspectMask; P.13
uint32 t mipLevel;
uint32\_t baseArrayLayer;
uint32\_t layerCount;
} VkImageSubresourceLayers;

typedef struct VkImageSubresourceRange { VkImageAspectFlags aspectMask; P.13 uint32\_t baseMipLevel;

uint32\_t levelCount; uint32\_t baseArrayLayer; uint32\_t layerCount; } VkImageSubresourceRange;

#### enum VkImageTiling:

VK\_IMAGE\_TILING\_{OPTIMAL, LINEAR}

#### enum VklmageType:

VK\_IMAGE\_TYPE\_{1D, 2D, 3D}

#### enum VkImageUsageFlagBits:

VK\_IMAGE\_USAGE\_X\_BIT where X is TRANSFER\_SRC, TRANSFER\_DST, SAMPLED, STORAGE COLOR\_ATTACHMENT,
DEPTH\_STENCIL\_ATTACHMENT,
TRANSIENT\_ATTACHMENT, INPUT\_ATTACHMENT

# typedef struct VkImageViewUsageCreateInfo { VkStructureType sType; P.15

const void\* pNext; VkImageUsageFlags usage; P.13 } VkImageViewUsageCreateInfo;

## typedef struct VkInputAttachmentAspectReference {

uint32\_t subpass uint32\_t inputAttachmentIndex; VkImageAspectFlags aspectMask; P.13 } VkInputAttachmentAspectReference;

#### typedef struct VkMemoryAllocateFlagsInfo {

VkStructureType sType; 215
const void\* pNext;
VkMemoryAllocateFlags flags;
uint32\_t deviceMask;
VkMemoryAllocateFlagsInfo;

flags: VK\_MEMORY\_ALLOCATE\_DEVICE\_MASK\_BIT

#### typedef struct VkMemoryBarrier { VkStructureType sType; P.15 const void\* pNext;

VkAccessFlags srcAccessMask; P.11 VkAccessFlags dstAccessMask; P.12 } VkMemoryBarrier;

typedef struct VkMemoryDedicatedAllocateInfo {
 VkStructureType sType; P.15
 const void\* pNext;
 VkImage image; VkBuffer buffer;
} VkMemoryDedicatedAllocateInfo;

#### typedef struct VkMemoryDedicatedRequirements {

VkStructureType sType; P.15 void\* pNext; VkBool32 prefersDedicatedAllocation; VkBool32 requiresDedicatedAllocation; } VkMemoryDedicatedRequirements;

#### typedef struct VkMemoryHeap {

VkDeviceSize size; VkMemoryHeapFlags flags; } VkMemoryHeap;

flags: VK\_MEMORY\_HEAP\_X\_BIT where X is DEVICE\_LOCAL, MULTI\_INSTANCE

#### typedef struct VkMemoryRequirements {

VkDeviceSize size; VkDeviceSize alignment; uint32\_t memoryTypeBits; } VkMemoryRequirements;

# typedef struct **VkMemoryRequirements2** { VkStructureType *sType*; P.15

void\* pNext; VkMemoryRequirements memoryRequirements; P.13 } VkMemoryRequirements2;

pNext must be NULL or point to: VkMemoryDedicatedRequirements P13

Continued on next page >

ASTC\_8x5\_[UNORM, SRGB]\_BLOCK,

ASTC\_8x6\_[UNORM, SRGB]\_BLOCK ASTC\_8x8\_[UNORM, SRGB]\_BLOCK,

Structures and Enumerations (continued)	VkStructureType sType; P.15 void* pNext;	VkDeviceSize minStorageBufferOffsetAlignment; int32
typedef struct VkMemoryType {	VkPhysicalDeviceFeatures features; P.14	int32_t minTexelGatherOffset;
VkMemoryPropertyFlags propertyFlags;	} VkPhysicalDeviceFeatures2;	uint32_t maxTexelGatherOffset;
uint32_t heapIndex;	pNext must be NULL or point to one of: VkPhysicalDevice16BitStorageFeatures P.14	float minInterpolationOffset; float maxInterpolationOffset;
} VkMemoryType;	VkPhysicalDeviceMultiviewFeatures P.14	uint32_t subPixelInterpolationOffsetBits;
<pre>propertyFlags: VK_MEMORY_PROPERTY_X_BIT where X is     DEVICE LOCAL, HOST VISIBLE, HOST COHERENT,</pre>	VkPhysicalDeviceProtectedMemoryFeatures P.15	uint32_t maxFramebufferWidth;
HOST_CACHED, LAZILY_ALLOCATED, PROTECTED	VkPhysicalDeviceSamplerYcbcrConversionFeatures P.15 VkPhysicalDeviceShaderDrawParameterFeatures P.15	uint32_t maxFramebufferHeight; uint32_t maxFramebufferLayers;
typedef struct VkOffset2D {	VkPhysicalDeviceVariablePointerFeatures P.15	VkSampleCountFlags framebufferColorSampleCounts;
int32_t <i>x</i> ;	•	VkSampleCountFlags framebufferDepthSampleCounts; P.1
int32_t y;	typedef struct VkPhysicalDeviceIDProperties {	VkSampleCountFlags framebufferStencilSampleCounts; <a href="Mills state=" list="" list<="" state="List state=" td=""></a>
} VkOffset2D;	VkStructureType sType; P.15 void* pNext;	framebufferNoAttachmentsSampleCounts; P.15
typedef struct VkOffset3D {	uint8_t deviceUUID[VK_UUID_SIZE];	uint32_t maxColorAttachments;
int32_t x;	uint8_t driverUUID[VK_UUID_SIZE];	VkSampleCountFlags sampledImageColorSampleCounts; P.15
int32_t y; int32_t z;	uint8_t deviceLUID[VK_LUID_SIZE]; uint32_t deviceNodeMask;	VkSampleCountFlags
} VkOffset3D;	VkBool32 deviceLUIDValid;	sampledImageIntegerSampleCounts; P.15
typedef struct VkPhysicalDevice16BitStorageFeatures {	} VkPhysicalDeviceIDProperties;	VkSampleCountFlags sampledImageDepthSampleCounts; P.15
VkStructureType sType; P.15	typedef struct VkPhysicalDeviceLimits {	VkSampleCountFlags
void* pNext;	uint32_t maxImageDimension1D;	sampledImageStencilSampleCounts; P.15
VkBool32 storageBuffer16BitAccess; VkBool32 uniformAndStorageBuffer16BitAccess;	uint32_t maxImageDimension2D; uint32_t maxImageDimension3D;	VkSampleCountFlags storageImageSampleCounts; P.15 uint32 t maxSampleMaskWords;
VkBool32 storagePushConstant16;	uint32_t maxImageDimensionCube;	VkBool32 timestampComputeAndGraphics;
VkBool32 storageInputOutput16;	uint32_t maxImageArrayLayers;	float timestampPeriod;
} VkPhysicalDevice16BitStorageFeatures;	uint32 <sup>-</sup> t <i>maxTexelBufferElements</i> ; uint32 <sup>-</sup> t <i>maxUniformBufferRange</i> ;	uint32_t maxClipDistances; uint32_t maxCullDistances;
typedef struct VkPhysicalDeviceExternalImageFormatInfo {	uint32_t maxStorageBufferRange;	uint32_t maxCombinedClipAndCullDistances;
VkStructureType sType; P.15	uint32_t maxPushConstantsSize;	uint32_t discreteQueuePriorities;
const void* pNext;	uint32_t maxMemoryAllocationCount;	float pointSizeRange[2]; float lineWidthRange[2];
VkExternalMemoryHandleTypeFlagBits handleType; P. VkPhysicalDeviceExternalImageFormatInfo;	uint32_t maxSamplerAllocationCount; VkDeviceSize bufferImageGranularity;	float pointSizeGranularity; float lineWidthGranularity;
j vit nysicalbeviceExternalinagerormatimo,	VkDeviceSize sparseAddressSpaceSize;	VkBool32 strictLines;
typedef struct VkPhysicalDeviceFeatures {	uint32_t maxBoundDescriptorSets;	VkBool32 standardSampleLocations;
VkBool32 robustBufferAccess; VkBool32 fullDrawIndexUint32;	uint32_t maxPerStageDescriptorSamplers; uint32_t maxPerStageDescriptorUniformBuffers;	VkDeviceSize optimalBufferCopyOffsetAlignment; VkDeviceSize optimalBufferCopyRowPitchAlignment;
VkBool32 imageCubeArray;	uint32_t maxPerStageDescriptorStorageBuffers;	VkDeviceSize nonCoherentAtomSize;
VkBool32 independentBlend;	uint32_t maxPerStageDescriptorSampledImages;	} VkPhysicalDeviceLimits;
VkBool32 geometryShader; VkBool32 tessellationShader;	uint32_t maxPerStageDescriptorStorageImages; uint32_t maxPerStageDescriptorInputAttachments;	typedef struct VkPhysicalDeviceMaintenance3Properties
VkBool32 sampleRateShading;	uint32_t maxPerStageResources;	VkStructureType sType; P.15
VkBool32 dualSrcBlend;	uint32_t maxDescriptorSetSamplers;	<pre>void* pNext; uint32_t maxPerSetDescriptors;</pre>
VkBool32 <i>logicOp</i> ; VkBool32 <i>multiDrawIndirect</i> ;	uint32_t maxDescriptorSetUniformBuffers; uint32_t maxDescriptorSetUniformBuffersDynamic;	VkDeviceSize maxMemoryAllocationSize;
VkBool32 <i>trialitio awindirect</i> , VkBool32 <i>drawIndirectFirstInstance</i> ;	uint32_t maxDescriptorSetStorageBuffers;	} VkPhysicalDeviceMaintenance3Properties;
VkBool32 depthClamp;	uint32_t maxDescriptorSetStorageBuffersDynamic;	typedef struct VkPhysicalDeviceMemoryProperties {
VkBool32 depthBiasClamp;	uint32_t maxDescriptorSetSampledImages; uint32_t maxDescriptorSetStorageImages;	uint32_t memoryTypeCount; VkMemoryType
VkBool32 fillModeNonSolid; VkBool32 depthBounds;	uint32_t maxDescriptorSetInputAttachments;	memoryTypes[VK_MAX_MEMORY_TYPES]; P.14
VkBool32 wideLines;	uint32_t maxVertexInputAttributes;	uint32_t memoryHeapCount; VkMemoryHeap memoryHeaps[VK_MAX_MEMORY_HEAPS]; P.13
VkBool32 largePoints;	uint32_t maxVertexInputBindings; uint32_t maxVertexInputAttributeOffset;	} VkPhysicalDeviceMemoryProperties;
VkBool32 alphaToOne; VkBool32 multiViewport;	uint32_t maxVertexInputBindingStride;	typedef struct VkPhysicalDeviceMultiviewFeatures {
VkBool32 samplerAnisotropy:	uint32_t maxVertexOutputComponents;	VkStructureType sType; P.15
VkBool32 textureCompressionETC2;	uint32_t maxTessellationGenerationLevel; uint32_t maxTessellationPatchSize;	void* pNext;
VkBool32 textureCompressionASTC_LDR; VkBool32 textureCompressionBC;	uint32_t maxressenationFatch5ize, uint32_t	VkBool32 multiview; VkBool32 multiviewGeometryShader;
VkBool32 occlusionQueryPrecise;	maxTessellationControlPerVertexInputComponents;	VkBool32 multiviewTessellationShader;
VkBool32 pipelineStatisticsQuery;	uint32_t maxTessellationControlPerVertexOutputComponents;	} VkPhysicalDeviceMultiviewFeatures;
VkBool32 vertexPipelineStoresAndAtomics; VkBool32 fragmentStoresAndAtomics;	uint32 t	typedef struct VkPhysicalDeviceMultiviewProperties {
VkBool32 shaderTessellationAndGeometryPointSize;	maxTessellationControlPerPatchOutputComponents;	VkStructureType sType; P.15
VkBool32 shaderImageGatherExtended;	uint32_t maxTessellationControlTotalOutputComponents;	<pre>void* pNext; uint32 t maxMultiviewViewCount;</pre>
VkBool32 shaderStorageImageExtendedFormats; VkBool32 shaderStorageImageMultisample;	uint32_t maxTessellationEvaluationInputComponents; uint32_t maxTessellationEvaluationOutputComponents;	uint32_t maxMultiviewInstanceIndex;
VkBool32 shaderStorageImageReadWithoutFormat;	uint32_t maxGeometryShaderInvocations;	} VkPhysicalDeviceMultiviewProperties;
VkBool32 shaderStorageImageWriteWithoutFormat;	uint32_t maxGeometryInputComponents; uint32_t maxGeometryOutputComponents;	typedef struct VkPhysicalDevicePointClippingProperties {
VkBool32 shaderUniformBufferArrayDynamicIndexing; VkBool32 shaderSampledImaqeArrayDynamicIndexing;	uint32_t maxGeometryOutputVertices;	VkStructureType sType; P.15
VkBool32 shaderStorageBufferArrayDynamicIndexing;	uint32_t maxGeometryTotalOutputComponents;	void* pNext;
VkBool32 shaderStorageImageArrayDynamicIndexing;	uint32_t maxFragmentInputComponents;	VkPointClippingBehavior pointClippingBehavior; } VkPhysicalDevicePointClippingProperties;
VkBool32 shaderClipDistance; VkBool32 shaderCullDistance;	uint32_t maxFragmentOutputAttachments; uint32_t maxFragmentDualSrcAttachments;	pointClippingBehavior:
VkBool32 shaderFloat64;	uint32_t maxFragmentCombinedOutputResources;	VK_POINT_CLIPPING_BEHAVIOR_X where X is
VkBool32 shaderInt64;	uint32_t maxComputeSharedMemorySize;	ALL_CLIP_PLANES, USER_CLIP_PLANES_ONLY
VkBool32 shaderInt16; VkBool32 shaderResourceResidency;	uint32_t maxComputeWorkGroupCount[3]; uint32_t maxComputeWorkGroupInvocations;	typedef struct VkPhysicalDeviceProperties {
VkBool32 shaderResourceMinLod;	uint32_t maxComputeWorkGroupSize[3];	uint32_t apiVersion; uint32_t driverVersion;
VkBool32 sparseBinding;	uint32_t subPixelPrecisionBits;	uint32_t vendorID; uint32_t deviceID;
VkBool32 sparseResidencyBuffer;	uint32_t subTexelPrecisionBits; uint32_t mipmapPrecisionBits;	VkPhysicalDeviceType deviceType; char deviceName[
VkBool32 sparseResidencyImage2D; VkBool32 sparseResidencyImage3D;	uint32_t maxDrawIndexedIndexValue;	VK_MAX_PHYSICAL_DEVICE_NAME_SIZE];
VkBool32 sparseResidency2Samples;	uint32_t maxDrawIndirectCount;	uint8_t pipelineCacheUUID[VK_UUID_SIZE];
VkBool32 sparseResidency4Samples;	float maxSamplerLodBias; float maxSamplerAnisotropy;	VkPhysicalDeviceLimits limits; P.14 VkPhysicalDeviceSparseProperties sparseProperties; P.15
VkBool32 sparseResidency8Samples; VkBool32 sparseResidency16Samples;	uint32_t maxViewports;	VKPhysicalDeviceSparseProperties; VkPhysicalDeviceProperties;
VkBool32 sparseResidencyAliased;	uint32_t maxViewportDimensions[2];	deviceType:
VkBool32 variableMultisampleRate;	float viewportBoundsRange[2]; uint32 t viewportSubPixelBits;	VK_PHYSICAL_DEVICE_TYPE_X where X is
VkBool32 inheritedQueries; } VkPhysicalDeviceFeatures;	size_t minMemoryMapAlignment;	OTHER, INTEGRATED_GPU, DISCRETE_GPU, VIRTUAL_GPU, CPU
	VkDeviceSize minTexelBufferOffsetAlignment;	VIII. OAL_OI O, OF O
typedef struct VkPhysicalDeviceFeatures2 {	VkDeviceSize minUniformBufferOffsetAlignment;	Continued on next page

## Structures and Enumerations (continued) enum VkPipelineStageFlagBits:

typedef struct

VkPhysicalDeviceProtectedMemoryFeatures {

VkStructureType sType; P.15 void\* pNext; VkBool32 protectedMemory; } VkPhysicalDeviceProtectedMemoryFeatures;

typedef struct

VkPhysicalDeviceProtectedMemoryProperties {

VkStructureType sType; P.15 void\* pNext; VkBool32 protectedNoFault; } VkPhysicalDeviceProtectedMemoryProperties;

VkPhysicalDeviceSamplerYcbcrConversionFeatures {

VkStructureType sType; P.15 void\* pNext; VkBool32 samplerYcbcrConversion;

} VkPhysicalDeviceSamplerYcbcrConversionFeatures;

typedef struct

VkPhysicalDeviceShaderDrawParameterFeatures {

VkStructureType sType; P.15 void\* pNext; VkBool32 shaderDrawParameters; } VkPhysicalDeviceShaderDrawParameterFeatures;

typedef struct VkPhysicalDeviceSparseProperties {

VkBool32 residencyStandard2DBlockShape; VkBool32 residencyStandard2DMultisampleBlockShape; VkBool32 residencyStandard3DBlockShape;

VkBool32 residencyAlignedMipSize; VkBool32 residencyNonResidentStrict;

} VkPhysicalDeviceSparseProperties;

typedef struct VkPhysicalDeviceSubgroupProperties {

VkStructureType sType; P.15 void\* pNext; uint32\_t subgroupSize; VkShaderStageFlags supportedStages; P.15
VkSubgroupFeatureFlags supportedOperations; VkBool32 quadOperationsInAllStages;

} VkPhysicalDeviceSubgroupProperties;

supportedOperations:

THE SHOP IN THE SH

typedef struct VkPhysicalDeviceSurfaceInfo2KHR {

VkStructureType sType; P.15 const void\* pNext; VkSurfaceKHR surface; VkPhysicalDeviceSurfaceInfo2KHR;

typedef struct VkPhysicalDeviceVariablePointerFeatures {

VkStructureType sType; P.15 void\* pNext;

VkBool32 variablePointersStorageBuffer; VkBool32 variablePointers;

} VkPhysicalDeviceVariablePointerFeatures;

enum VkPipelineBindPoint:

VK\_PIPELINE\_BIND\_POINT\_COMPUTE, VK PIPELINE BIND POINT GRAPHICS

enum VkPipelineCreateFlagBits:

VK\_PIPELINE\_CREATE\_X where X is DISABLE\_OPTIMIZATION\_BIT, ALLOW\_DERIVATIVES\_BIT,
DERIVATIVE\_BIT, VIEW\_INDEX\_FROM\_DEVICE\_INDEX\_BIT, DISPATCH BASE

typedef struct VkPipelineShaderStageCreateInfo {

VkStructureType sType; P.15 const void\* pNext; VkPipelineShaderStageCreateFlags flags; = 0 VkShaderStageFlagBits stage; P.15 VkShaderModule module;

const VkSpecializationInfo\* pSpecializationInfo; P.15 } VkPipelineShaderStageCreateInfo;

const char\* pName;

typedef struct VkSpecializationInfo {
 uint32\_t mapEntryCount;
 const VkSpecializationMapEntry\* pMapEntries; P.15 size\_t dataSize; const void\* pData; } VkSpecializationInfo;

typedef struct VkSpecializationMapEntry {

uint32\_t constantID; uint32 t offset; size t*size*; } VkSpecializationMapEntry;

VK\_PIPELINE\_STAGE\_X\_BIT where X is TOP\_OF\_PIPE, DRAW\_INDIRECT, VERTEX\_[INPUT, SHADER], TESSELLATION\_[CONTROL, EVALUATION]\_SHADER, [COMPUTE, GEOMETRY, FRAGMENT] SHADER, [EARLY, LATE] FRAGMENT\_TESTS, COLOR\_ATTACHMENT\_OUTPUT, TRANSFER, BOTTOM\_OF\_PIPE, HOST, ALL\_{GRAPHICS, COMMANDS}

typedef struct

VkPipelineTessellationDomainOriginStateCreateInfo{

VkStructureType sType; P.15 const void\* pNext;

VkTessellationDomainOrigin domainOrigin; } VkPipelineTessellationDomainOriginStateCreateInfo;

VK\_TESSELLATION\_DOMAIN\_ORIGIN\_UPPER\_LEFT VK\_TESSELLATION\_DOMAIN\_ORIGIN\_LOWER\_LEFT

typedef struct VkProtectedSubmitInfo {
 VkStructureType sType; P15
 const void\* pNext; VkBool32 protectedSubmit;
} VkProtectedSubmitInfo;

enum VkQueryPipelineStatisticFlagBits:

VK\_QUERY\_PIPELINE\_STATISTIC\_X\_BIT where X is INPUT\_ASSEMBLY\_{VERTICES, PRIMITIVES}, VERTEX\_SHADER\_INVOCATIONS, GEOMETRY\_SHADER\_{INVOCATIONS, PRIMITIVES}, CLIPPING\_{INVOCATIONS, PRIMITIVES},
FRAGMENT\_SHADER\_INVOCATIONS,
TESSELLATION\_CONTROL\_SHADER\_PATCHES,
TESSELLATION\_EVALUATION\_SHADER\_INVOCATIONS, COMPUTE\_SHADER\_INVOCATIONS

typedef struct VkRect2D {

VkOffset2D offset; P14 VkExtent2D extent; P.12 } VkRect2D;

VkRenderPassInputAttachmentAspectCreateInfo {

VkStructureType sType; P.15 const void\* pNext; uint32\_t aspectReferenceCount; const VkInputAttachmentAspectReference\* pAspectReferences; P.13

} VkRenderPassInputAttachmentAspectCreateInfo;

const uint32\_t\* pCorrelationMasks; } VkRenderPassMultiviewCreateInfo;

enum VkSampleCountFlagBits:

VK\_SAMPLE\_COUNT\_X\_BIT where X is 1, 2, 4, 8, 16, 32, 64

typedef struct

VkSamplerYcbcrConversionImageFormatProperties {

VkStructureType sType; P.15 void\* pNext; uint32 t combinedImageSamplerDescriptorCount;

} VkSamplerYcbcrConversionImageFormatProperties;

typedef struct VkSamplerYcbcrConversionInfo {

VkStructureType sType; P.15 const void\* pNext; VkSamplerYcbcrConversion conversion; } VkSamplerYcbcrConversionInfo

enum VkShaderStageFlagBits:

VK\_SHADER\_STAGE\_X where X is {VERTEX, GEOMETRY, FRAGMENT, COMPUTE}\_BIT, TESSELLATION\_CONTROL\_BIT, TESSELLATION\_EVALUATION\_BIT, ALL GRAPHICS, ALL

enum VkSharingMode:

VK SHARING MODE EXCLUSIVE, VK\_SHARING\_MODE\_CONCURRENT

typedef struct VkSparseMemoryBind

VkDeviceSize resourceOffset; VkDeviceSize size; VkDeviceMemory memory; VkDeviceSize memoryOffset; VkSparseMemoryBindFlags flags;

} VkSparseMemoryBind;

flags: VK\_SPARSE\_MEMORY\_BIND\_METADATA\_BIT

VkStructureType

The name of each VkStructureType value is obtained by taking the name of the structure, stripping the leading Vk, prefixing each capital letter with \_, converting the entire resulting string to upper case, and prefixing it with VK\_STRUCTURE\_TYPE\_.

typedef struct VkSurfaceCapabilitiesKHR {

uint32\_t minImageCount; uint32\_t maxImageCount; VkExtent2D currentExtent; P.12 VkExtent2D minImageExtent; P.12 VkExtent2D maxImageExtent; P.12

viint32 t maximageArrayLayers;
VkSurfaceTransformFlagsKHR supportedTransforms;
VkSurfaceTransformFlagBitsKHR currentTransform;
VkCompositeAlphaFlagsKHR

supportedCompositeAlpha; P.12
VkImageUsageFlags supportedUsageFlags; P.13 } VkSurfaceCapabilitiesKHR;

enum VkCompositeAlphaFlagBitsKHR:

VK\_COMPOSITE\_ALPHA\_X\_BIT\_KHR where X is
OPAQUE, PRE\_MULTIPLIED, POST\_MULTIPLIED, INHERIT

typedef struct VkSurfaceFormatKHR {

VkFormat format; P.13
VkColorSpaceKHR colorSpace; } VkSurfaceFormatKHR;

colorSpace: VK\_COLOR\_SPACE\_SRGB\_NONLINEAR\_KHR

enum VkSurfaceTransformFlagBitsKHR {

VK\_SURFACE\_TRANSFORM\_X\_BIT\_KHR where X is IDENTITY ROTATE\_[90, 180, 270], HORIZONTAL\_MIRROR HORIZONTAL\_MIRROR\_ROTATE\_[90, 180, 270], INHERIT

typedef struct VkViewport {

float x; float y; float width; float height; float minDepth; float maxDepth; } VkViewport;

#### Learn more about Vulkan

Vulkan is maintained by the Khronos Group, a worldwide consortium of organizations that work to create and maintain key standards used across many industries. Visit Khronos online for resources to help you use and master Vulkan:

Main Vulkan Resource Page: khronos.org/vulkan/

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