

# Department of Computer Science and Engineering, AUST

## Semester Wise Class Schedule, Spring 2016

	2 <sup>ND</sup> Year 1 <sup>ST</sup> Semester ( Section C )											
	8:00-8:50	8:50-9:40	9:40-10:30	10:30-11:20	11:20-12:10	12:10-1:00	1:00-1:50	1:50-2:40	2:40-3:30	3:30-4:20	4:20-5:10	5:10-6:00
<b>SAT</b>				CSE 2100 (C1/C2)* 7B01 ( SS, RR )				MATH 2101 7C07	HUM 2109 7C07			
<b>SUN</b>				CSE 2104 (C1) 7B05 ( SK, MHA )			MATH 2101 7C06	CSE 2103 7C06 SK	EEE 2141 7C06			
<b>MON</b>					CSE 2105 7C07 NJL	HUM 2109 7C07	CSE 2106 (C2) 7B04 ( SSH, MHA )					
<b>TUE</b>			CSE 2105 7A07 NJL	CSE 2103 7A07 SK			EEE 2142 (C1) 3B07					
							CSE 2104 (C2) 7B05 ( SK, MHA )					
<b>WED</b>				MATH 2101 7C06	CSE 2103 7C06 SK	CSE 2105 7C06 NJL	EEE 2142 (C2) 3B07					
<b>THU</b>	EEE 2141 7C07	EEE 2141 7C07	HUM 2109 7C07	CSE 2106 (C1) 7B04 ( SSH, MAT )								

Course no.	Course Title	Cr. Hr.	Course no.	Course Title	Cr. Hr.
HUM2109	Society, Ethics and Technology	3	CSE2103	Data Structures	3
MATH2101	Mathematics-III	3	CSE2104	Data Structures Lab	1.5
EEE2141	Electronic Devices & Circuits	3	CSE2105	Digital Logic Design	3
EEE2142	Electronic Devices & Circuits Lab	1.5	CSE2106	Digital Logic Design Lab	1.5
CSE2100	Software Development-II	0.75			