

# Department of Computer Science and Engineering, AUST

## Semester Wise Class Schedule, Spring 2017

	<b>3<sup>RD</sup> Year 2<sup>ND</sup> Semester ( Section B )</b>											
	<b>8:00-8:50</b>	<b>8:50-9:40</b>	<b>9:40-10:30</b>	<b>10:30-11:20</b>	<b>11:20-12:10</b>	<b>12:10-1:00</b>	<b>1:00-1:50</b>	<b>1:50-2:40</b>	<b>2:40-3:30</b>	<b>3:30-4:20</b>	<b>4:20-5:10</b>	<b>5:10-6:00</b>
<b>SAT</b>	CSE 3215 7C06 MHA	CSE 3223 7C06 TAB	CSE 3213 7C06 MMH									
<b>SUN</b>	CSE 3223 7C07 TAB	HUM 3207 7C07	CSE 3211 7C07 Dr. Rezaul	CSE 3200 (B1/B2)* 7B05 ( THM , Tushar )								
<b>MON</b>				CSE 3213 7A05 MMH	HUM 3207 7A05	CSE 3215 7A05 MHA	CSE 3214 (B1) 7B08 ( MMH , Sharmin )					
<b>TUE</b>	CSE 3211 7C06 Dr. Rezaul	CSE 3211 7C06 Dr. Rezaul	HUM 3207 7C06				CSE 3216 (B1/B2)* 7B07 ( MHA , SMS )					
<b>WED</b>				CSE 3214 (B2) 7B08 ( MMH , Sharmin )			CSE 3215 7A07 MHA	CSE 3213 7A07 MMH	CSE 3223 7A07 TAB			
<b>THU</b>				CSE 3224 (B1/B2)* 7B05 ( EH , TAB )								

<b>Course no.</b>	<b>Course Title</b>	<b>Cr. Hr.</b>		<b>Course no.</b>	<b>Course Title</b>	<b>Cr. Hr.</b>
<b>HUM3207</b>	Industrial Law and Safety Management	<b>3</b>		<b>CSE3215</b>	Microcontroller Based System Design	<b>3</b>
<b>CSE3200</b>	Software Development-V	<b>0.75</b>		<b>CSE3216</b>	Microcontroller Based System Design Lab	<b>0.75</b>
<b>CSE3211</b>	Data Communication	<b>3</b>		<b>CSE3223</b>	Information System Design and Software Engineering	<b>3</b>
<b>CSE3213</b>	Operating System	<b>3</b>		<b>CSE3224</b>	Information System Design and Software Engineering Lab	<b>0.75</b>
<b>CSE3214</b>	Operating System Lab	<b>1.5</b>				