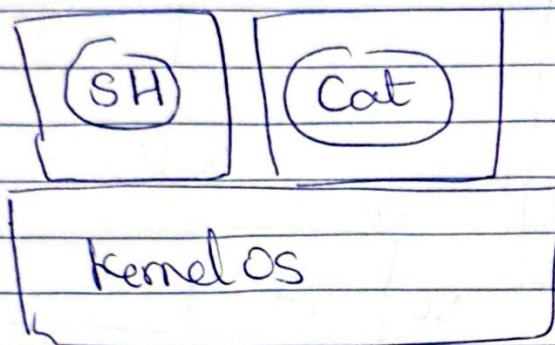


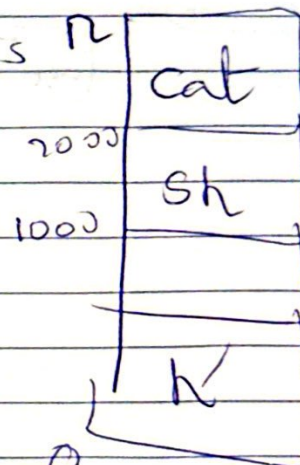
# Page Tables : Lecture 4

## Isolation

what we are trying to do is have some sort of isolation between different modules.



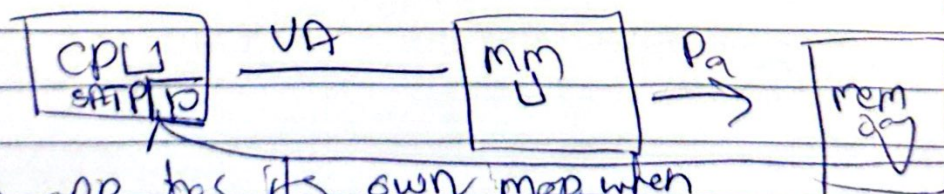
somewhere in the memory there is the kernel, SH, Cat and they make sure that accidentally nothing is referred in the wrong space



so cpu executes some common

\$d \$7(a0) 0x1000

VA	Pa
0x1000	0xFFFF0



every app has its own map when which defines its address space, so when the OS switches CPU from one process to another it also switches the state register to the root of the map to appropriate process



E ADD E

only the kernel can update SATP address

