FX3 VALIDATION BOARD

REV - 1.0

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P-PORT CONNECTIONS

U1A

P - PORT

SEC 1/7

PVDDQ

P_GPIO0_DQ0_MMC_D0	F10	R5	22	P_DQ0	P_DQ0 {4}
P_GPIO1_DQ1_MMC_D1	F9	R205	22	P_DQ1	P_DQ1 {4}
P_GPIO2_DQ2_MMC_D2	F7	R206	22	P_DQ2	P_DQ2 {4}
P_GPIO3_DQ3_MMC_D3	G10	R207	22	P_DQ3	P_DQ3 {4}
P_GPIO4_DQ4_MMC_D4	G9	R208	22	P_DQ4	P_DQ4 {4}
P_GPIO5_DQ5_MMC_D5	F8	R209	22	P_DQ5	P_DQ5 {4}
P_GPIO6_DQ6_MMC_D6	H10	R210	22	P_DQ6	P_DQ6 {4}
P_GPIO7_DQ7_MMC_D7	H9	R211	22	P_DQ7	P_DQ7 {4}
P_GPIO8_DQ8_GPIO	J10	R212	22	P_DQ8	P_DQ8 {4}
P_GPIO9_DQ9_GPIO	J9	R213	22	P_DQ9	P_DQ9 {4}
P_GPIO10_DQ10_GPIO	K11	R214	22	P_DQ10	P_DQ10 {4}
P_GPIO11_DQ11_GPIO	L10	R215	22	P_DQ11	P_DQ11 {4}
P_GPIO12_DQ12_GPIO	K10	R216	22	P_DQ12	P_DQ12 {4}
P_GPIO13_DQ13_GPIO	K9	R217	22	P_DQ13	P_DQ13 {4}
P_GPIO14_DQ14_GPIO	J8	R218	22	P_DQ14	P_DQ14 {4}
P_GPIO15_DQ15_GPIO	G8	R219	22	P_DQ15	P_DQ15 {4}
P_GPIO16_PCLK_MMC-CLK	J6	R220	22	PCLK	PCLK {4}
P_GPIO17_CTL0-CE#_GPIO	K8	R221	22	CTL0	CTL0 {4}
P_GPIO18_CTL1-WE#_MMC-CMD	K7	R222	22	CTL1	CTL1 {4}
P_GPIO19_CTL2-OE#_GPIO	J7	R223	22	CTL2	CTL2 {4}
P_GPIO20_CTL3-DACK#_GPIO	H7	R224	22	CTL3	CTL3 {4}
P_GPIO21_CTL4-DRQ#_GPIO	G7	R225	22	CTL4_SW	
P_GPIO22_CTL5-UART_RX-A7_GPIO	G6	R226	22	CTL5	CTL5 {4}
P_GPIO23_CTL6-UART_TX-A6_GPIO	K6	R227	22	CTL6	CTL6 {4}
P_GPIO24_CTL7-A5_GPIO	H8	R228	22	CTL7	CTL7 {4}
P_GPIO25_CTL8-A4_GPIO	G5	R229	22	CTL8	CTL8 {4}
P_GPIO26_CTL9-A3_GPIO	H6	R230	22	CTL9	CTL9 {4}
P_GPIO27_CTL10-A2_GPIO	K5	R231	22	CTL10	CTL10 {4}
P_GPIO28_CTL11-A1_UART_RX	J5	R232	22	CTL11	CTL11 {4}
P_GPIO29_CTL12-A0_UART_TX	H5	R233	22	CTL12	CTL12 {4}
P_GPIO30_PMODE0	G4	R234	22	PMODE_0	
P_GPIO31_PMODE1	H4	R235	22	PMODE_1	
P_GPIO32_PMODE2	L4	R236	22	PMODE_2	
INT_N_CTL15	L8			INT_N_CTL15	INT_N_CTL15 {4}
RESET_N	C5			RESET_N	RESET_N {3}

TEST0	K4	TEST0
TEST1	L3	TEST1
TEST2	K3	TEST2
TEST3	L2	TEST3
TEST4	A8	TEST4

PVDDQ

R170 DNL

INT_N_CTL15

FX3_121_BUMP

J1

MICTOR

PCLK
P_DQ2
P_DQ14
P_DQ1
P_DQ3
P_DQ0
CTL4_SW
P_DQ12
P_DQ11
P_DQ5
P_DQ13
P_DQ15
P_DQ9
CTL8
CTL9
CTL6
CTL10

CTL3
P_DQ6
P_DQ7
CTL0
CTL7
P_DQ4
CTL5
INT_N_CTL15
CTL11
CTL1
CTL2
CTL12
P_DQ10
P_DQ8

2-767004-2
PART_NUMBER = 2-767004-2
Manufacturer = Tyco Electronics

J2

MICTOR

TEST3
TEST2
TEST1
RESET_N
TEST0
PMODE_2
PMODE_1
PMODE_0
TEST4

2-767004-2
PART_NUMBER = 2-767004-2
Manufacturer = Tyco Electronics

SW1

TEST0
TEST1
TEST0_CON {4}
TEST0_DIP
TEST1_CON {4}
TEST1_DIP
SW3A
SW3B
DIP6

DPDT SWITCH
PART_NUMBER = CAS-220TB
Manufavturer = -

PART_NUMBER = CHS-06TA
Manufacturer = Copal

SW2

TEST2
TEST3
TEST2_CON {4}
TEST2_DIP
TEST3_CON {4}
TEST3_DIP
SW3C
SW3D

DPDT SWITCH

SW4

TEST4
TEST4_CON {4}
TEST4_DIP
SW3E

DPDT SWITCH
PART_NUMBER = CAS-220TB
Manufavturer = -

SW5

PMODE_0
PMODE_1
PMODE0_CON {4}
PMODE0_DIP
PMODE1_CON {4}
PMODE1_DIP
SW6A
SW6B
DIP4

DPDT SWITCH

SW7

PMODE_2
PMODE2_CON {4}
PMODE2_DIP
SW6C

DPDT SWITCH

PVDDQ

TEST0_DIP	R1	10K
TEST1_DIP	R2	10K
TEST2_DIP	R3	10K
TEST3_DIP	R4	10K
TEST4_DIP	R19	10K
PMODE0_DIP	R6	10K
PMODE1_DIP	R7	10K
PMODE2_DIP	R8	10K

SW31

CTL4_SW
CTL4 {4}
CTL4_LT {3}

DPDT SWITCH

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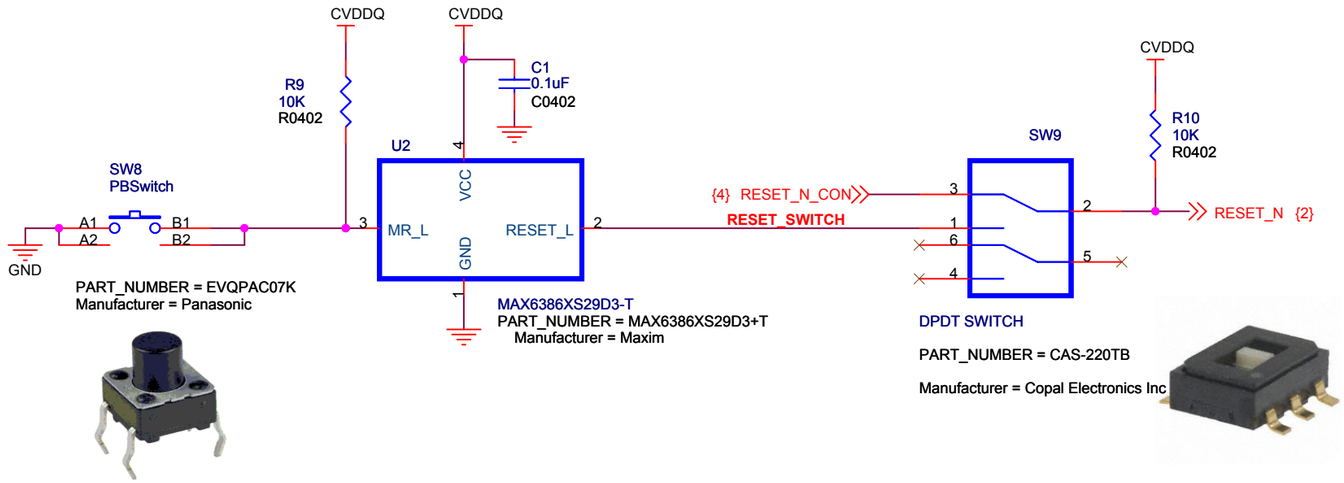
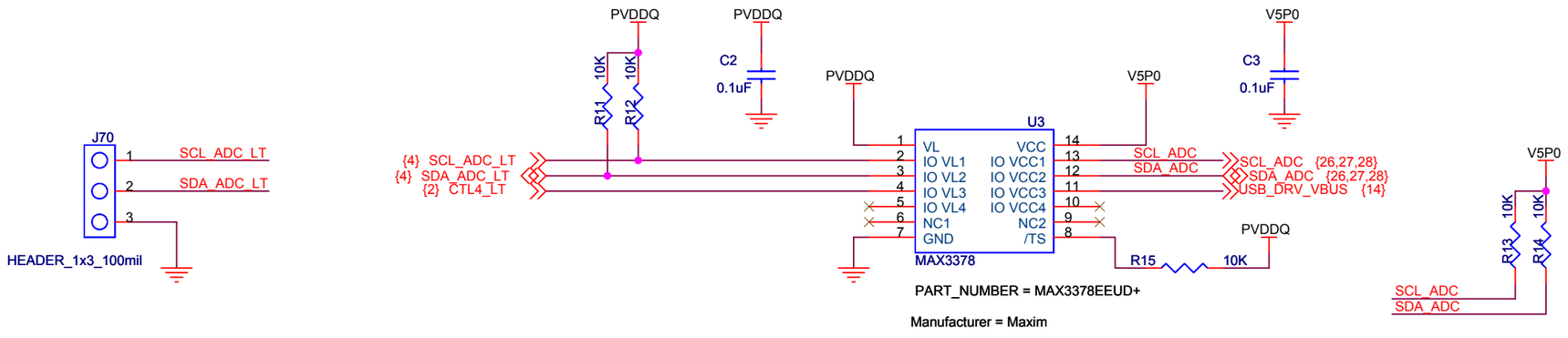
P-PORT CONNECTIONS

RESET_CONNECTION



LEVEL TRANSLATOR

FOR CURRENT SENSING ADC - I2C



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P PORT TO SAMTEC

J77A

QSH-060

SEC 1/2

PVDDQ_R	1	2	PVDDQ_R
	3	4	PVDDQ_R
	5	6	
	7	8	
	9	10	
	11	12	
	13	14	
	15	16	
	17	18	
	19	20	
{2} TEST0_CON	21	22	
{2} TEST1_CON	23	24	SDA_ADC_LT {3}
{2} TEST2_CON	25	26	S1PORT_SEL2_LT {8}
{2} TEST3_CON	27	28	S1PORT_SEL1_LT {8}
{8} S1PORT_SEL3_LT	29	30	PMODE2_CON {2}
{2} CTL3	31	32	P_DQ6 {2}
{2} CTL0	33	34	
	35	36	PMODE0_CON {2}
{2} CTL7	37	38	S1_DQ28 {7}
{2} CTL5	39	40	S1_DQ30 {7}
CTL9_HDR	41	42	P_DQ7 {2}
CTL10_HDR	43	44	PCLK {2}
CTL6_HDR	45	46	PMODE1_CON {2}
{2} TEST4_CON	47	48	
CTL8_HDR	49	50	P_DQ4 {2}
{2} CTL1	51	52	INT_N_CTL15 {2}
	53	54	CTL11 {2}
{2} CTL2	55	56	S0_DQ16 {5}
{2} CTL12	57	58	S0_DQ27 {5}
{3} RESET_N_CON	59	60	CTL4 {2}
	121	122	
	123	124	

J67 HDR_1X2_100MIL: 1 — 2 SCL_ADC_LT {3}

J68 HDR_1X2_100MIL: 1 — 2 SDA_ADC_LT {3}

QSH_120pin_CONN
PART_NUMBER = QSH-060-01-L-D-A
Manufacturer = Samtec Inc

J77B

QSH-060

SEC 2/2

{2} P_DQ10	61	62	S1_DQ29 {7}
{5} S0_DQ26	63	64	S1_DQ31 {7}
{2} P_DQ14	65	66	P_DQ8 {2}
{5} S0_DQ23	67	68	P_DQ0 {2}
{5} S0_DQ22	69	70	P_DQ3 {2}
{2} P_DQ12	71	72	S0_DQ25 {5}
{5} S0_DQ18	73	74	P_DQ1 {2}
{5} S0_DQ21	75	76	P_DQ2 {2}
{5} S0_DQ20	77	78	S0_DQ24 {5}
{2} P_DQ11	79	80	P_DQ5 {2}
{2} P_DQ13	81	82	S0PORT_SEL1_LT {5}
{2} P_DQ15	83	84	S0PORT_SEL2_LT {5}
{5} S0_DQ17	85	86	I2C_SDA_PPORT {6,13}
{5} S0_DQ19	87	88	I2C_SCL_PPORT {6,13}
{2} P_DQ9	89	90	
	91	92	
	93	94	
	95	96	
	97	98	
	99	100	
	101	102	
	103	104	
	105	106	
	107	108	
	109	110	
	111	112	
	113	114	
	115	116	
PVDDQ_R	117	118	PVDDQ_R
	119	120	
	125	126	
	127	128	

V5P0 — V5P0 — PVDDQ

R171 DNL R1206

QSH_120pin_CONN
PART_NUMBER = QSH-060-01-L-D-A
Manufacturer = Samtec Inc

GPIO- 23/25-27 HDR OPTION

{2} CTL6	J79	CTL6_HDR	{2} CTL9	J76	CTL9_HDR
{2} CTL8	J75	CTL8_HDR	{2} CTL10	J78	CTL10_HDR

HDR_1X2_100MIL

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S0-PORT CONNECTIONS

U1B

S0 - PORT

SEC 2/7

S0VDDQ

S0-GPIO33_S0-SD0_DQ16_GPIO	K2	R237	22	S0_GPIO33_DQ16	S0_GPIO33_DQ16 {6}
S0-GPIO34_S0-SD1_DQ17_GPIO	J4	R238	22	S0_GPIO34_DQ17	S0_GPIO34_DQ17 {6}
S0-GPIO35_S0-SD2_DQ18_GPIO	K1	R239	22	S0_GPIO35_DQ18	S0_GPIO35_DQ18 {6}
S0-GPIO36_S0-SD3_DQ19_GPIO	J2	R240	22	S0_GPIO36_DQ19	S0_GPIO36_DQ19 {6}
S0-GPIO37_S0-SD4_DQ20_GPIO	J3	R241	22	S0_GPIO37_DQ20	S0_GPIO37_DQ20 {6}
S0-GPIO38_S0-SD5_DQ21_GPIO	J1	R242	22	S0_GPIO38_DQ21	S0_GPIO38_DQ21 {6}
S0-GPIO39_S0-SD6_DQ22_GPIO	H2	R243	22	S0_GPIO39_DQ22	S0_GPIO39_DQ22 {6}
S0-GPIO40_S0-SD7_DQ23_GPIO	H3	R244	22	S0_GPIO40_DQ23	S0_GPIO40_DQ23 {6}
S0-GPIO41_S0-CMD_DQ24_GPIO	F4	R245	22	S0_GPIO41_DQ24	S0_GPIO41_DQ24 {6}
S0-GPIO42_S0-CLK_DQ25_GPIO	G2	R246	22	S0_GPIO42_DQ25	S0_GPIO42_DQ25 {6}
S0-GPIO43_S0-WP_DQ26_GPIO	G3	R247	22	S0_GPIO43_DQ26	S0_GPIO43_DQ26 {6}
S0-GPIO44_S0S1-INS_DQ27_GPIO	F3	R248	22	S0_GPIO44_DQ27	S0_GPIO44_DQ27 {6}
S0-GPIO45_MMC1RSTOUT_GPIO	F2	R249	22		S0_GPIO45_MMC1_RSTOUT {6}

TP13

FX3_121_BUMP

SPDT SWITCH OPTION
S0-GPIO (33 - 44)

SW15

S0_GPIO33_DQ16	2	A0	0B1 / 0B2	48 / 45	GPIO33 TP1 S0_DQ16 {4}
S0_GPIO34_DQ17	4	A1	1B1 / 1B2	47 / 44	GPIO34 TP2 S0_DQ17 {4}
S0_GPIO35_DQ18	8	A2	2B1 / 2B2	42 / 39	GPIO35 TP3 S0_DQ18 {4}
S0_GPIO36_DQ19	10	A3	3B1 / 3B2	41 / 38	GPIO36 TP4 S0_DQ19 {4}
S0_GPIO37_DQ20	15	A4	4B1 / 4B2	35 / 32	GPIO37 TP5 S0_DQ20 {4}
S0_GPIO38_DQ21	17	A5	5B1 / 5B2	34 / 31	GPIO38 TP6 S0_DQ21 {4}
S0_GPIO39_DQ22	21	A6	6B1 / 6B2	29 / 26	GPIO39 TP7 S0_DQ22 {4}
S0_GPIO40_DQ23	23	A7	7B1 / 7B2	28 / 25	GPIO40 TP8 S0_DQ23 {4}

S0PORT_SEL1

SEL 24; NC 14; GND 3, 5, 7, 9, 11, 13, 16; GND 18, 20, 22, 27, 30, 33, 37, 40, 43, 46

8BIT SPDT

Manufacturer = TI

PART_NUMBER = TS3L301DGVR

SW34

S0_GPIO41_DQ24	2	A0	0B1 / 0B2	48 / 45	GPIO41 TP9 S0_DQ24 {4}
S0_GPIO42_DQ25	4	A1	1B1 / 1B2	47 / 44	GPIO42 TP10 S0_DQ25 {4}
S0_GPIO43_DQ26	8	A2	2B1 / 2B2	42 / 39	GPIO43 TP11 S0_DQ26 {4}
S0_GPIO44_DQ27	10	A3	3B1 / 3B2	41 / 38	GPIO44 TP12 S0_DQ27 {4}
	15	A4	4B1 / 4B2	35 / 32	
	17	A5	5B1 / 5B2	34 / 31	
	21	A6	6B1 / 6B2	29 / 26	
	23	A7	7B1 / 7B2	28 / 25	

S0PORT_SEL2

SEL 24; NC 14; GND 3, 5, 7, 9, 11, 13, 16; GND 18, 20, 22, 27, 30, 33, 37, 40, 43, 46

8BIT SPDT

Manufacturer = TI

PART_NUMBER = TS3L301DGVR

V3P3

R204 10K, R203 10K

SW35

1 → 4 S0PORT_SEL1

2 → 3 S0PORT_SEL2

DIP_SWITCH_2POS

PART_NUMBER = 76SB02ST

Manufavturer = Grayhill Inc

PVDDQ

C208 0.1uF

C209 0.1uF

U15

1	VL	VCC	14
2	IO VL1	IO VCC1	13
3	IO VL2	IO VCC2	12
4	IO VL3	IO VCC3	11
5	IO VL4	IO VCC4	10
6	NC1	NC2	9
7	GND	/TS	8

{4} S0PORT_SEL1_LT ← S0PORT_SEL1_LT

{4} S0PORT_SEL2_LT ← S0PORT_SEL2_LT

S0PORT_SEL1 → V3P3

S0PORT_SEL2 → V3P3

MAX3378

R16 10K → PVDDQ

PART_NUMBER = MAX3378EEUD+

Manufacturer = Maxim

C202 0.1uF C0402; C201 0.1uF C0402; C200 0.1uF C0402; C198 0.1uF C0402; C199 0.1uF C0402

C207 0.1uF C0402; C205 0.1uF C0402; C206 0.1uF C0402; C204 0.1uF C0402; C203 0.1uF C0402

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S1-PORT CONNECTIONS

U1C

S1 - PORT

SEC 3/7

S1VDDQ	S1-GPIO46_S1-SD0_GPIO_DQ28_UART-RTS	F5	R250	22	S1_GPIO46_DQ28_U1-RTS {7}
	S1-GPIO47_S1-SD1_GPIO_DQ29_UART-CTS	E1	R251	22	S1_GPIO47_DQ29_U1-CTS {7}
	S1-GPIO48_S1-SD2_GPIO_DQ30_UART-TX	E5	R252	22	S1_GPIO48_DQ30_U1-TX {7}
	S1-GPIO49_S1-SD3_GPIO_DQ31_UART-RX	E4	R253	22	S1_GPIO49_DQ31_U1-RX {7}
	S1-GPIO50_S1-CMD_GPIO_I2S-CLK	D1	R254	22	S1_GPIO50_I2S-CLK {7}
	S1-GPIO51_S1-CLK_GPIO_I2S-SD	D2	R255	22	S1_GPIO51_I2S-SD {7}
	S1-GPIO52_S1-WP_GPIO_I2S-WS	D3	R256	22	S1_GPIO52_I2S-WS {7}
LVDDQ	S1-L_GPIO53_S1-SD4_SPI-SCK_GPIO_UART-RTS_SPI-SCK	D4	R257	22	S1_GPIO53_SPI-SCK_U2-RTS_GPIO53 {8}
	S1-L_GPIO54_S1-SD5_SPI-SSN_GPIO_UART-CTS_I2S-CLK_SPI-SSN	C1	R258	22	S1_GPIO54_SPI-SSN_U2-CTS_I2S-CLK {8}
	S1-L_GPIO55_S1-SD6_SPI-MISO_GPIO_UART-TX_I2S-SD_SPI-MISO	C2	R259	22	S1_GPIO55_SPI-MISO_U2-TX_I2S-SD {8}
	S1-L_GPIO56_S1-SD7_SPI-MOSI_GPIO_UART-RX_I2S-WS_SPI-MOSI	D5	R260	22	S1_GPIO56_SPI-MOSI_U2-RX_I2S-WS {8}
	S1-L_GPIO57_MMC2RSTOUT_GPIO_I2S-MCLK	C4	R261	22	S1_GPIO57_I2S-MCLK {7}

FX3_121_BUMP

S0 & S1-PORT TO MICTOR

J37

MICTOR

{7} S1_GPIO46_DQ28_U1-RTS	1	2	S0_GPIO33_DQ16 {5}
{7} S1_GPIO47_DQ29_U1-CTS	3	4	S0_GPIO34_DQ17 {5}
{7} S1_GPIO48_DQ30_U1-TX	5	6	S0_GPIO35_DQ18 {5}
{7} S1_GPIO49_DQ31_U1-RX	7	8	S0_GPIO36_DQ19 {5}
{7} S1_GPIO50_I2S-CLK	9	10	S0_GPIO37_DQ20 {5}
{7} S1_GPIO51_I2S-SD	11	12	S0_GPIO38_DQ21 {5}
{7} S1_GPIO52_I2S-WS	13	14	S0_GPIO39_DQ22 {5}
{8} S1_GPIO53_SPI-SCK_U2-RTS_GPIO53	15	16	S0_GPIO40_DQ23 {5}
{8} S1_GPIO54_SPI-SSN_U2-CTS_I2S-CLK	17	18	S0_GPIO41_DQ24 {5}
{8} S1_GPIO55_SPI-MISO_U2-TX_I2S-SD	19	20	S0_GPIO42_DQ25 {5}
{8} S1_GPIO56_SPI-MOSI_U2-RX_I2S-WS	21	22	S0_GPIO43_DQ26 {5}
{7} S1_GPIO57_I2S-MCLK	23	24	S0_GPIO44_DQ27 {5}
	25	26	S0_GPIO45_MMC1_RSTOUT {5}
	27	28	I2C_SCL_PPORT {4,13}
	29	30	I2C_SDA_PPORT {4,13}
	31	32	I2C_CHARGER_DETECT {13}
{13} I2C_SCL_FX3	33	34	
{13} I2C_SDA_FX3	35	36	
	37	38	
	39		
	40		
	41		
	42		
	43		

2-767004-2
PART_NUMBER = 2-767004-2
Manufacturer = Tyco Electronics

I2S1 / I2S2 SELECTION

J17

1 — I2S1_CLK {7}
2 — I2S_CLK
3 — I2S2_CLK {8}

HEADER_1x3_100mil

J18

1 — I2S1_SD {7}
2 — I2S_SD
3 — I2S2_SD {8}

HEADER_1x3_100mil

J19

1 — I2S1_WP {7}
2 — I2S_WP
3 — I2S2_WP {8}

HEADER_1x3_100mil

I2S HEADER

LVDDQ
S1VDDQ
V5P0

J20

	1	14	
	2	13	
	3	12	
I2S_CLK	4	11	
I2S_SD	5	10	
I2S_WP	6	9	
I2S_MCLK {7}	7	8	

HDR14_100MIL

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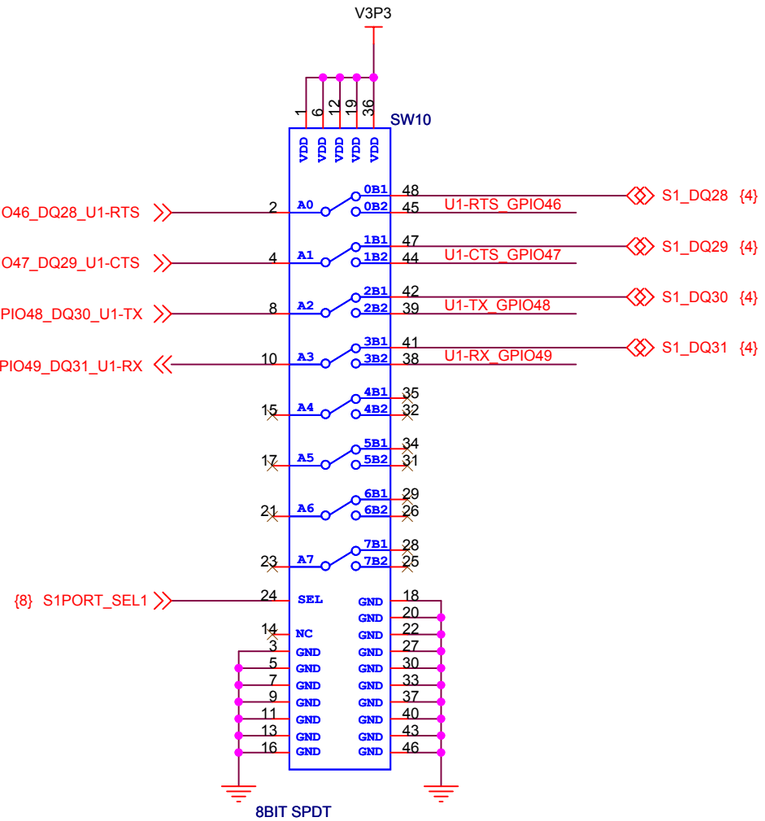
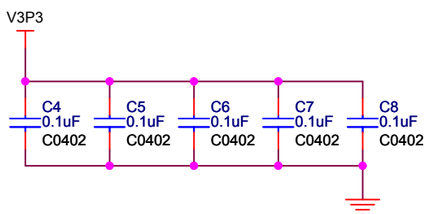
S1-GPIO (46-52 & 57)

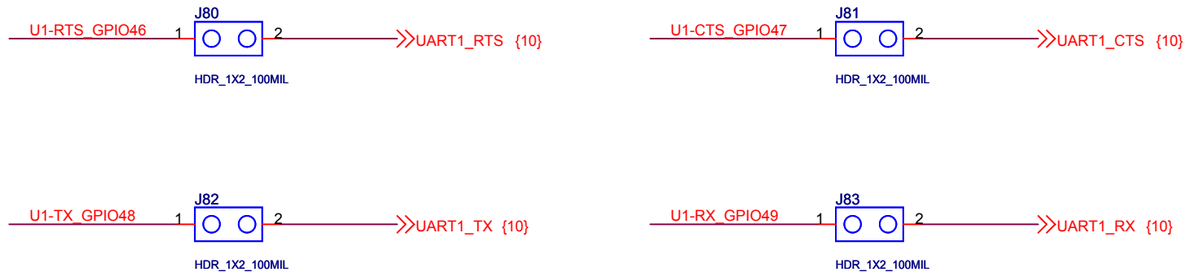
SPDT SWITCH OPTION



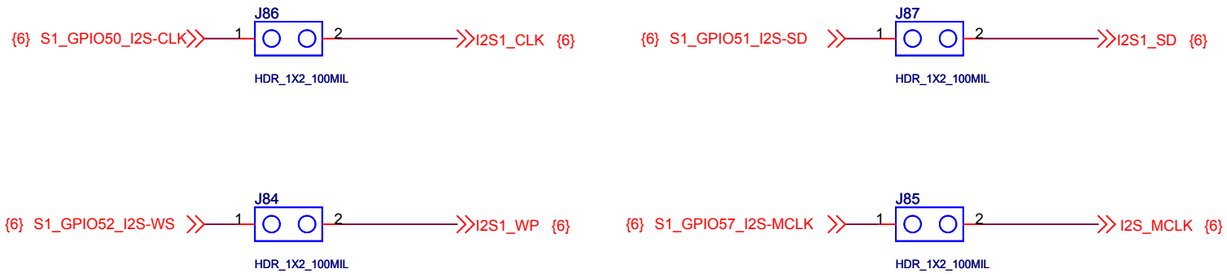
Manufacturer = TI
PART_NUMBER = TS3L301DGVR



S1-GPIO (46-49) : SD / GPIO / UART1



S1-GPIO (50-52 & 57) : SD / GPIO / I2S1



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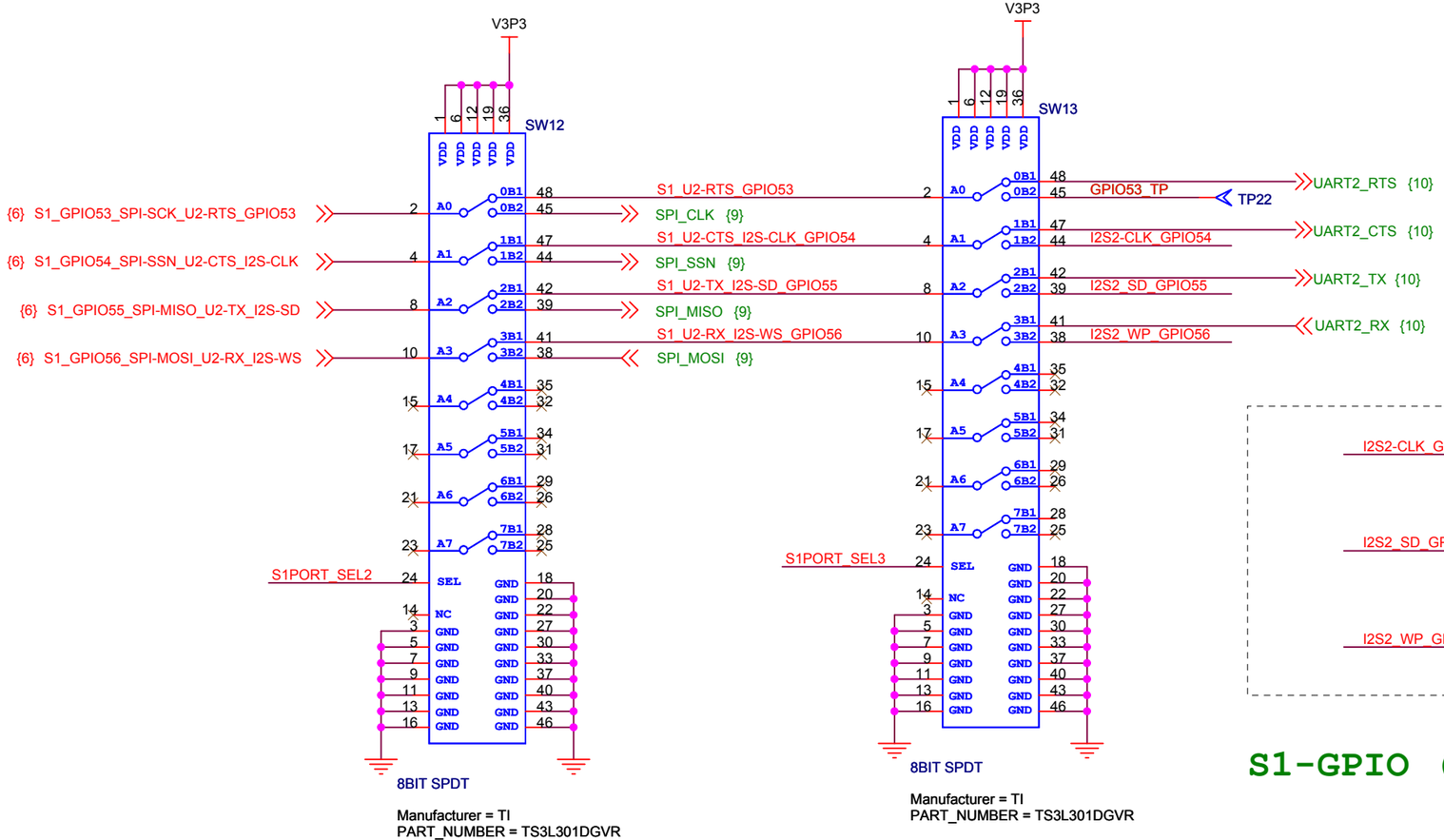
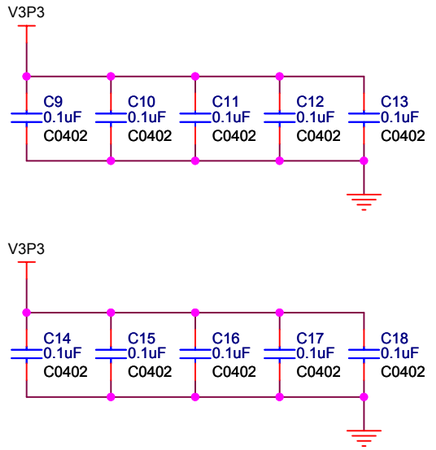
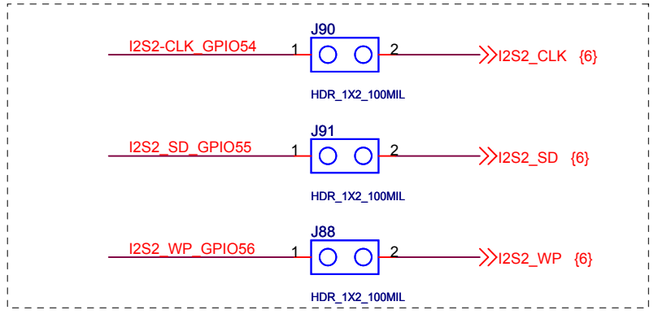
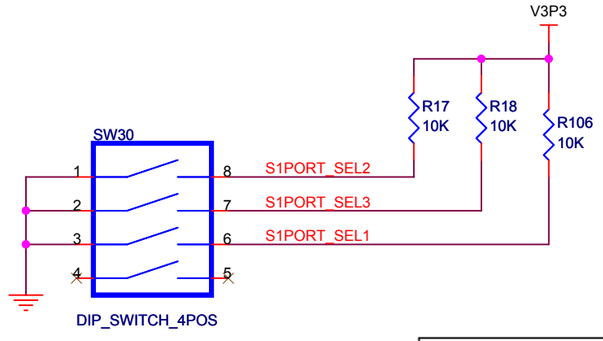
S1-GPIO (53-56) : SPDT SWITCH OPTION





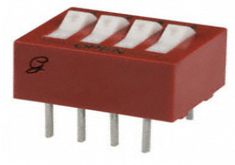


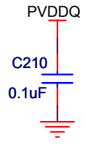
S1-GPIO (53-56) : SD/ GPIO/ UART2/ SPI/ I2S2

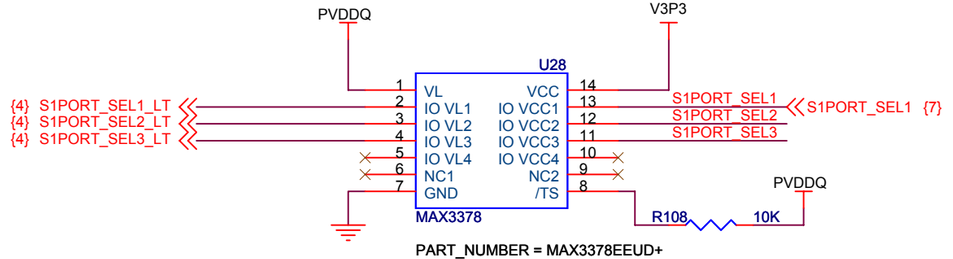


PART_NUMBER = 76SB04ST

Manufacturer = Grayhill Inc

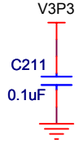






PART_NUMBER = MAX3378EEUD+

Manufacturer = Maxim



INPUT SEL	INPUT/OUTPUT An	FUNCTION
L	nB_1	$A_n = nB_1$
H	nB_2	$A_n = nB_2$

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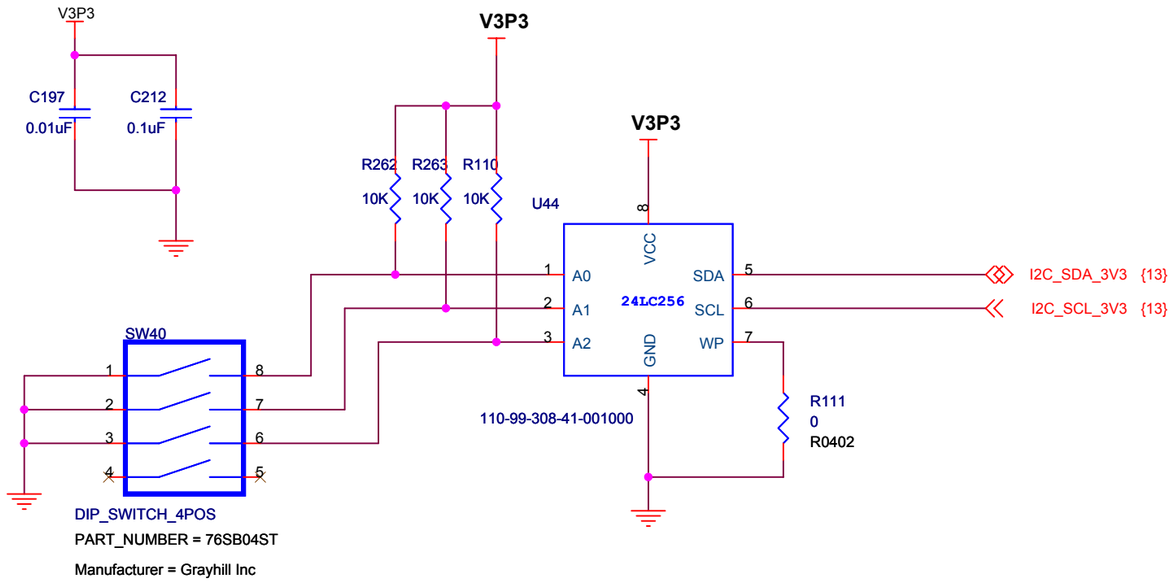
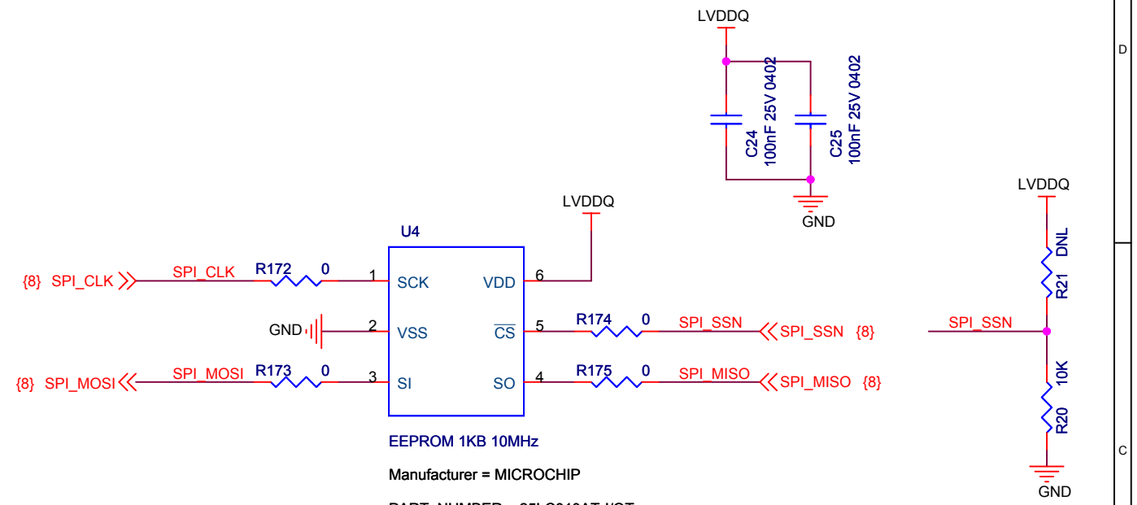
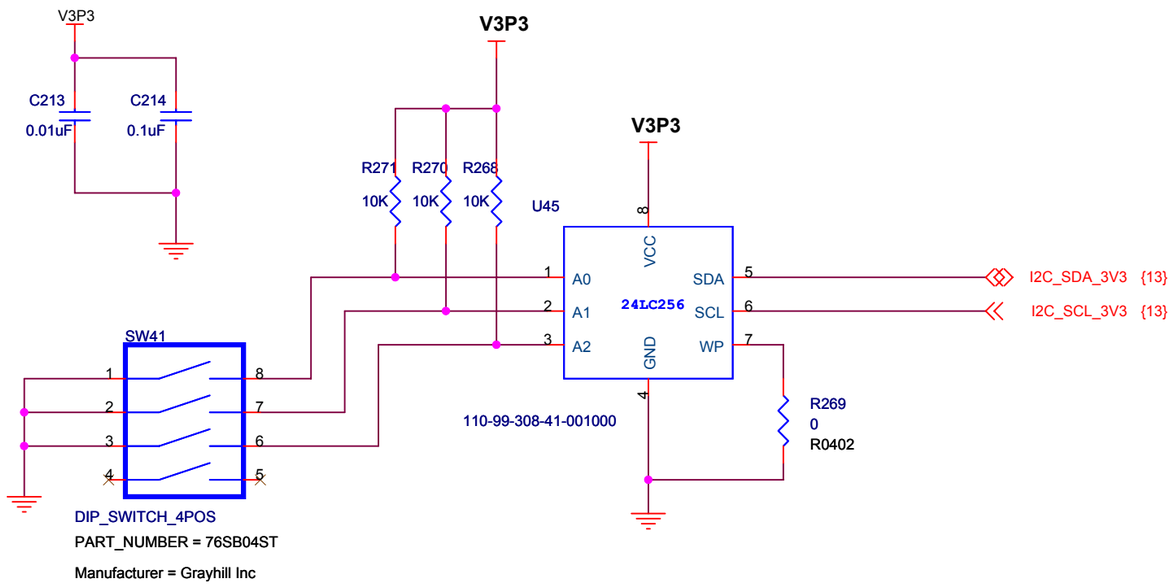
I2C EEPROM

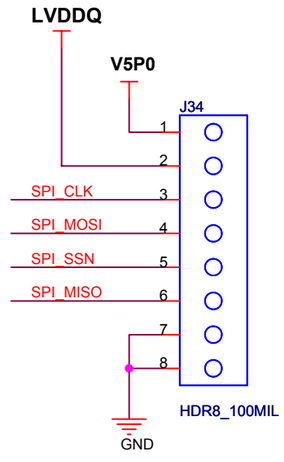


SPI EEPROM





SPI HEADER



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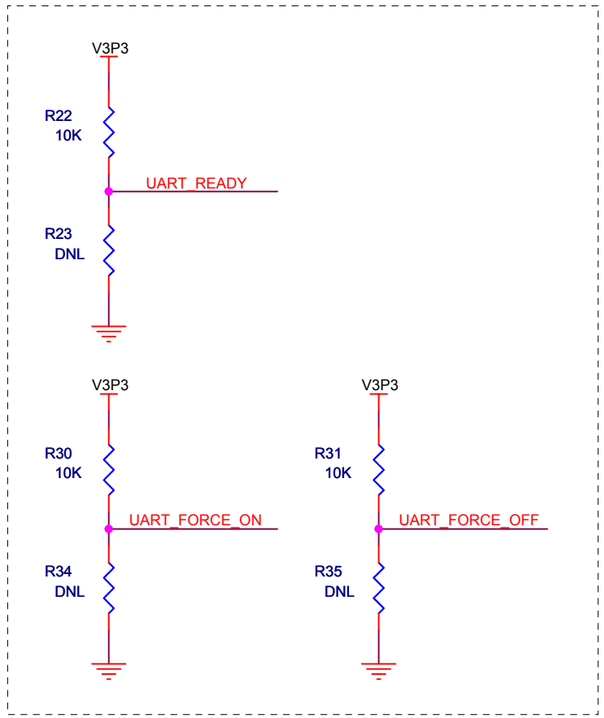
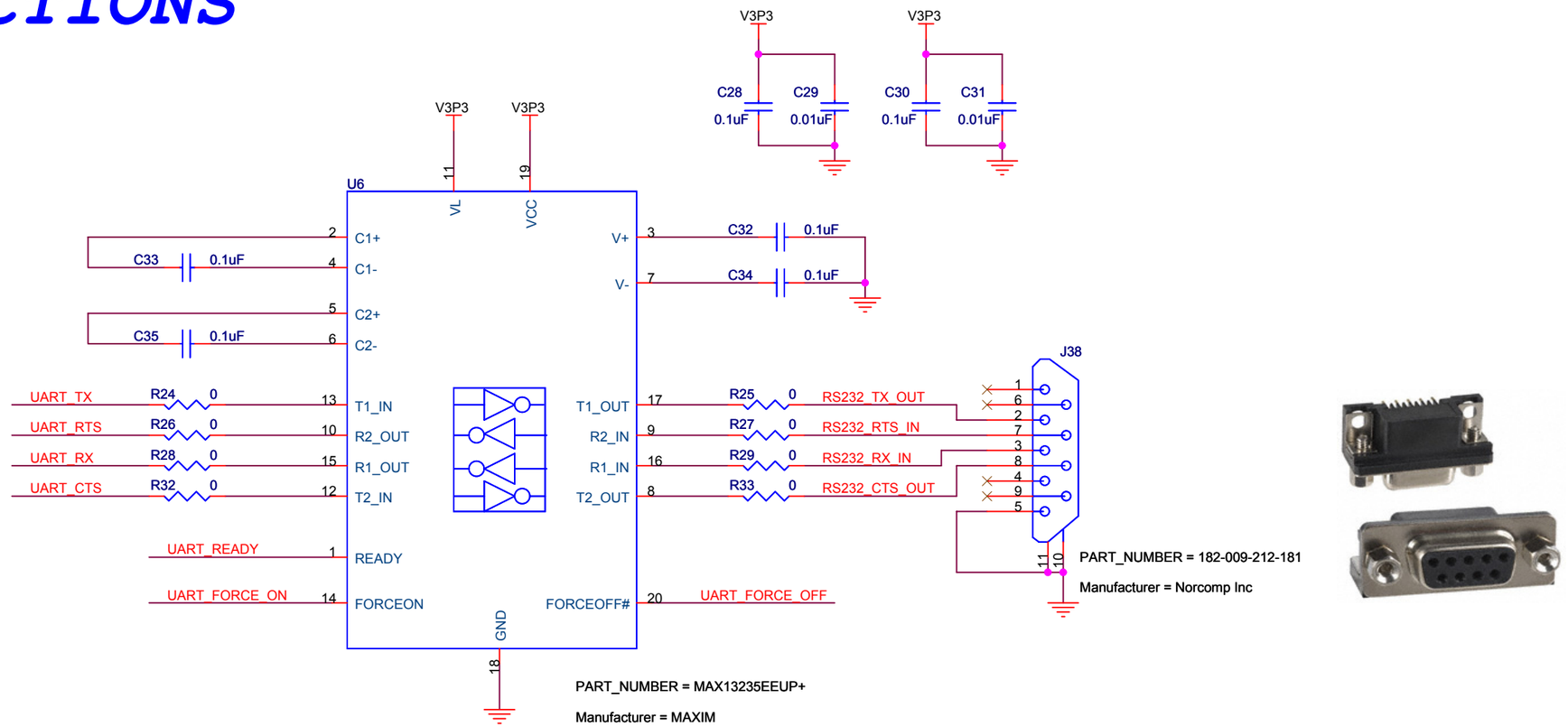
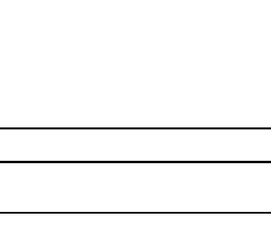
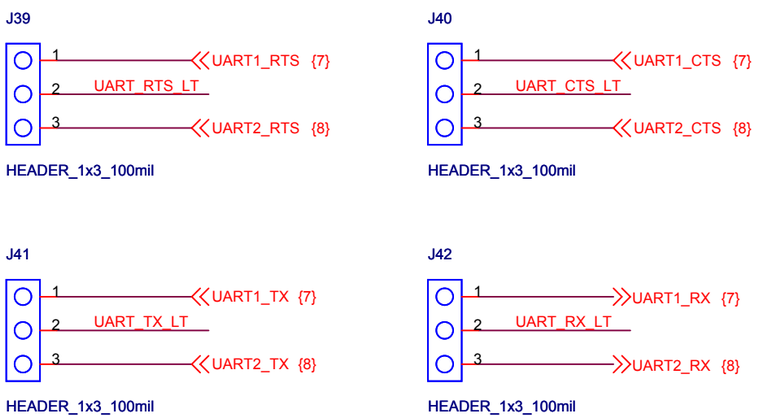
S1 UART CONNECTIONS



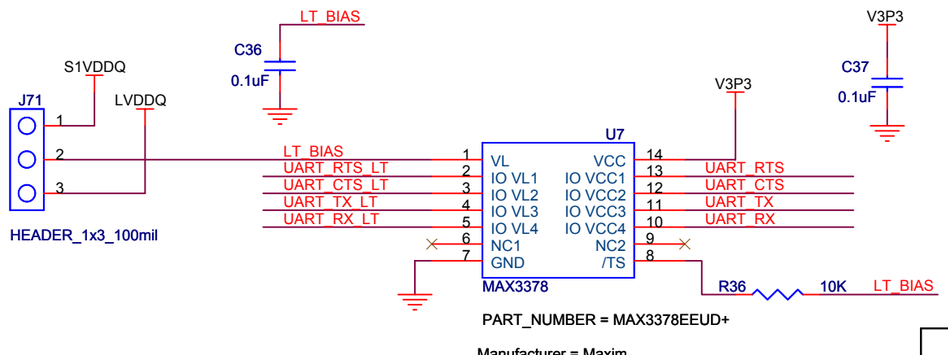




UART 1 / UART 2 SELECTION



LEVEL TRANSLATOR



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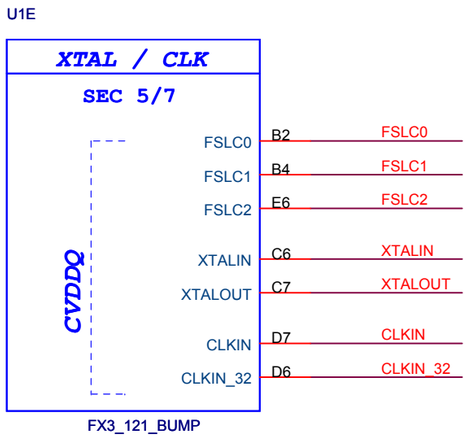
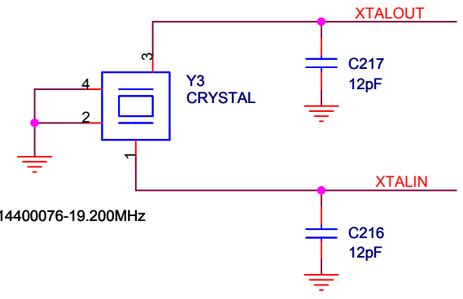
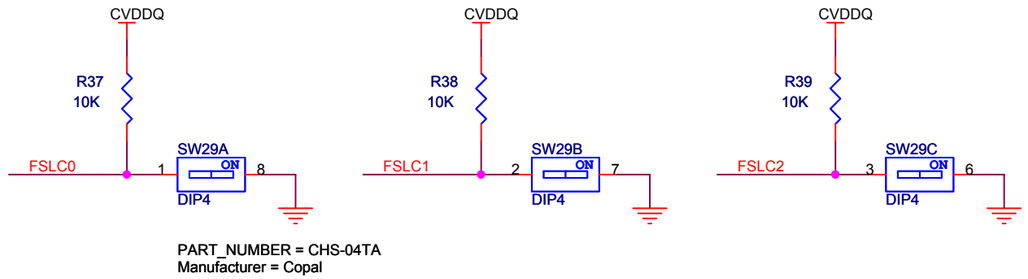
CLOCK SIGNALS



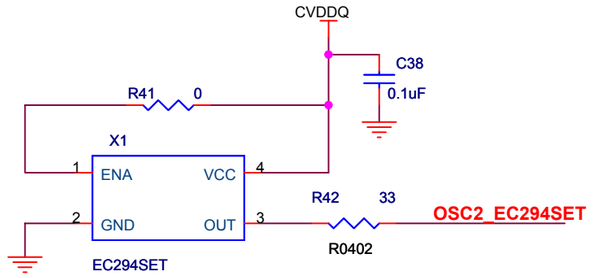
CRYSTAL



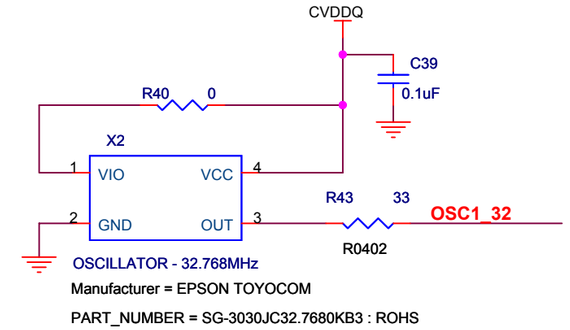
FSLC [0..2]

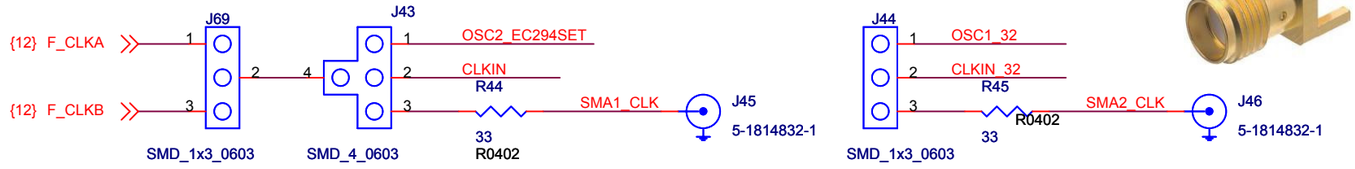


CLOCK IN



CLOCK IN_32





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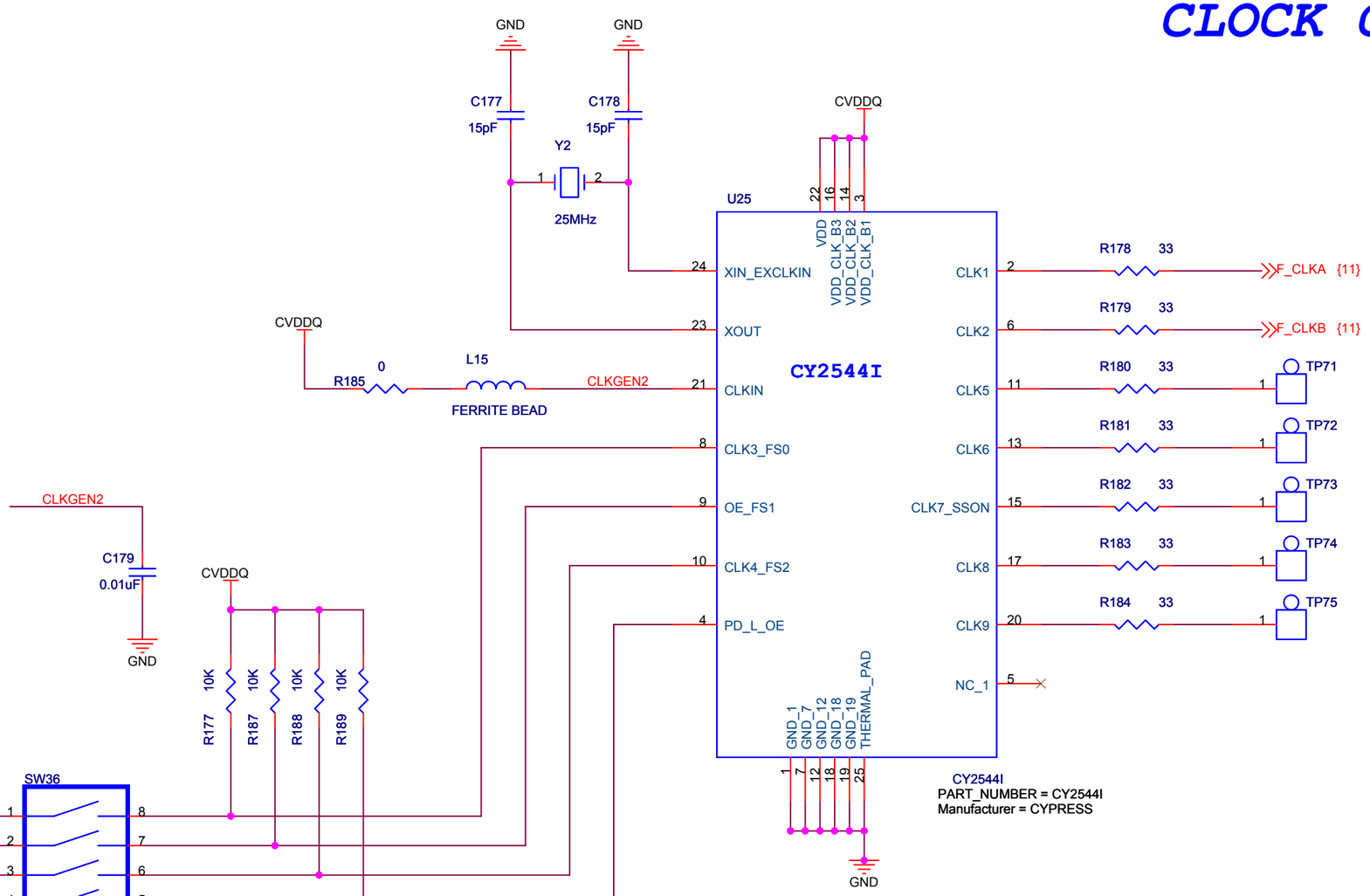
CLOCK GENERATOR



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JTAG CONNECTIONS

U1F

MISC

SEC 6/7

I2C_VDDQ

I2C-GPIO58_SCL D9 — TP29 — I2C_SCL_FX3 {6}

I2C-GPIO59_SDA D10 — TP30 — I2C_SDA_FX3 {6}

I2C-GPIO60_CHARGER-DETECT D11 — TP31 — I2C_CHARGER_DETECT {6}

TCK F6 — TCK

TDI E7 — TDI

TDO C10 — TDO

TMS E8 — TMS

TRST_N B11 — TRST_N

FX3_121_BUMP

I2C_VDDQ

I2C_SCL_FX3 R176 10K

I2C_SDA_FX3 R186 10K

J52

1 2 3

HEADER_1x3_100mil

DEFAULT : PIN 2-3

I2C_VDDQ

R51 10K

R52 10K

R53 10K

R54 10K

R55 10K

J51

JTAG_ICE_CONN

1	VT_REF	V_SUPPLY	2
3	N_TRST	GND1	4
5	TDI	GND2	6
7	TMS	GND3	8
9	TCK	GND4	10
11	RTCK	GND5	12
13	TDO	GND6	14
15	N_SRST	GND7	16
17	DBGRQ	GND8	18
19	DBGACK	GND9	20

TRST_N

TDI

TMS

TCK

RTCK

TDO

N_SRST

I2C_VDDQ

PART_NUMBER = 2520-6002UB

Manufacturer = 3M

Level Translation

J66

1 I2C_SCL_LT

2 I2C_SCL_FX3

3 I2C_SCL_PPORT {4,6}

HEADER_1x3_100mil

J89

1 I2C_SDA_LT

2 I2C_SDA_FX3

3 I2C_SDA_PPORT {4,6}

HEADER_1x3_100mil

I2C_VDDQ

C134 0.1uF

I2C_VDDQ

V3P3

C135 0.1uF

U29

1	VL	VCC	14
2	IO VL1	IO VCC1	13
3	IO VL2	IO VCC2	12
4	IO VL3	IO VCC3	11
5	IO VL4	IO VCC4	10
6	NC1	NC2	9
7	GND	/TS	8

I2C_SCL_LT

I2C_SDA_LT

I2C_CHARGER_DETECT

I2C_SCL_3V3 {9}

I2C_SDA_3V3 {9}

I2C_CHARGER_DETECT_3V3 {15}

PVDDQ

R109 10K

MAX3378

PART_NUMBER = MAX3378EEUD+

Manufacturer = Maxim

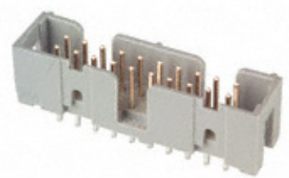
V3P3

R113 10K

R115 10K

I2C_SCL_3V3

I2C_SDA_3V3



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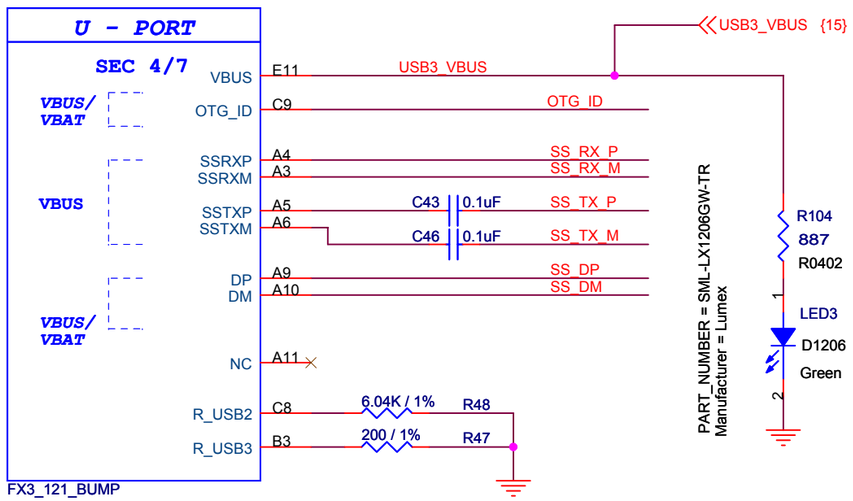
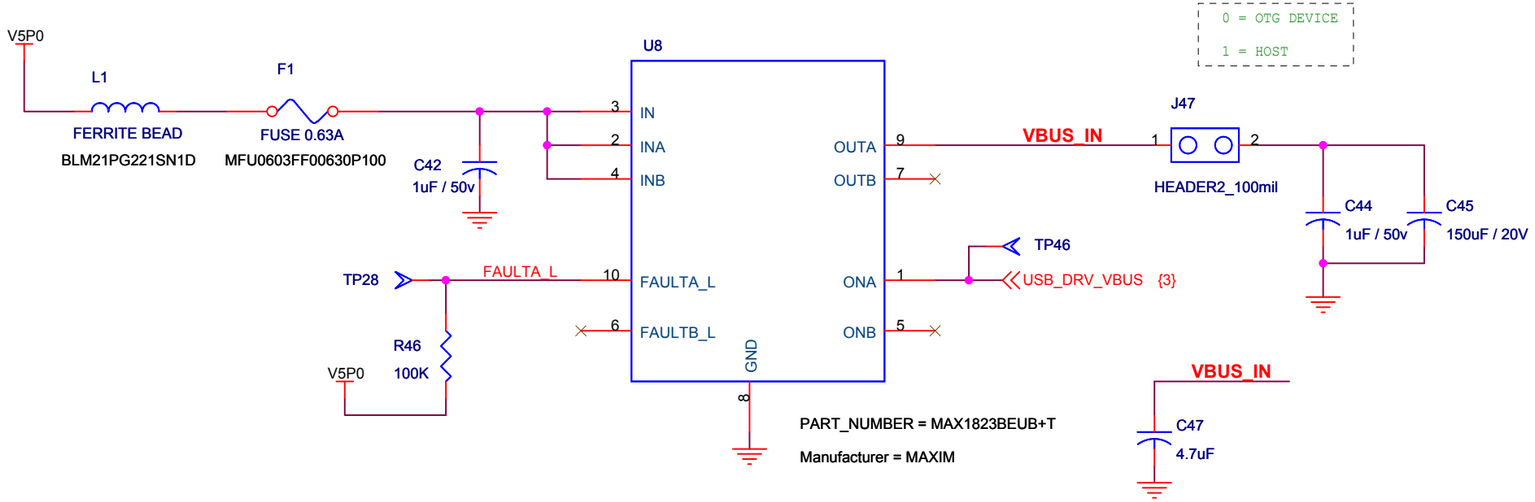
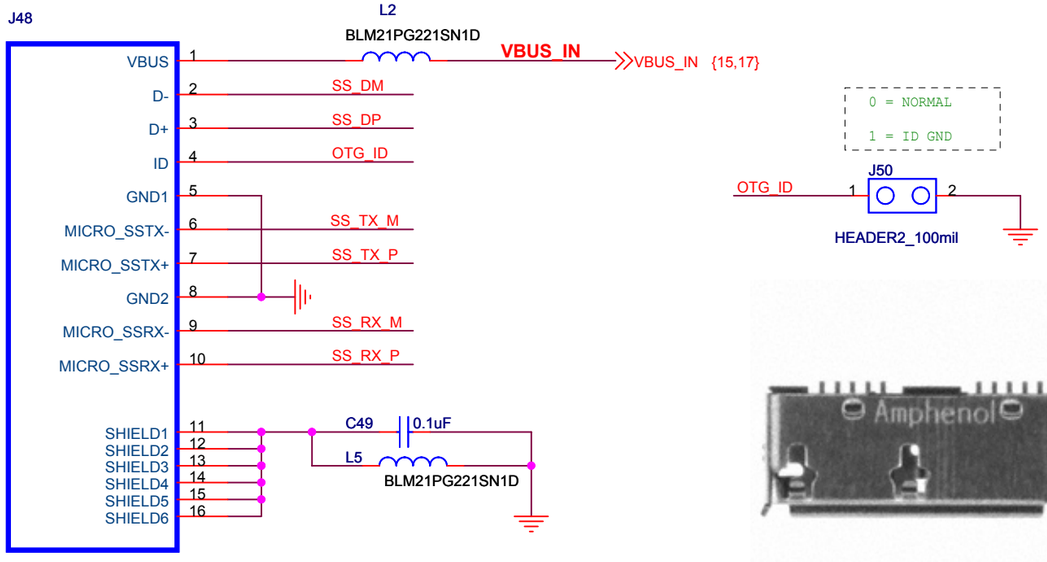
USB CONNECTIONS





USB3.0 MICRO -TYPE B



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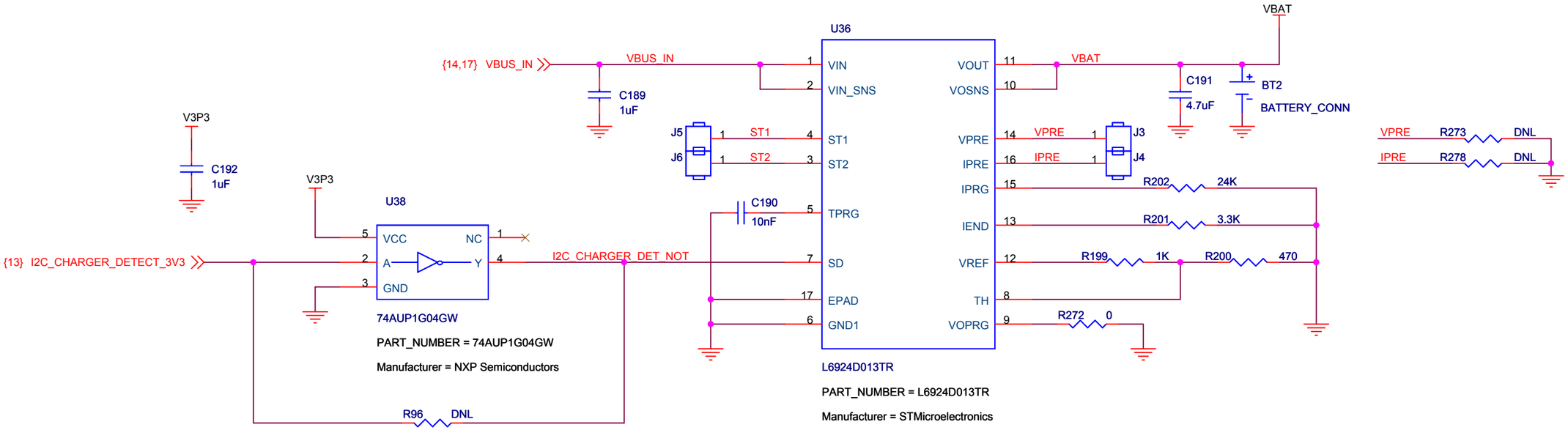
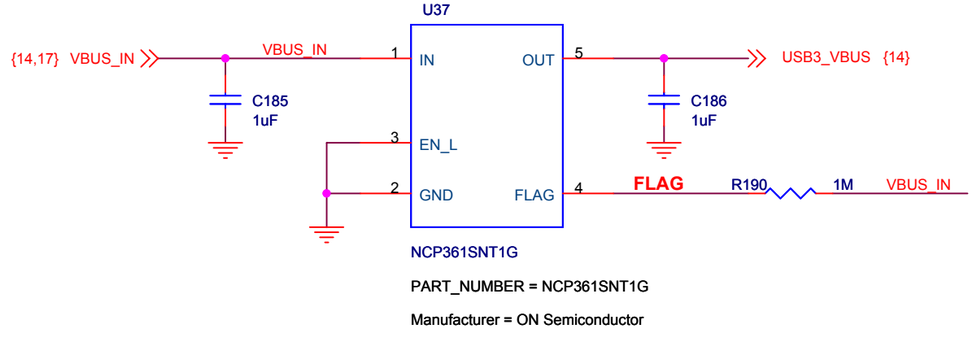
BATTERY CHARGER SYSTEM



USB Positive Overvoltage Protection Controller



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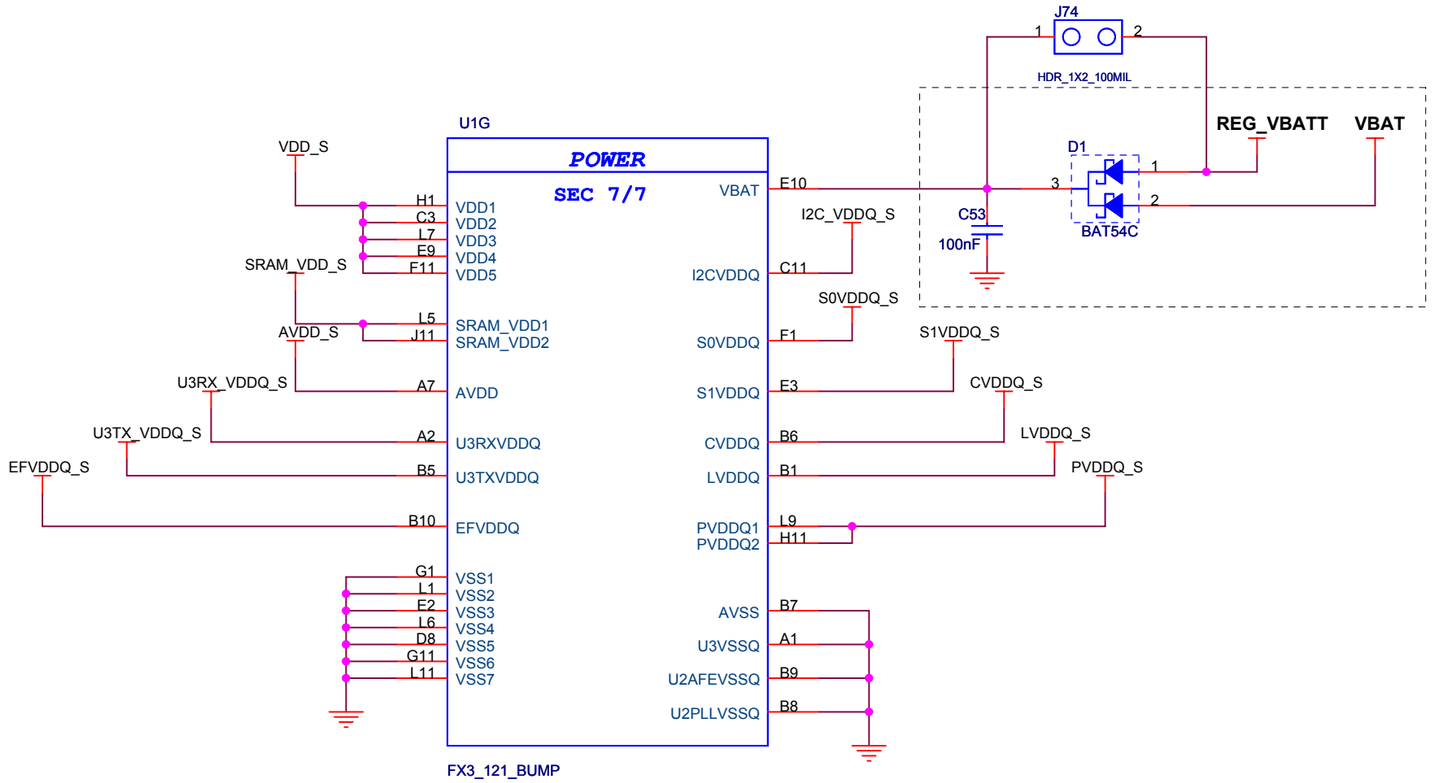
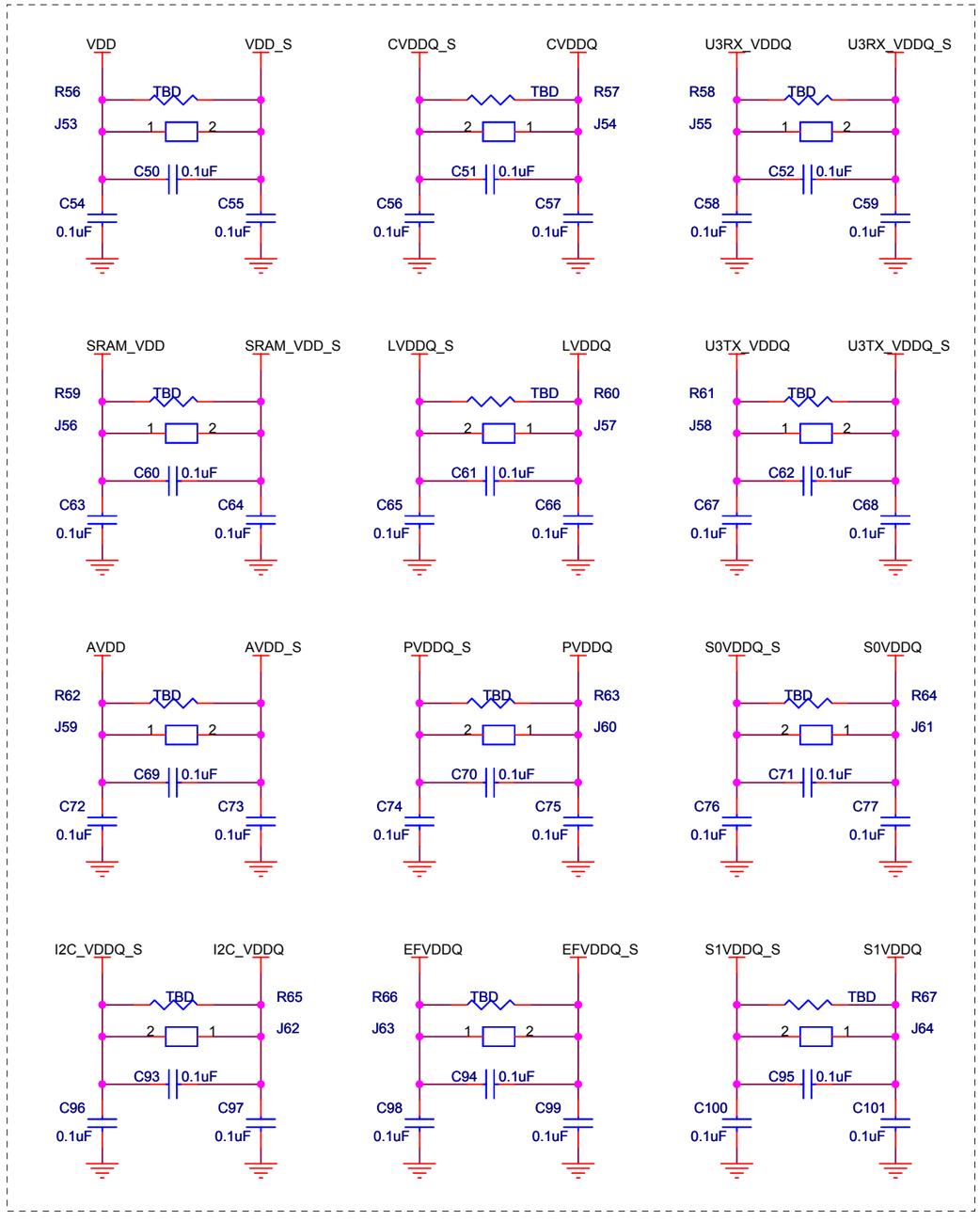
FX3 POWER & DECOUPLING





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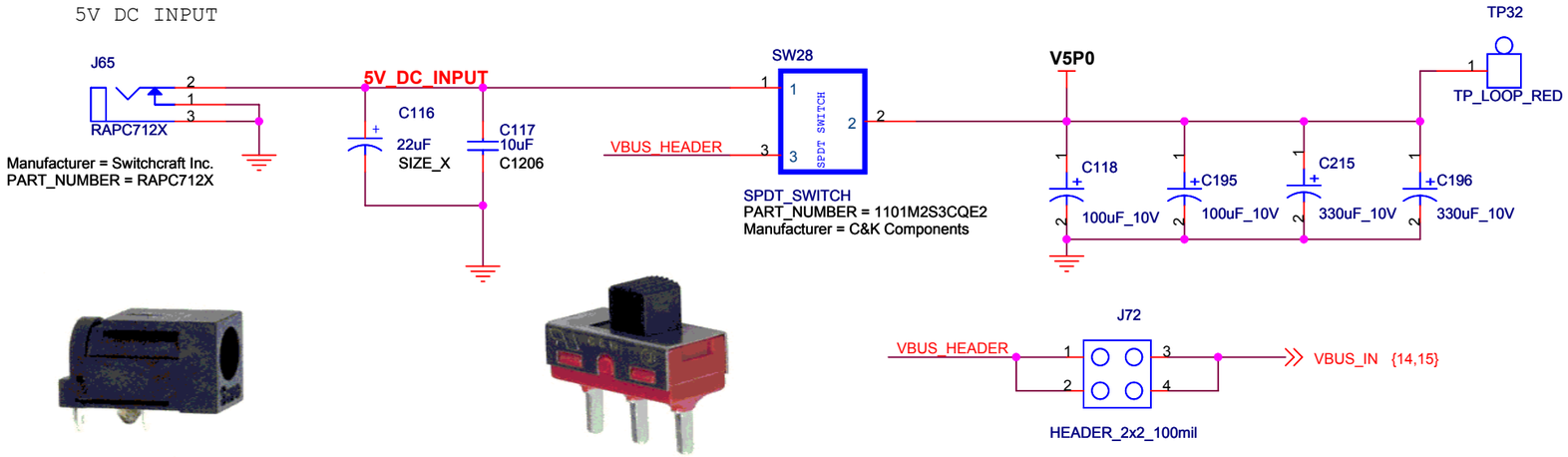
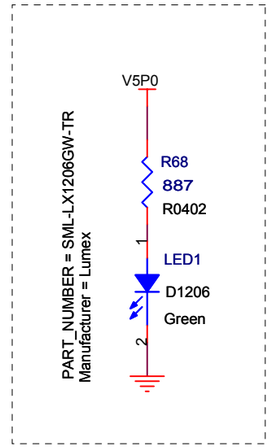
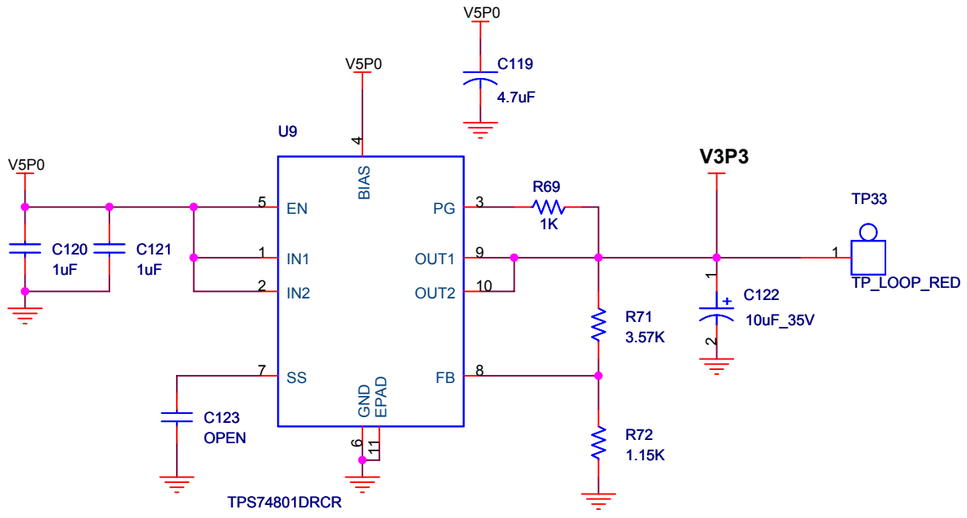
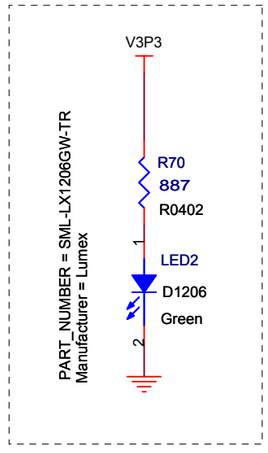
5V - DC INPUT POWER





3.3V @ 1.5A REGULATOR





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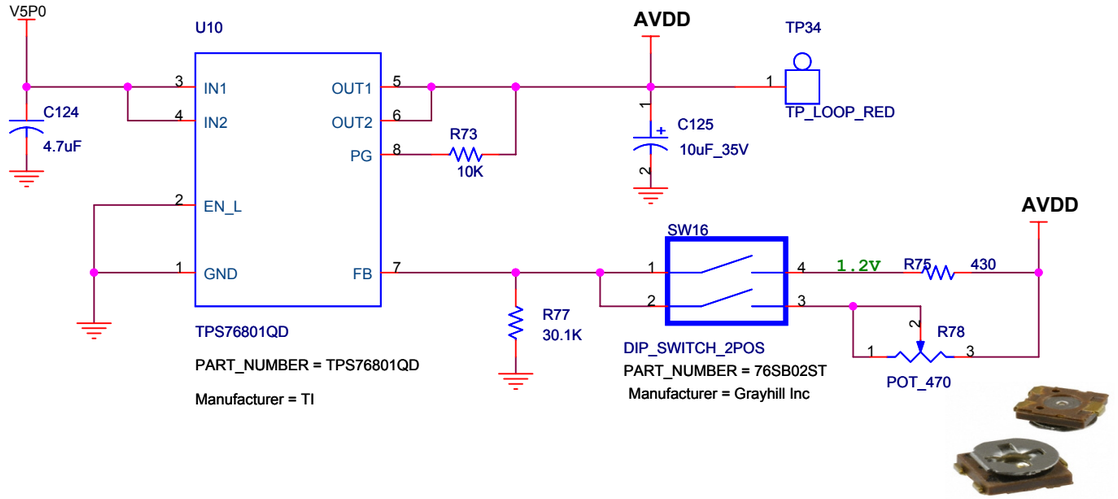
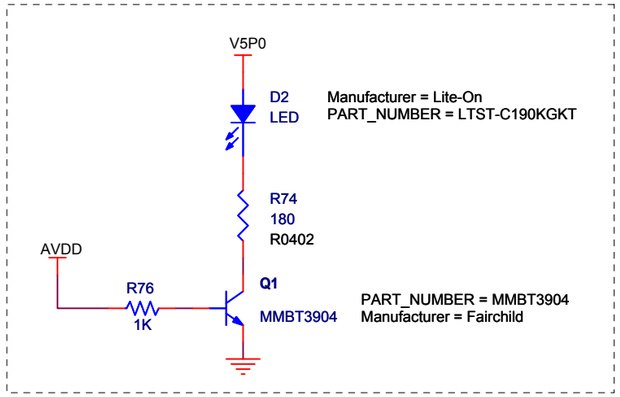
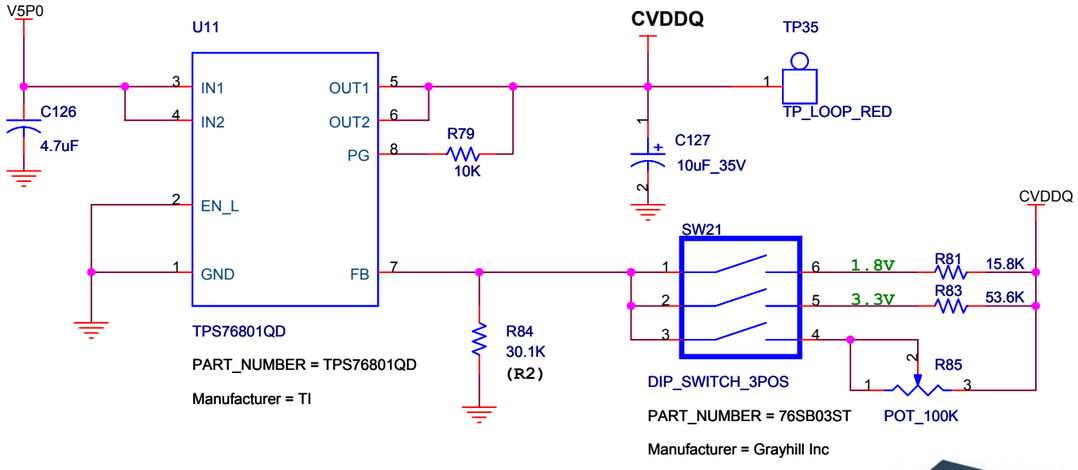
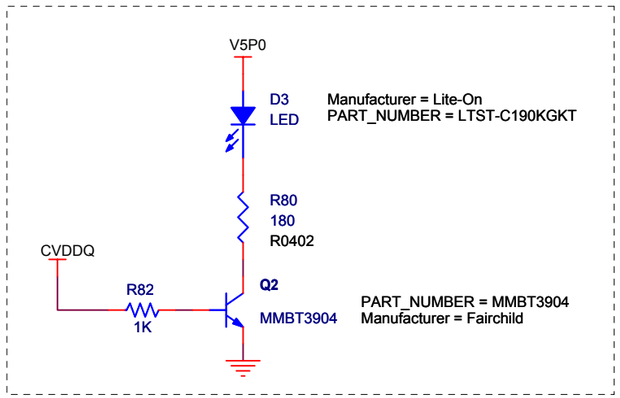
AVDD (1.2V @ 1A) REGULATOR



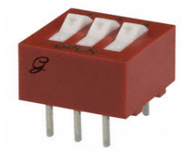


CVDDQ (1.8/3.3V @ 1A) REGULATOR





$$R1 = \left(\frac{V_O}{V_{ref}} - 1\right) \times R2$$



PART_NUMBER = PV36W104C01B00

Manufacturer = Murata

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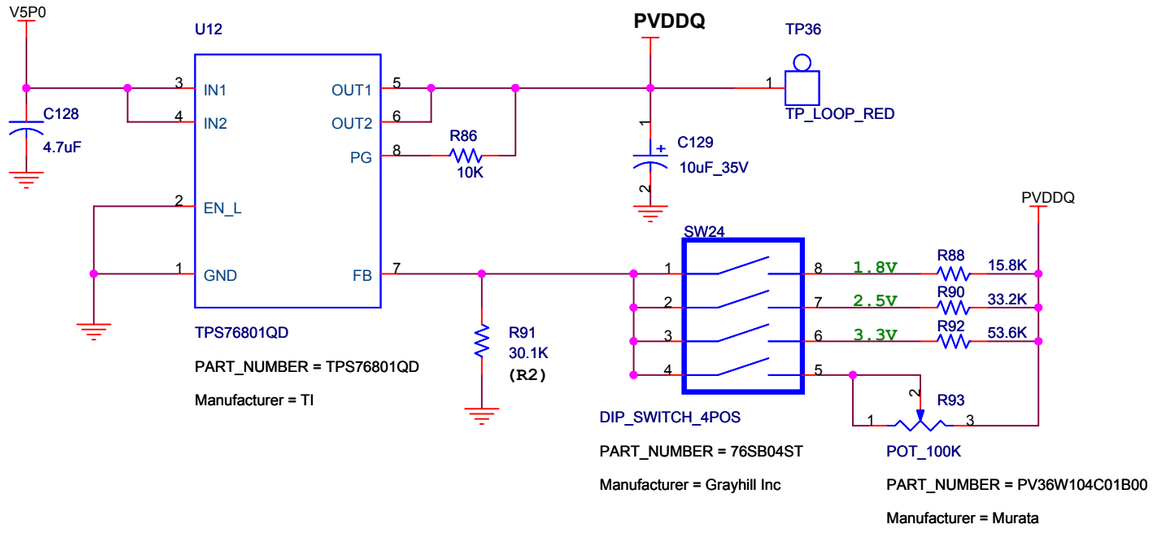
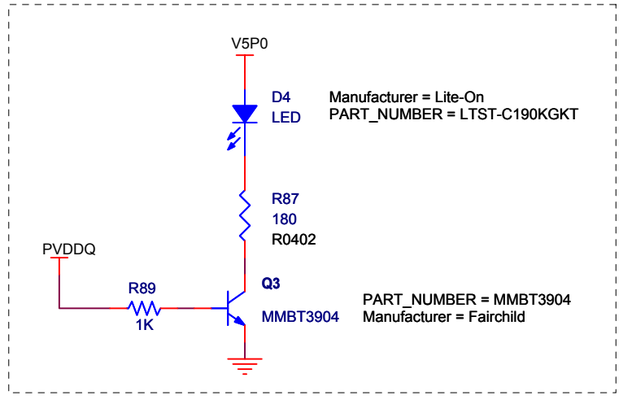
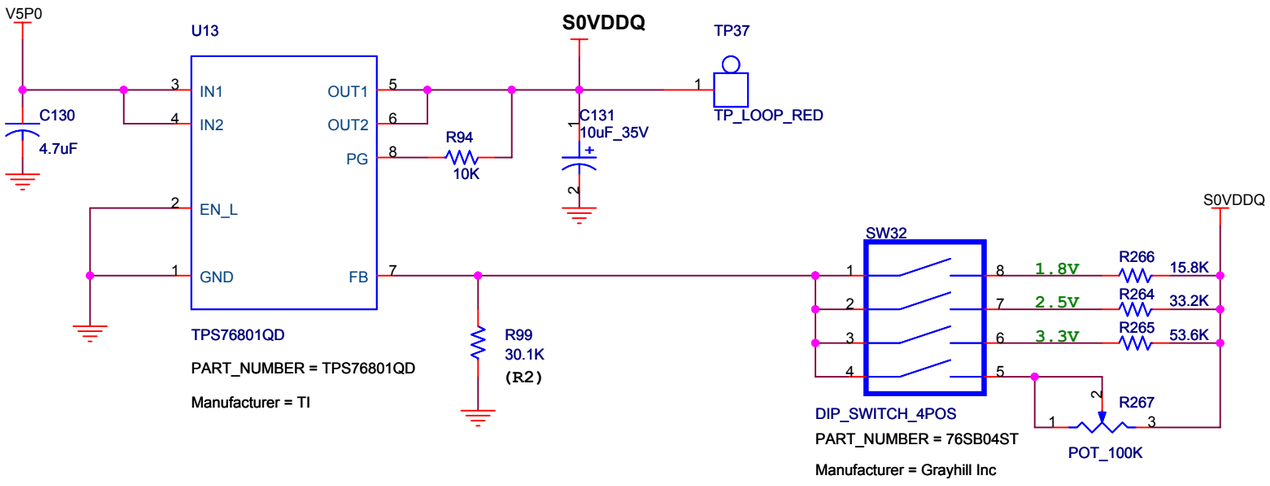
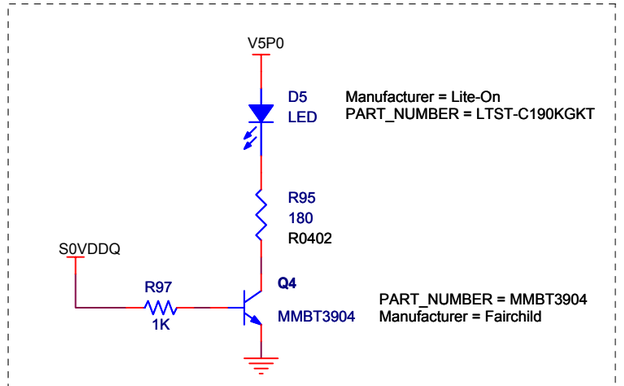
PVDDQ (1.8/2.5V/3.3V @ 1A) REGULATOR





S0VDDQ (1.8/2.5V/3.3V @ 1A) REGULATOR





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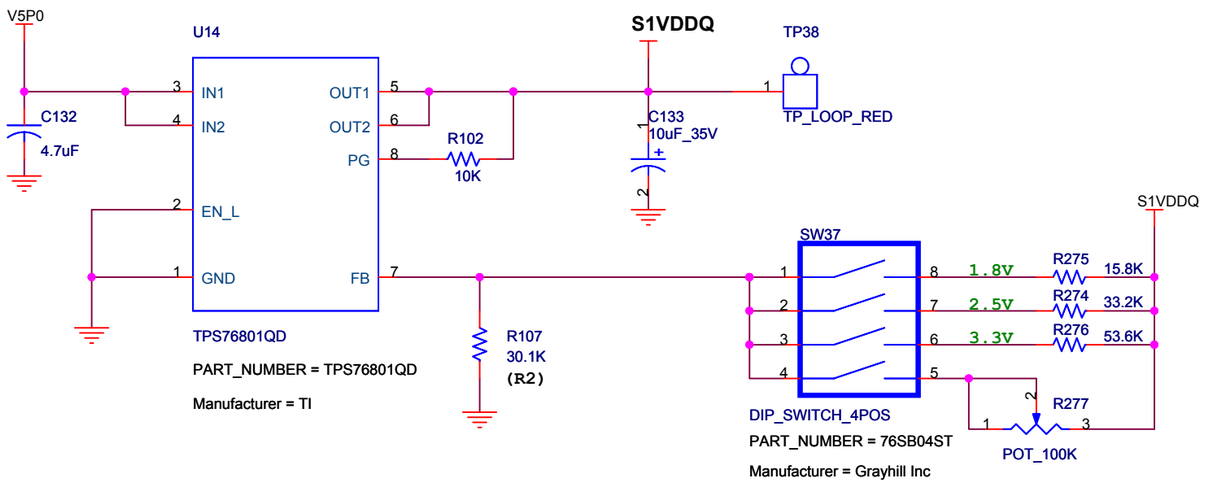
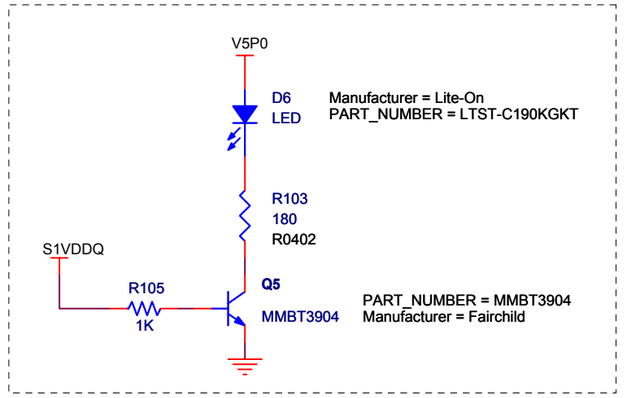
S1VDDQ (1.8/2.5V/3.3V @ 1A) REGULATOR





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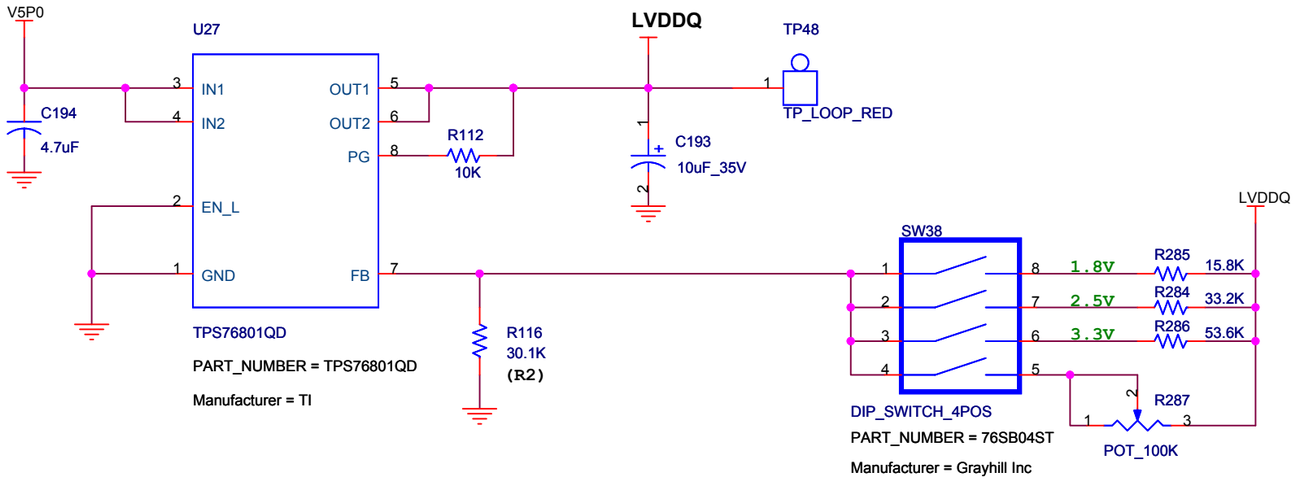
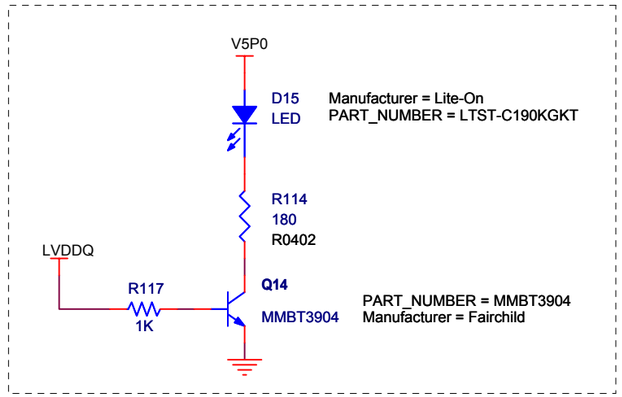
LVDDQ (1.8/2.5V/3.3V @ 1A) REGULATOR





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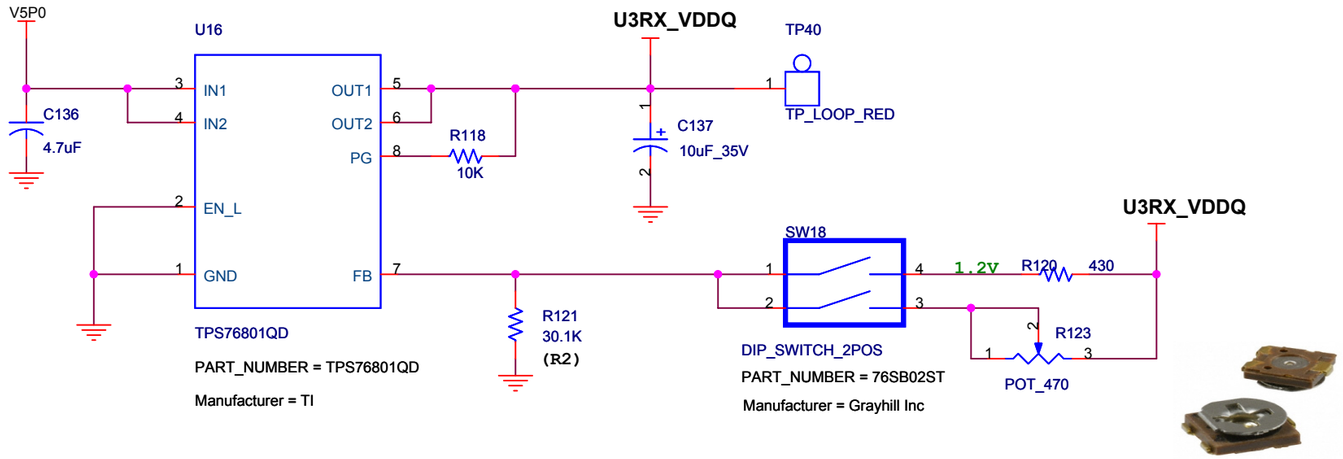
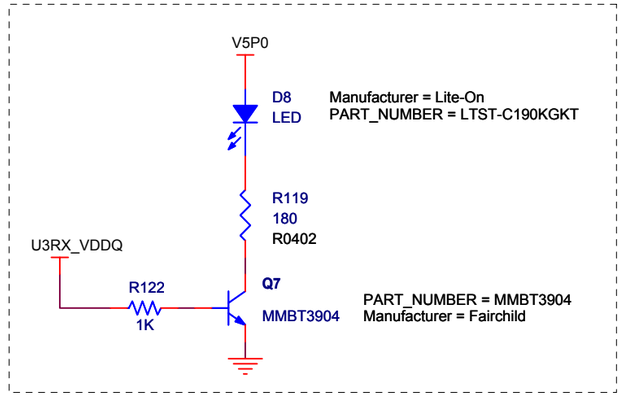
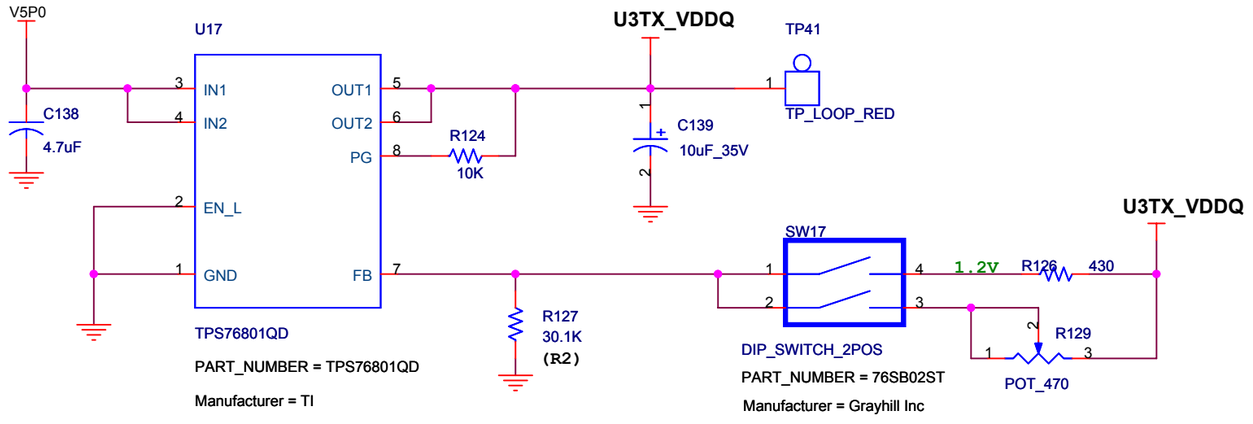
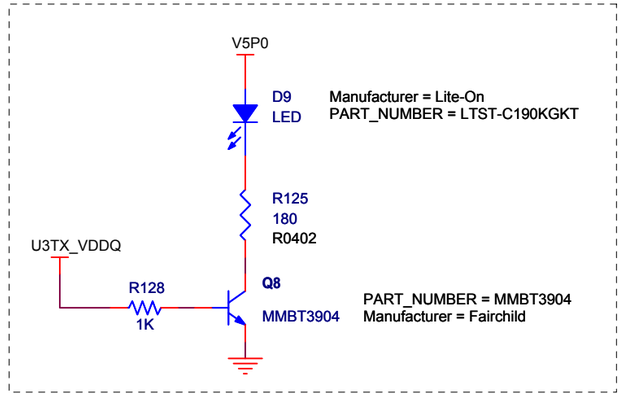
U3RX_VDDQ (1.2V @ 1A) REGULATOR





U3TX_VDDQ (1.2V @ 1A) REGULATOR





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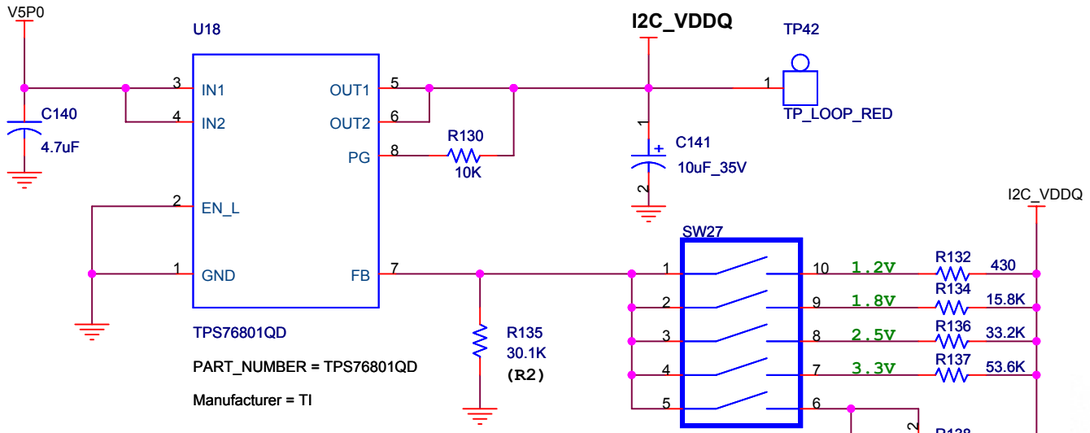
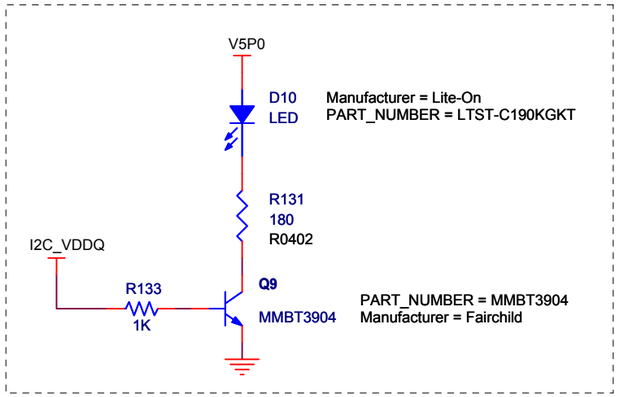
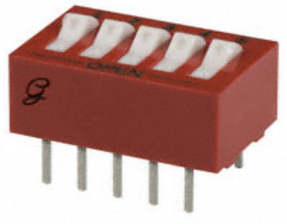
I2C_VDDQ (1.2/1.8/2.5V/3.3V @ 1A) REGULATOR

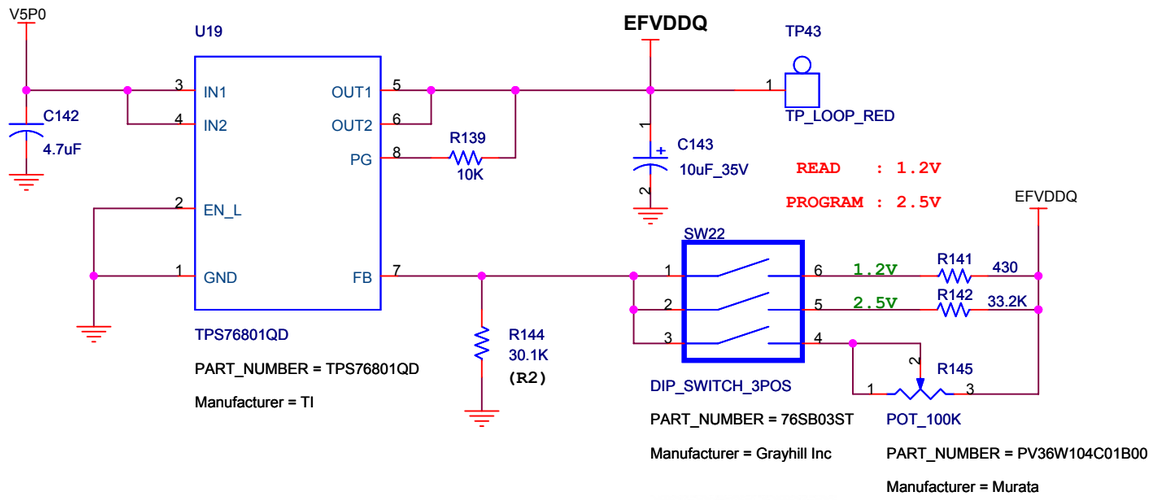


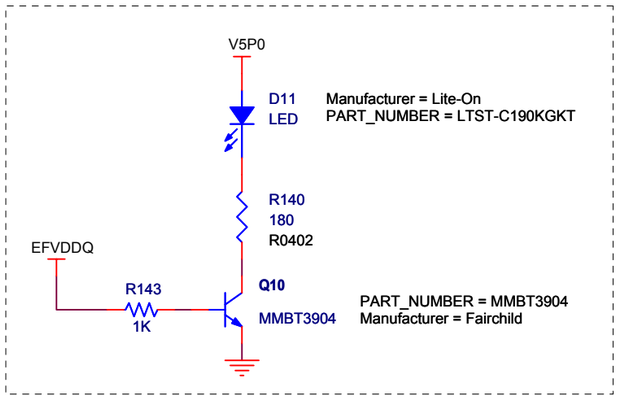


$$R1 = \left(\frac{V_O}{V_{ref}} - 1\right) \times R2$$



EFVDDQ (1.2/2.5V @ 1A) REGULATOR





$$R1 = \left(\frac{V_O}{V_{ref}} - 1\right) \times R2$$

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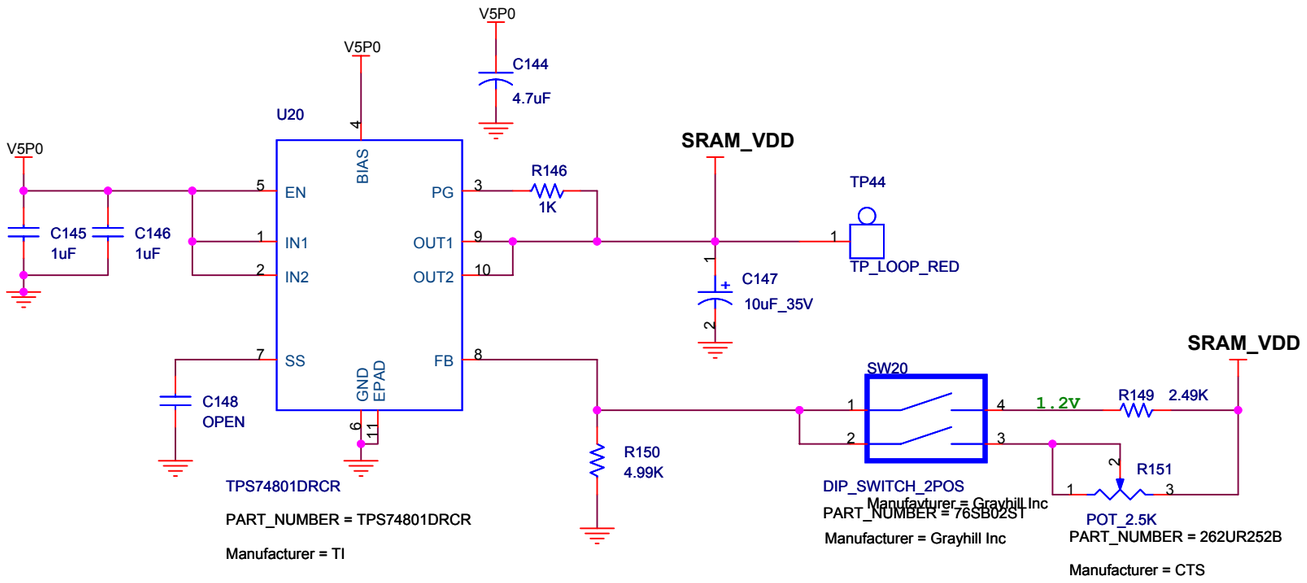
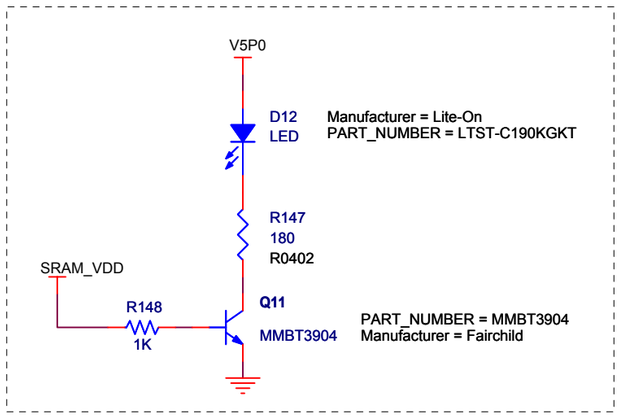
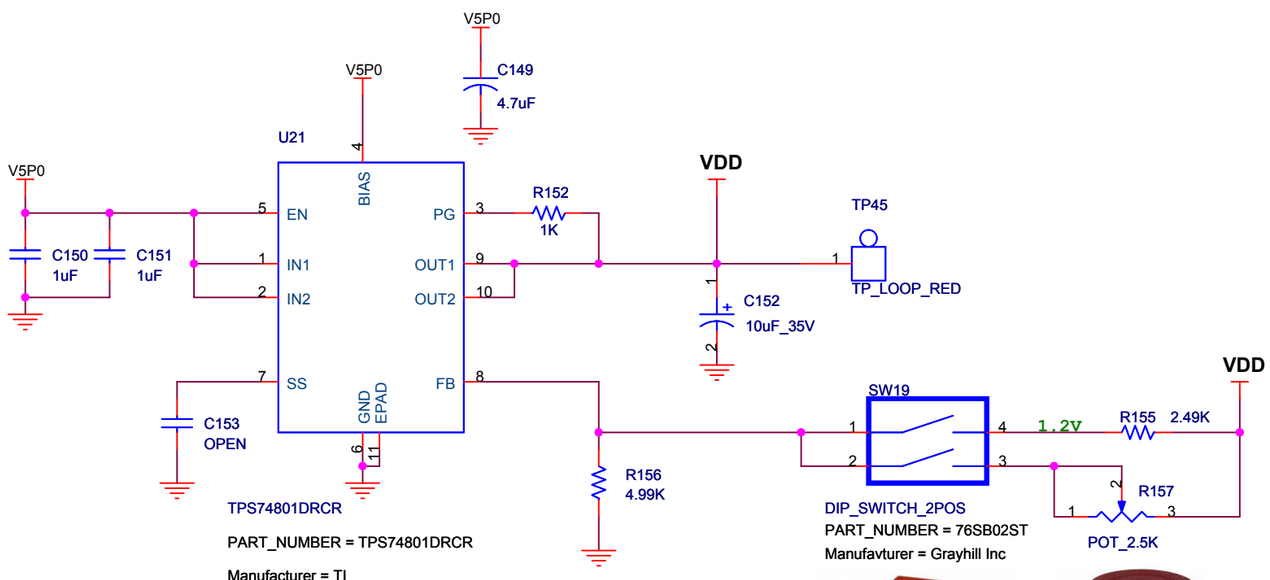
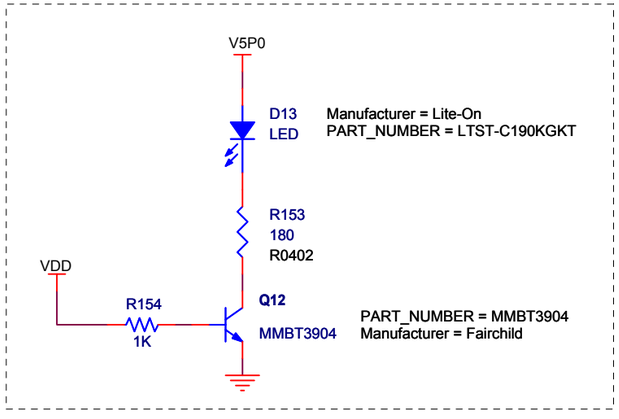
SRAM_VDD (1.2V @ 1.5A) REGULATOR





VDD (1.2V @ 1.5A) REGULATOR





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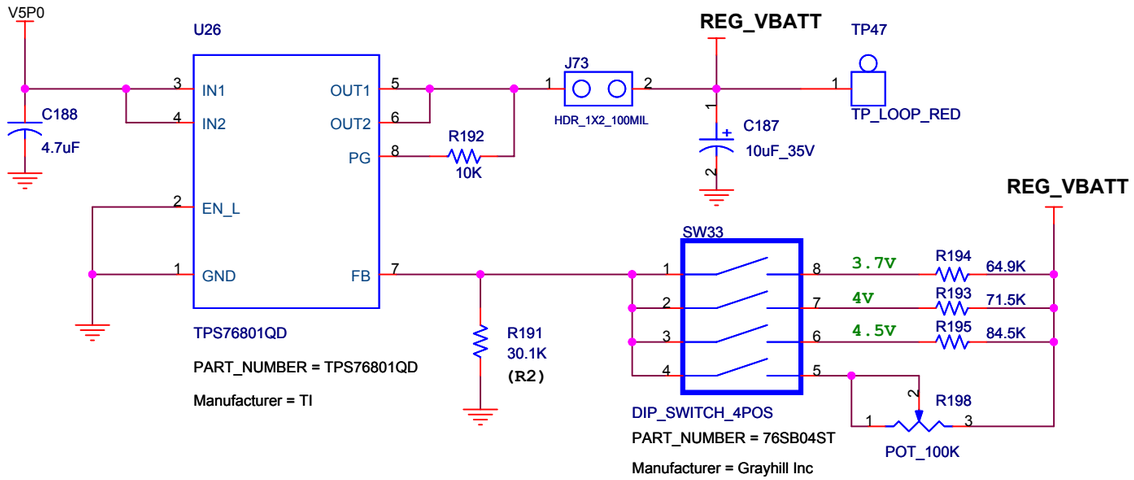
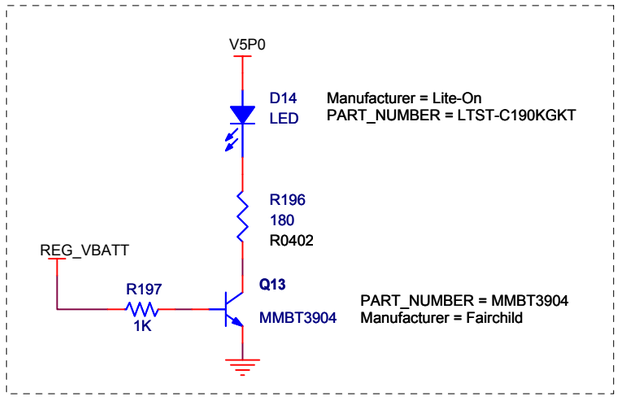
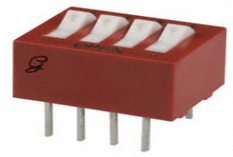
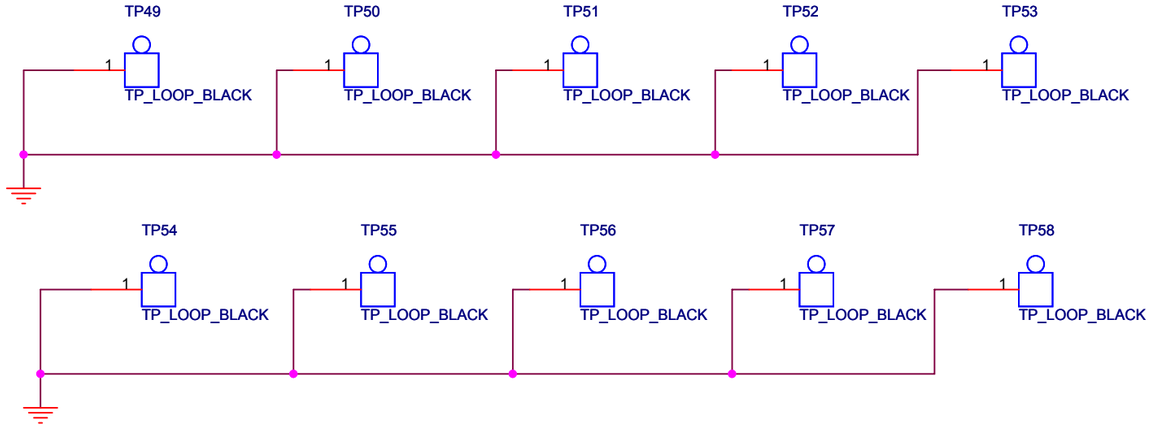
REG_VBATT (3.7 / 4V / 4.5V @ 1A) REGULATOR





$$R1 = \left(\frac{V_O}{V_{ref}} - 1\right) \times R2$$





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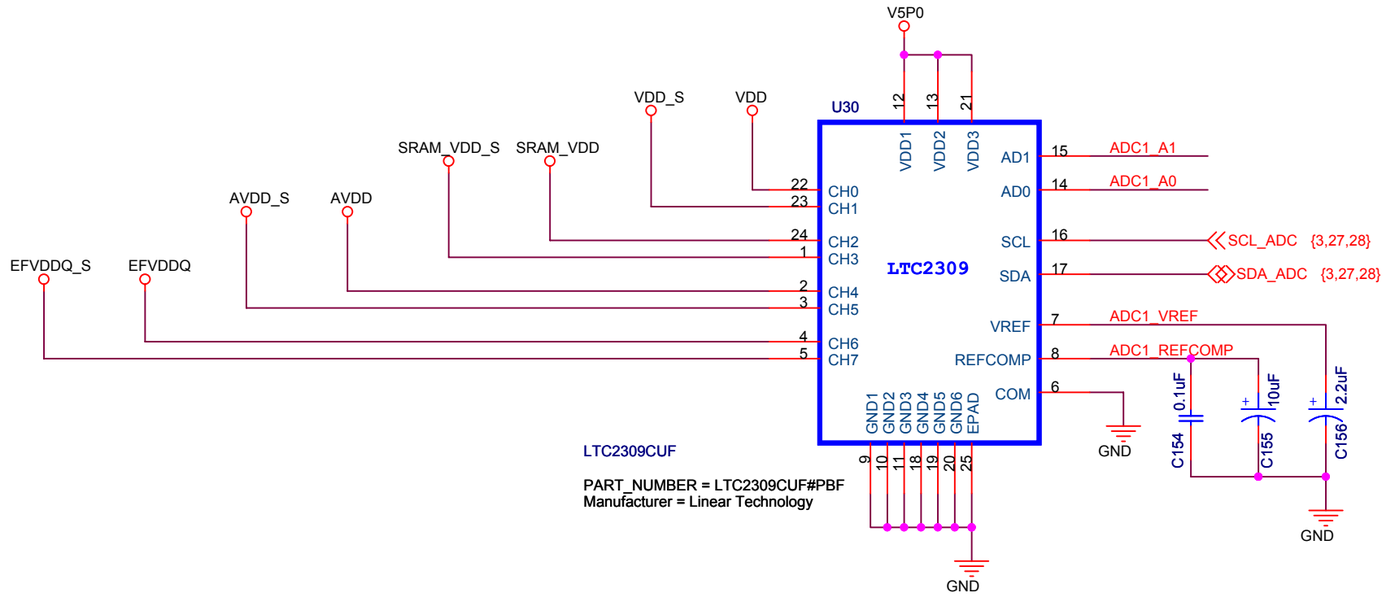
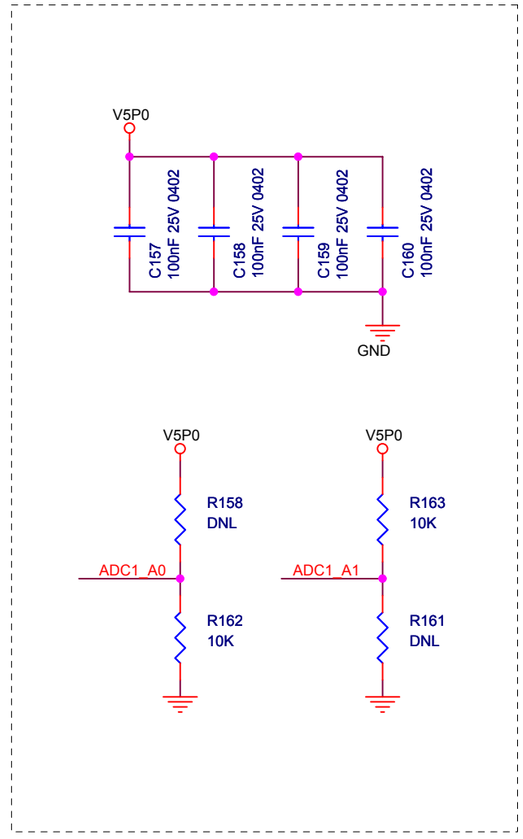
CURRENT SENSING ADC - 1

VDD / SRAM_VDD / AVDD / EFVDDQ





I2C ADDRESS

AD0	AD1	ADDRESS						
0	1	0	0	1	1	0	1	0

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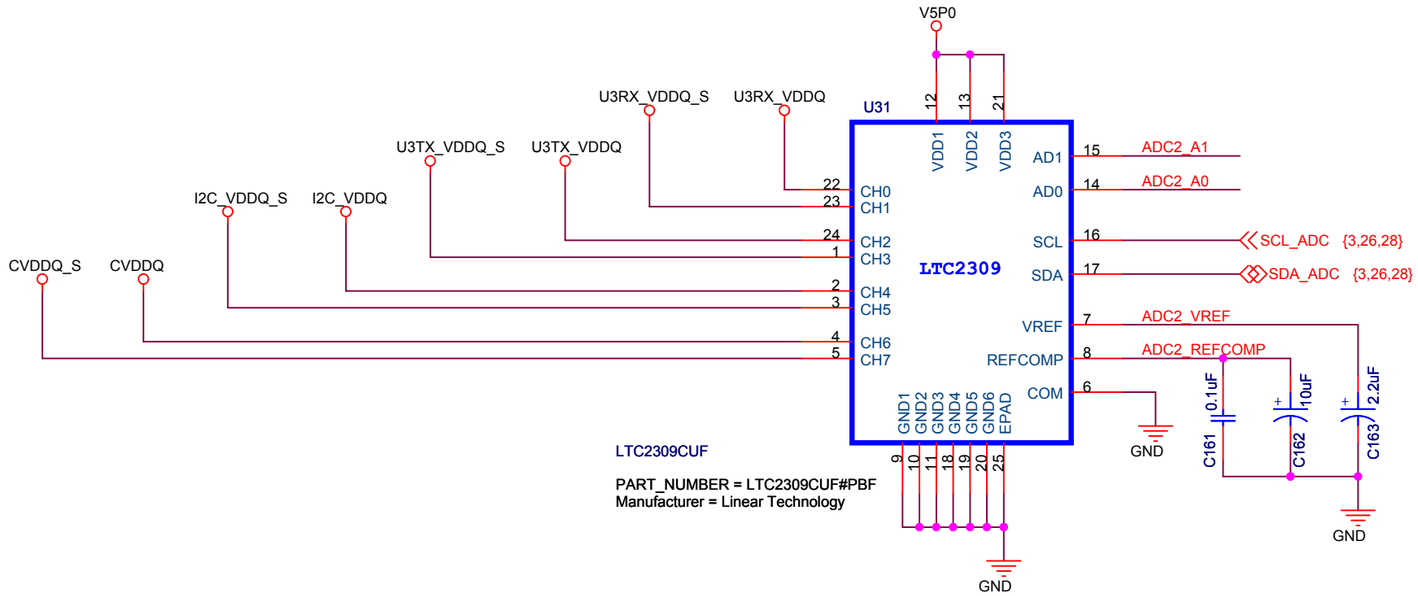
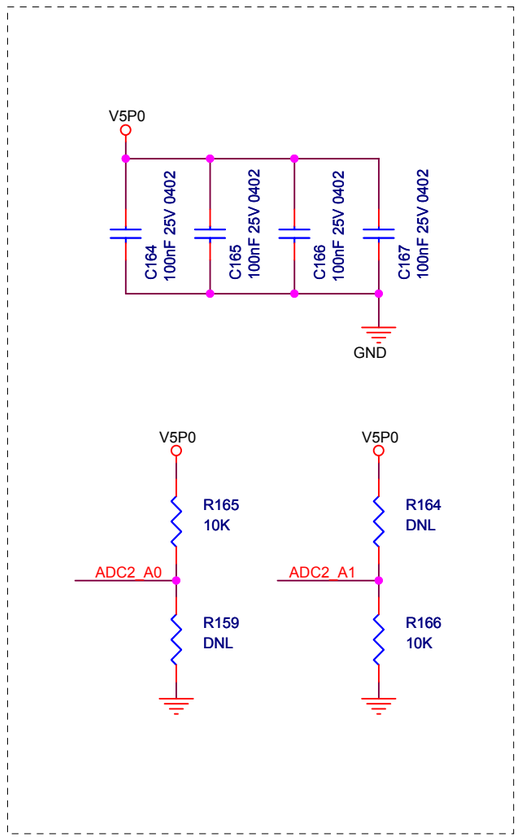
CURRENT SENSING ADC - 2

CVDDQ / I2C_VDDQ / U3RX_VDDQ / U3TX_VDDQ





I2C ADDRESS

AD0	AD1	ADDRESS						
1	0	0	0	0	1	0	1	0

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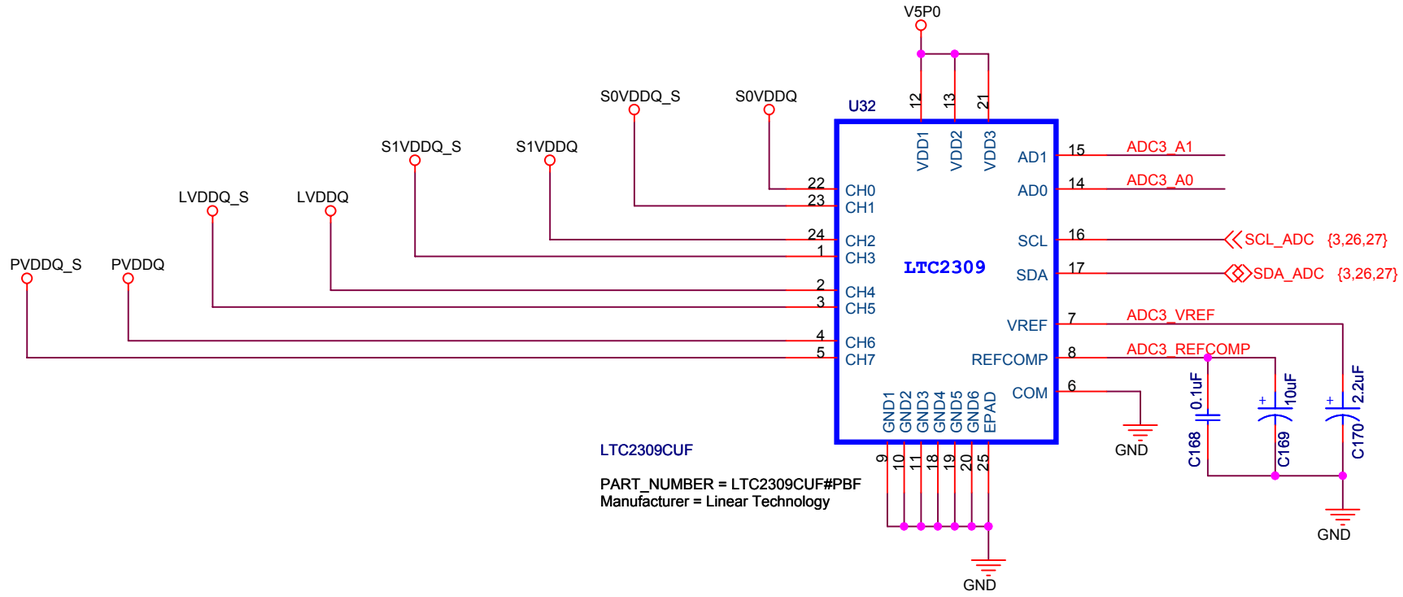
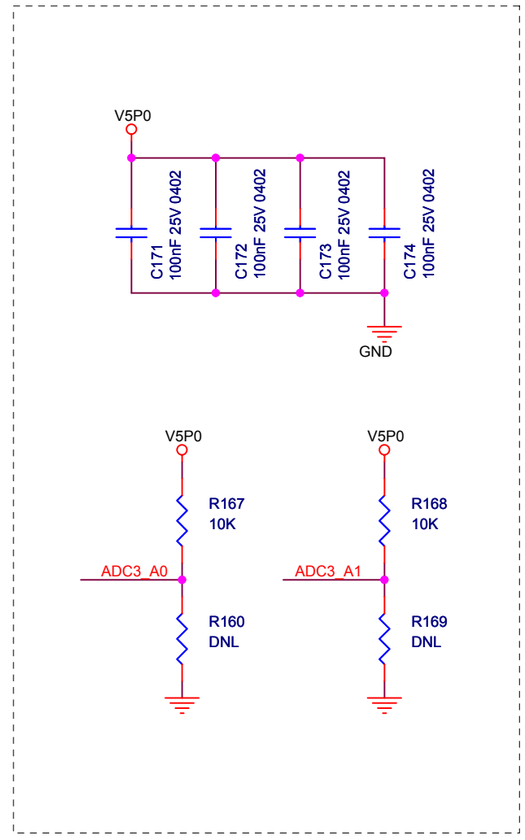
CURRENT SENSING ADC - 3

PVDDQ / LVDDQ / S0VDDQ / S1VDDQ





I2C ADDRESS

AD0	AD1	ADDRESS						
1	1	0	1	0	1	0	0	0

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