

Monday, March 20th 2017

University of Michigan
College of Engineering
1760 Broadway St.
Ann Arbor, MI 48105
Phone: (734)-680-3913
hanfa@umich.edu

Dear Members in the Department of Radiation Oncology,

My name is Fang Han, a Junior undergraduate student majored in Electrical Engineering. I am writing this letter to apply an Electronic Engineering Assistant position. I am interested in this job for hardware implementing using Verilog, which is what I am good at. Also I am excited at programming and playing around with FPGA in the lab.

I have some relevant projects experience. In the current semester, I am taking EECS470 and by working with my groupmates, we are trying to implement and synthesis a 3-way-superscalar R10k microprocessor using Verilog. It makes me more comfortable with Verilog and Linux develop environment. Last year, I programmed a Xilinx FPGA board and has implemented some basic functions on it. Moreover, I have learned Java and can build some simple GUIs, capable but not that skillful.

Excluding my course work and other activities, normally I have approximately 15 to 20 hours available for this project weekly (probably 2-3 hours a day, before April 22nd). The hour's number might be changed to 1 hour per day when meeting exam weeks or project dues. However, I am totally available for my summer vacation, starting from early May to September. For the Fall semester, I am also willing to accept part-time assignments.

Thanks for your time reading and considerations. Contact me if you get any questions about me.

Sincerely,

Fang Han