

BSF

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| BSF | Bit Set f | | | | | | | | |
|-------------------|--|--------------|--------------------|------|------|--------|-------------------|--------------|--------------------|
| Syntax: | [<i>label</i>] BSF f,b[,a] | | | | | | | | |
| Operands: | $0 \leq f \leq 255$ $0 \leq b \leq 7$ $a \in [0,1]$ | | | | | | | | |
| Operation: | $1 \rightarrow f\langle b \rangle$ | | | | | | | | |
| Status Affected: | None | | | | | | | | |
| Encoding: | <table><tr><td>1000</td><td>bbba</td><td>ffff</td><td>ffff</td></tr></table> | 1000 | bbba | ffff | ffff | | | | |
| 1000 | bbba | ffff | ffff | | | | | | |
| Description: | Bit 'b' in register 'f' is set. If 'a' is 0 Access Bank will be selected, overriding the BSR value. If 'a' = 1, then the bank will be selected as per the BSR value. | | | | | | | | |
| Words: | 1 | | | | | | | | |
| Cycles: | 1 | | | | | | | | |
| Q Cycle Activity: | | | | | | | | | |
| | <table><tr><td>Q1</td><td>Q2</td><td>Q3</td><td>Q4</td></tr><tr><td>Decode</td><td>Read register 'f'</td><td>Process Data</td><td>Write register 'f'</td></tr></table> | Q1 | Q2 | Q3 | Q4 | Decode | Read register 'f' | Process Data | Write register 'f' |
| Q1 | Q2 | Q3 | Q4 | | | | | | |
| Decode | Read register 'f' | Process Data | Write register 'f' | | | | | | |

Example: BSF FLAG_REG, 7, 1

Before Instruction
 FLAG_REG = 0x0A
After Instruction
 FLAG_REG = 0x8A

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