

CLRWDT

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CLRWDT

Clear Watchdog Timer

Syntax: `[label] CLRWDT`

Operands: None

Operation: 000h → WDT,
000h → WDT postscaler,
1 → \overline{TO} ,
1 → \overline{PD}

Status Affected: \overline{TO} , \overline{PD}

Encoding:

| | | | |
|------|------|------|------|
| 0000 | 0000 | 0000 | 0100 |
|------|------|------|------|

Description: CLRWDT instruction resets the Watchdog Timer. It also resets the postscaler of the WDT. Status bits \overline{TO} and \overline{PD} are set.

Words: 1

Cycles: 1

Q Cycle Activity:

| Q1 | Q2 | Q3 | Q4 |
|--------|--------------|--------------|--------------|
| Decode | No operation | Process Data | No operation |

Example: CLRWDT

Before Instruction

WDT Counter = ?

After Instruction

WDT Counter = 0x00

\overline{WDT} Postscaler = 0

\overline{TO} = 1

\overline{PD} = 1

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