Laboratory Portfolio Template ITEC10261

Student ID

Student Name

Lab	Task	Completed	Filename
2.3	Master Slave Flip Flop	☐	
2.5	Master Slave Flip Flop using Nand and inverter	☐	
2.6	Master Slave Flip Flop with defect fixed	☐	
3.2	Bus circuit with labels	☐	
3.5	Bus circuit with memory	☐	
3.5	Memory contents (cdm file)	☐	
4.1.1	Top Memory contents for Harvard Circuit	☐	
4.1.2	Bottom Memory contents for Harvard Circuit	☐	
4.2	Memory Contents for Von-Neumann Circuit	☐	
4.3	Circuit with connected program counter	☐	
4.3	Memory Contents for the above	☐	
5.2	Hex Calculator circuit	☐	
5.2	Hex Calculator results (text file)	☐	

The tasks below are from Lab 6/7 "Things to do" sections

Lab	Task	Completed	Filename
6.2-6	Fetch and Instruction control details (text file)	☐	
6.7	Program behaviour – memory contents (cdm file)	☐	
6.8	Details for BNZ instruction (same file as 6.2-6)	☐	
6.10	Circuit for BNZ instruction	☐	
6.10	Memory contents for BNZ (cdm file)	☐	
7.2	Processor Circuit	☐	
7.2-4	Instruction Set (text file)	☐	

Notes – section 6 in the lab 7 "things to do" section refers to the two files specified in 7.2 and 7.2-4 above – it is not a separate requirement. The extension work detailed in Lab 7 is included with all other extension work at the end of this document.

8	68000 instruction notes (text file)	☐
9.2	Annotated 68000 program	☐

Extension Tasks

7	Circuit (cdl file)	☐
7	Memory dump containing test program(cdm file)	☐
7.7	Options Discussion Document – (Text file)	☐
9	Translated programs – put these in a folder	☐

Note the 68000 programs for Extension work – Lab 9 should be grouped together in a subfolder within your zipfile. Put the name of this folder in the box above.