## Model 1 Computer Architecture

Here is the 8-bit machine described in Appendix C of Brookshear and Brylow (2015):

Central Processing Unit (CPU)		Main Memory (RAM	1)
Registers  0 Program Counter (PC)  1 IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII	Bus	Address Cell  00  01  02   FF	
Questions (12 min)		Start times	:

1. What are the three main components in the diagram? Based on its name, what do you think each hardware component does?

The CPU is "the brain" of the computer that performs calculations and makes decisions. The RAM is where "working memory" is stored. The Bus connects the CPU and RAM together.

2. How many registers does the CPU have? How many memory cells are in RAM?

16 registers (0–F) and 256 memory cells (00–FF).

**3**. The CPU has circuits for adding the contents of two registers; think of registers as the "pins" in last week's lab. What is the largest number this machine can add without overflowing?

Because the machine has eight bits, the largest number is 0xFF or 255.

**4**. Common tasks the CPU performs include *loading* data from a memory cell into a register and *storing* data from a register into a memory cell. Describe what the CPU would need to do in order to add twenty numbers stored in memory.

The CPU would need to use two registers: one for a running total, and one to load data from memory. Start by setting the total register to zero. For each number, load it into the other register, and then add it to the total.