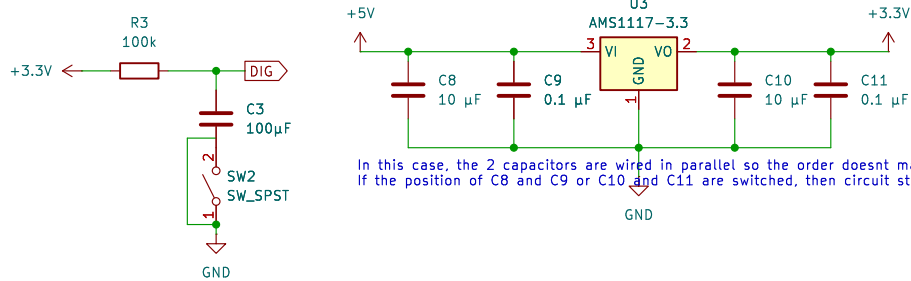


In this case, what matters is the combination of the capacitors, which is $10\text{ }\mu\text{F}$ and $0.1\text{ }\mu\text{F}$ for wide-band filter

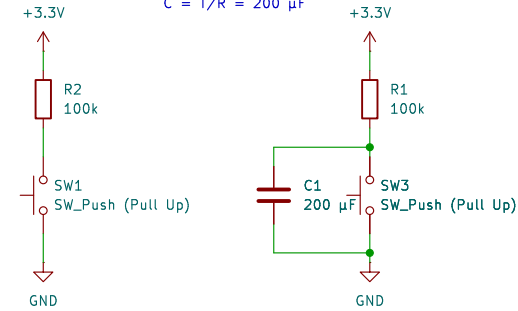
Given 5V, get 3V3 via a buck converter (AMS1117-3.3) on this board



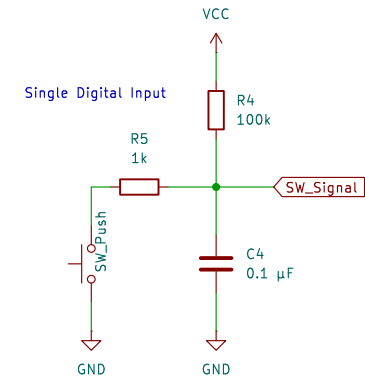
In this case, the 2 capacitors are wired in parallel so the order doesn't matter. If the position of C8 and C9 or C10 and C11 are switched, then the circuit stays the same, doesn't matter

$T = RC$, where T is debounce (ms), R resistance, and C capacity (farad)
 $C = T/R = 10/10^5 = 100\text{ }\mu\text{F}$

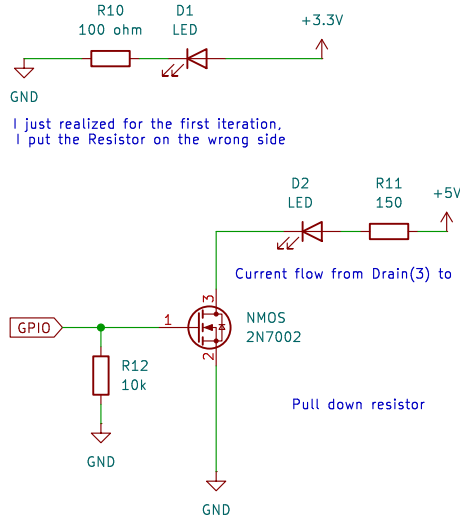
$T = RC$, where T is debounce (ms), R resistance, and C capacity (farad)
 $C = T/R = 200\text{ }\mu\text{F}$



no schmitt trigger, basically a cheat la, schmitt trigger inhe

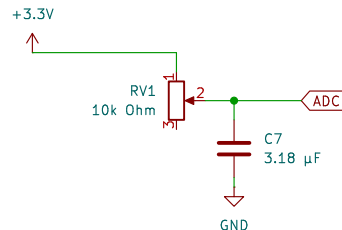


$T = RC$, where T is debounce (ms), R resistance, and C capacity (farad)
 $C = T/R = 0.01/10^5 = 0.1\text{ }\mu\text{F}$

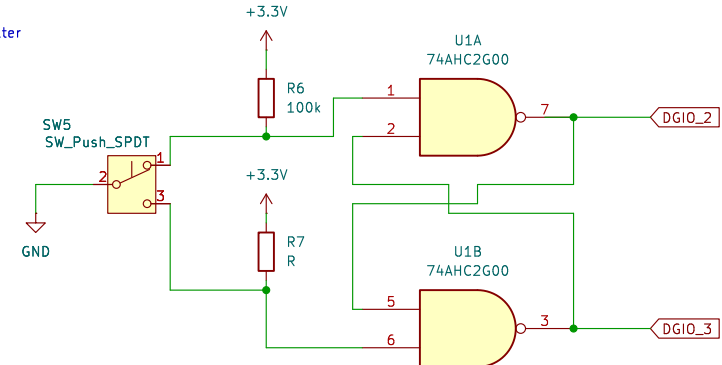


Current flow from Drain(3) to Source(2) NMOS

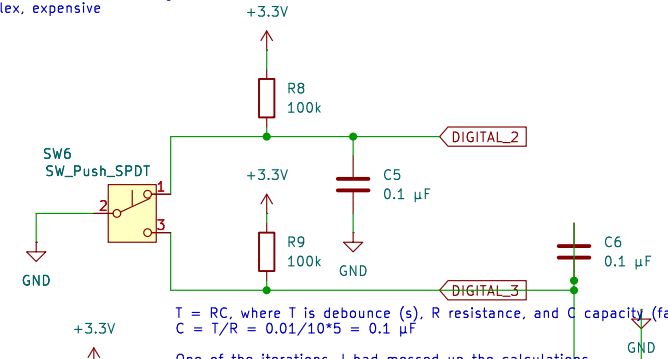
Pull down resistor



This is basic Potentiometer circuit

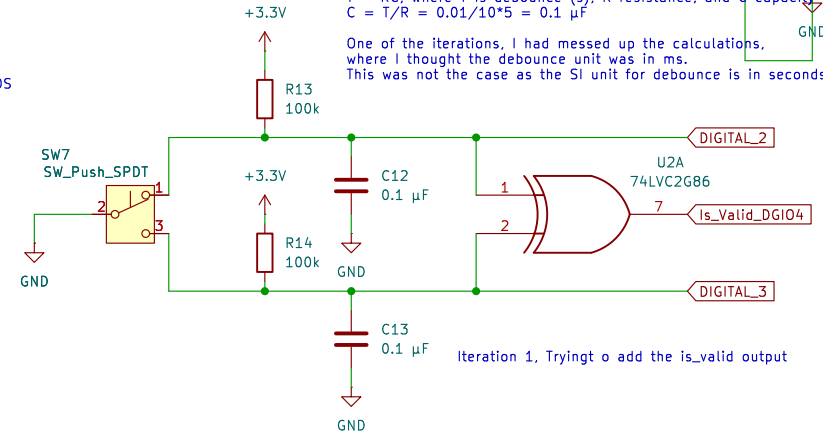


First iteration of the SPDT w/ Debounce using NAND. Complex, expensive



$T = RC$, where T is debounce (s), R resistance, and C capacity (farad)
 $C = T/R = 0.01/10^5 = 0.1\text{ }\mu\text{F}$

One of the iterations, I had messed up the calculations, where I thought the debounce unit was in ms. This was not the case as the SI unit for debounce is in seconds, so 10ms is 0.01 seconds



Iteration 1, Trying to add the is_valid output

Sheet: /
 File: ROV_RC_Draft1_UNSORTED.kicad_sch

Title:

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