引脚序号 (LQFP48封装)	引脚名称	类 型	1/0	主功能 (复位后默认)	复用功 默认复用	能 重映射	ST-LinkV2	开发板	扩展接口
1	V _{BAT}	S	-	V _{BAT}	-	-			
2	PC13-TAMPER- RTC(5)	I/O	-	PC13 ⁽⁶⁾	TAMPER-RTC	-			GPIO1
3 4	PC14-OSC32_IN ⁽⁵⁾ PC15- OSC32_OUT ⁽⁵⁾	I/O I/O	-	PC14 ⁽⁶⁾ PC15 ⁽⁶⁾	OSC32_IN OSC32_OUT	-			<u> </u>
5	OSC IN	I/O	-	OSC IN	-	PD0(7)			
6	OSC OUT	0	-	OSC OUT		PD0(7) PD1(7)			
7	NRST	I/O	-	NRST	-	-			
8	V _{SSA}	S	-	V _{SSA}	-	-			
9	V _{DDA}	S	-	V _{DDA}	-	-		—	
10	PA0-WKUP	I/O	-	PA0	WKUP/ USART2_CTS ⁽⁹⁾ / ADC12_IN0/ TIM2_CH1_ _{ETR} (9)	-		KEY	
11	PA1	I/O	-	PA1	USART2_RTS [®] / ADC12_IN1/ TIM2_CH2 [®]	-		LED	
12	PA2	I/O	-	PA2	USART2_TX ⁽⁹⁾ / ADC12_IN2/ TIM2_CH3 ⁽⁹⁾	-			USART2_TX
13	PA3	I/O	-	PA3	USART2_RX ⁽⁹⁾ / ADC12_IN3/ TIM2_CH4 ⁽⁹⁾	-			USART2_RX
14	PA4	I/O	-	PA4	SPI1_NSS ⁽⁹⁾ / USART2_CK ⁽⁹⁾ / ADC12_IN4	-			GPIO2
15	PA5	I/O	-	PA5	SPI1_SCK [®] / ADC12 IN5	-	T_JTCK		
16	PA6	I/O	-	PA6	SPI1_MISO(9)/ ADC12_IN6/ TIM3_CH1(9)	TIM1_BKIN			GPIO3
17	PA7	I/O	-	PA7	SPI1_MOSI [®] / ADC12_IN7/ TIM3_CH2 [®]	TIM1_CH1N			GPIO4
18	PB0	I/O	-	PB0	ADC12_IN8/ TIM3_CH3 [®]	TIM1_CH2N	NRST		
10	DD1	1/0	_	PB1	ADC12_IN9/	TIM4 CUON		ID.	
19	PB1	I/O			TIM3_CH4 ⁽⁹⁾	TIM1_CH3N		IR	
20	PB2	I/O	FT	PB2/BOOT1	-	-	LOW		
21	PB10	I/O	FT	PB10	I2C2_SCL/ USART3 TX ⁽⁹⁾	TIM2_CH3			I2C2_SCL
22	PB11	I/O	FT	PB11	I2C2_SDA/ USART3_RX ⁽⁹⁾	TIM2_CH4			I2C2_SDA
23	V _{SS_1}	S	-	V _{SS_1}	-	-			
24	V _{DD_1}	S	-	V _{DD_1}	-	-			
25	PB12	I/O	FT	PB12	SPI2_NSS/ I2C2_SMBAI/ USART3_CK ⁽⁹⁾ / TIM1_BKIN ⁽⁹⁾	-	T_SWDIO_IN		
26	PB13	I/O	FT	PB13	SPI2_SCK/ USART3_CTS ⁽⁹⁾ / TIM1_CH1N ⁽⁹⁾	-	T_JTCK		
27	PB14	I/O	FT	PB14	USART3_RTS ⁽⁹⁾ TIM1_CH2N ⁽⁹⁾	-	T_JTMS		
28	PB15	I/O	FT	PB15	SPI2_MOSI/ TIM1_CH3N ⁽⁹⁾	-			GPIO5
29	PA8	I/O	FT	PA8	USART1_CK/	-			GPIO6
	FAO	1/0	r i	FAO	TIM1_CH1 ⁽⁹⁾ / MCO	-			GFIO0
30	PA9	I/O	FT	PA9	USART1_TX ⁽⁹⁾ / TIM1 CH2 ⁽⁹⁾	-	State	USART1_TX	
31	PA10	I/O	FT	PA10	USART1_RX ⁽⁹⁾ /	-		USART1_RX	
32	PA11	I/O	FT	PA11	TIM1 CH3 ⁽⁹⁾ USART1_CTS/ CANRX ⁽⁹⁾ / USBDM/ TIM1_CH4 ⁽⁹⁾	-	USB_DM	-	
33	PA12	I/O	FT	PA12	USART1_RTS/ CANTX [®] /USBDP	-	USB_DP		
34	PA13	I/O	FT	JTMS/SWDIO	-	PA13		SWDIO	
35 36	VSS_2 VDD 2	S S	-	V _{SS_2} V _{DD_2}	-	-			
37	PA14	I/O	- FT	JTCK/SWCLK	-	- PA14		SWCLK	
38	PA15	1/0	FT	JTDI	-	TIM2_CH1_ ETR/ PA15 /SPI1_NSS		SPI1_NSS	
39	PB3	I/O	FT	JTDO	-	TIM2_CH2 / PB3 TRACESWO		SPI1_SCK	
40	PB4	I/O	FT	JNTRST	-	TIM3_CH1/ PB4/		SPI1_MISO	
41	PB5	I/O		PB5	I2C1_SMBAI	TIM3_CH2 / SPI1_MOSI		SPI1_MOSI	
42	PB6	I/O	FT	PB6	12C1_SCL ⁽⁹⁾ / TIM4_CH1 ⁽⁹⁾ 12C1_SDA ⁽⁹⁾ /	USART1_TX		I2C1_SCL	
43	PB7	I/O	FT	PB7	TIM4_CH2 ⁽⁹⁾	USART1_RX		I2C1_SDA	
44	BOOT0	1		BOOT0	-	-	LOW		
45	PB8	I/O	FT	PB8	TIM4_CH3®	I2C1_SCL / CANRX			I2C1
46	PB9	I/O	FT	PB9	TIM4_CH4 ⁽⁹⁾	I2C1_SDA/ CANTX			I2C1
	V _{SS_3}	S	-	V _{SS_3}	-	-			
47 48	V _{DD_3}	S	-	V _{DD_3}	-	-			