

WBS	Name	Start	Finish	Work
1	Implementation in software(Python)	May 1	May 15	15d
2	Design the hardware structure of encoder	May 15	May 17	3d
3	Implementation of encoder (VHDL)	May 18	Jun 2	14d
4	Implementation of hash function(VHDL)	Jun 5	Jun 9	5d
5	Design the hardware structure of decoder	Jun 10	Jun 18	9d
6	Implementation of decoder(VHDL)	Jun 19	Jul 21	25d
7	Implementation of the rest part(VHDL)	Jul 22	Aug 4	12d
8	Writing master paper	Jul 10	Sep 1	40d