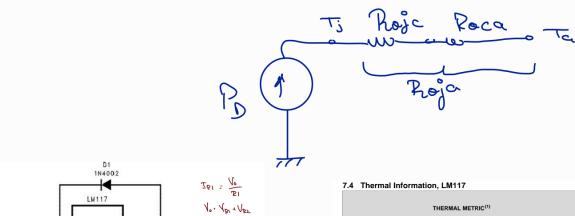
Analisis térmico regulador 3,21:



D1 1N4002	
T ₀ (= √	<u>0</u>
Vo. Vp.	4 VRZ
V_{IN} C C ADJ $D2$ ADJ C	Vez = (Izz
1N4002 + + + + + + + + + + + + + + + + + +	_ (TSS
$I_{ADJ} \approx 50 \mu\text{A}$ R^2 C^2	
$V_{OUT} = 1.25V \left(1 + \frac{R2}{R1}\right) + R2 + I_{ADJ}$	
AN-181 U1 protects against C1 (input shorts) D2 protects against C2 (output shorts)	13

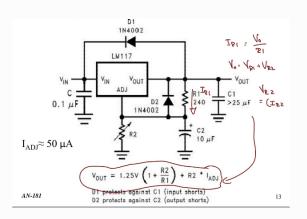
		LM117		UNIT
THERMAL METRIC ⁽¹⁾		NDS (TO-3)	NDT (TO)	
		2 PINS 3 PINS		7 .
$R_{\theta JA}$	Junction-to-ambient thermal resistance (2)	39	186	°C/W
R _{0JC(top)}	Junction-to-case (top) thermal resistance	2	21	°C/W

- (1) For more information about traditional and new thermal metrics, see the Semiconductor and IC package thermal metrics application
- No heatsink.

luego si que será preciso utilizar un disipador tal que:

Roca = Roja - Rojc = 186 - 21 = 165°C/W

Rosa < 136,64°C/W



Cal cula valores teoricos:

$$3.3V = 1.25(1 + \frac{22}{10^3}) + B2 \cdot 50 \cdot 10^{-6}$$

 $R_2 = 1577 J2$