# Mini benchmark – things to consider

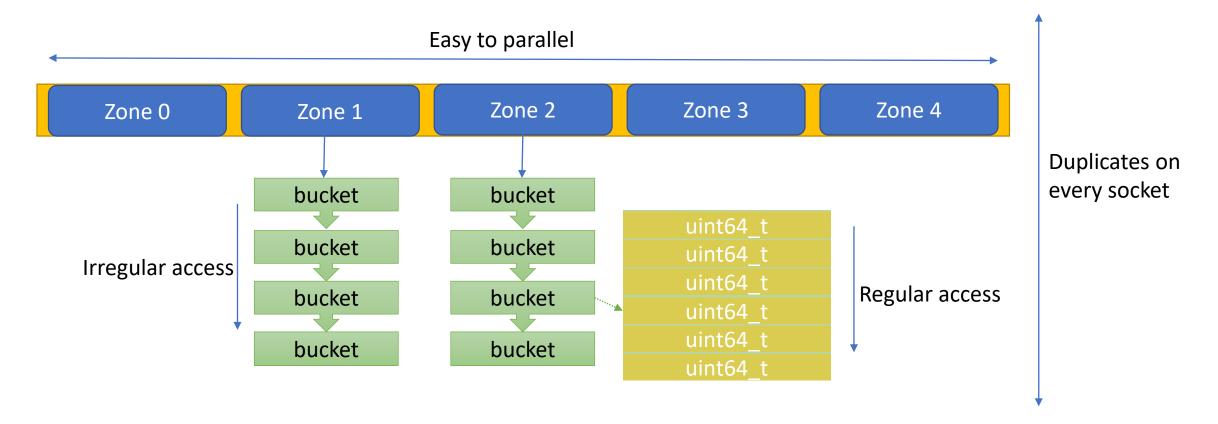
Cache: impact the performance across multiple runs

• CPU pipeline: memory latency might be hidden by the hardware prediction (prefetch).

 Compiler Optimization: some operations might not be actually executed if workload is too trivial

NUMA effect: workload should always local to threads

# Workload design



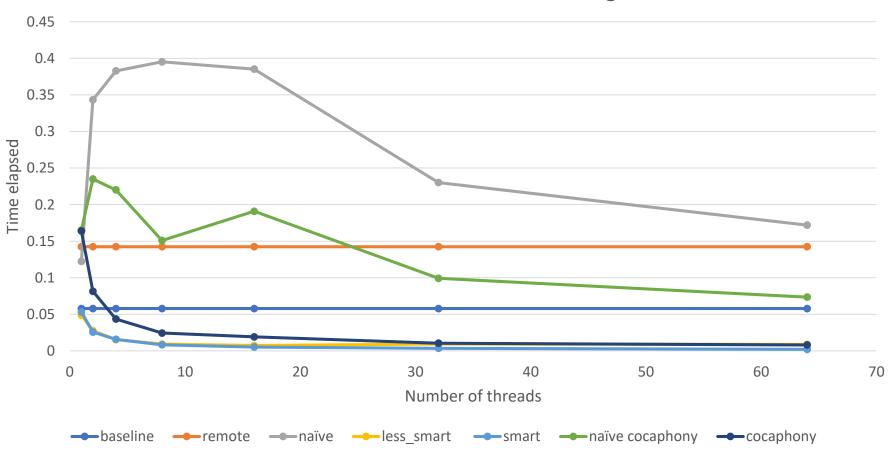
Workload goal: to sum all the uint64\_t

## Implementation Details

- All the experiments are repeated at least 3 times, the reported results are the average of them.
- Cache lines are evicted (using clflush) from all sockets across different runs.
- Thread creation/joining time not included, all threads are guaranteed to start at the same time.
- Each thread will only access its socket-local workload, in other words, no cross-socket memory access (except for the global sum, if applicable)

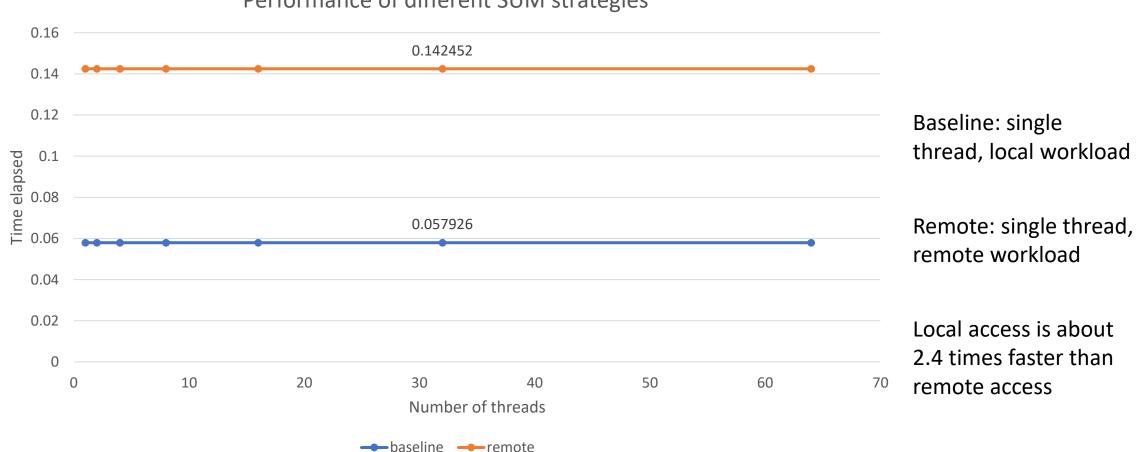
## Results - Overall

#### Performance of different SUM strategies



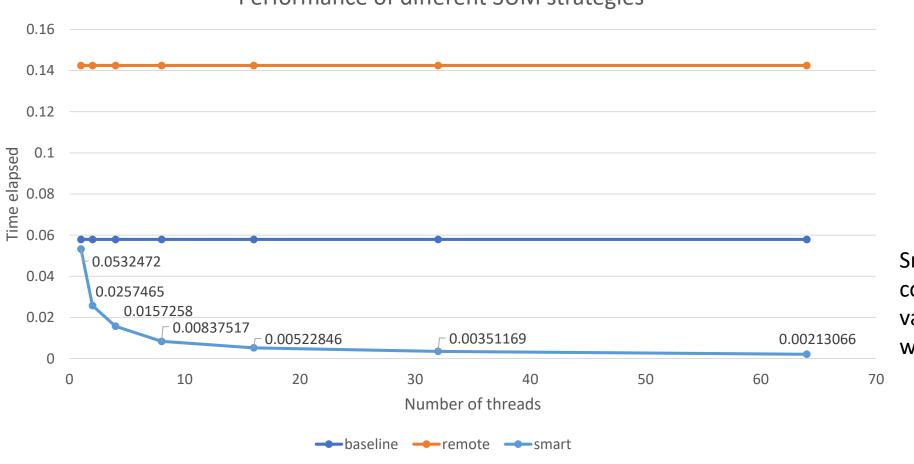
#### Results – Baseline & NUMA effect





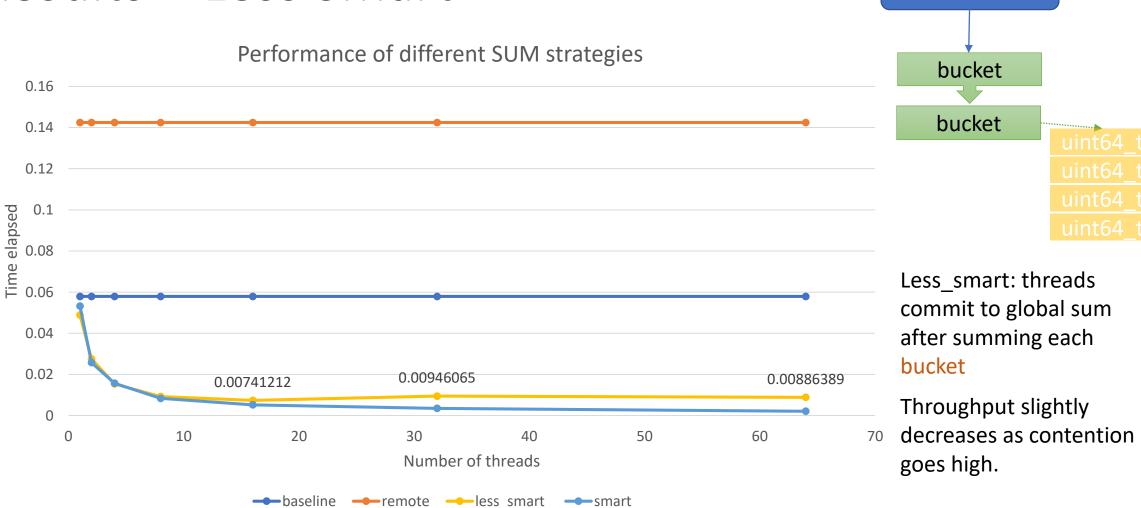
#### Results – Best Possible





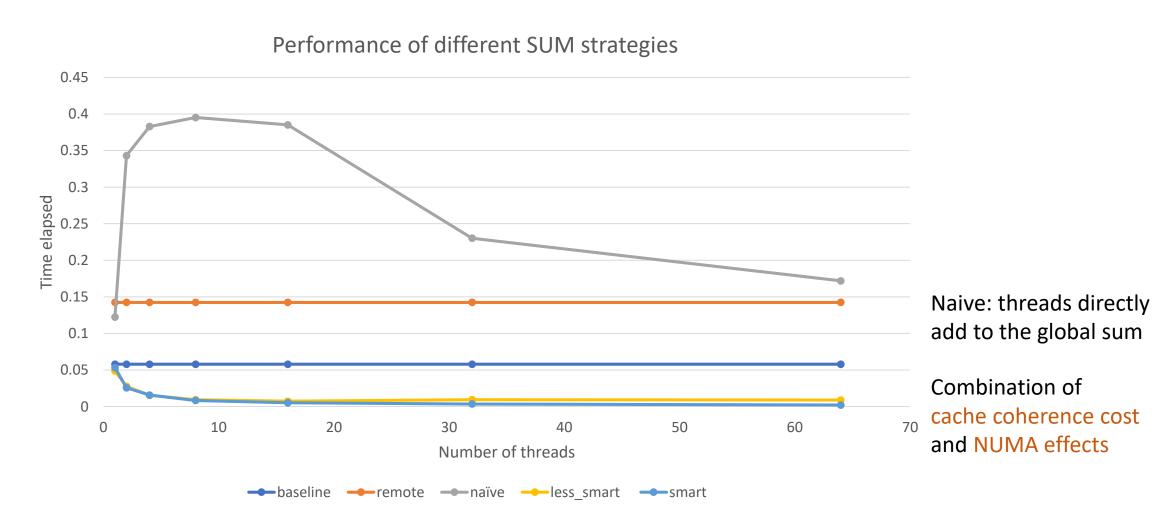
Smart: each thread commits to its local variable, only sync with global sum once

### Results – Less Smart



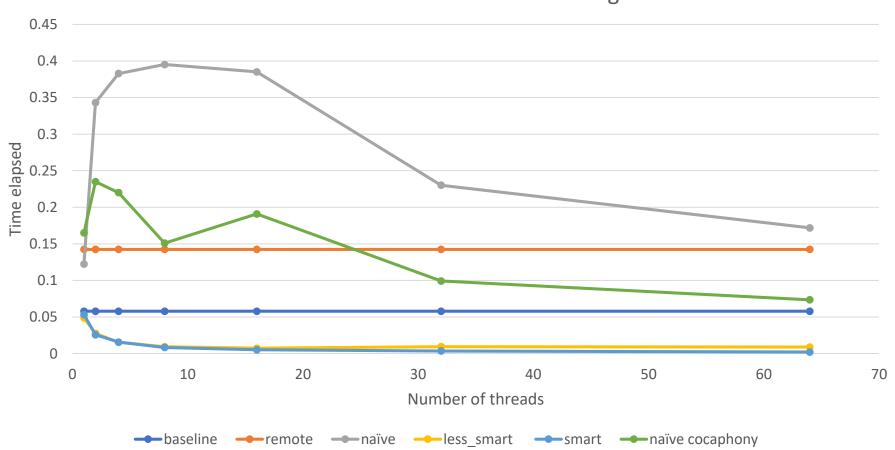
Zone 2

### Results – Naïve



## Results – Naïve cacophony



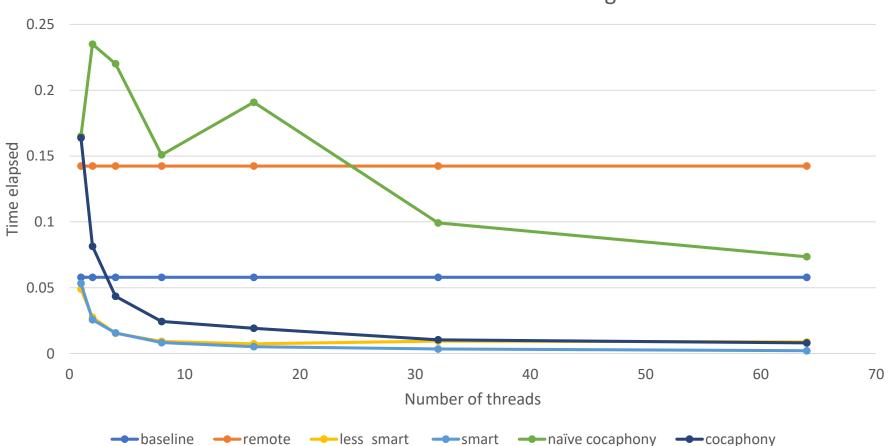


Naïve cacophony: threads adds to its core-local copy, and sync with global sum once.

False sharing without NUMA effects.

## Results – Smart cacophony

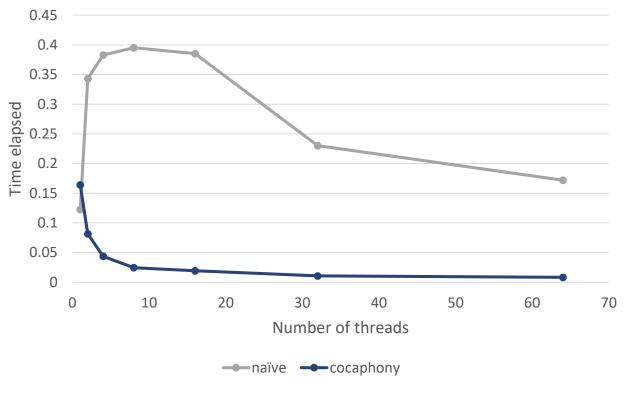




Smart cacophony: threads adds to its core-local copy, but each copy is padded to cache line size to prevent false sharing

# Results – Programmability





#### Cacophony

#### Naïve