

# **An Improved MRC-Rake Symbol Detector for OTFS Modulation Using Expectation Cancellation**

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# Outline



- **Motivations**
- EC-MRC-Rake detector
- Simulations

# Orthogonal Time Frequency Space (OTFS)



- Properties [1]
  - Symbols are modulated in the delay-Doppler (DD) domain
  - Exploits the time-frequency diversity to improve the error rate
  - With lower peak-to-average power ratio (PAPR) than OFDM
- Probability graph based symbol detection in the DD domain [2], [3], [4]
  - To approximately solve the Maximum A Posteriori (MAP) detection problem using **factor graph**
  - **Damping factor** is introduced to mitigate the error rate degradation caused by loopy graph
  - **DD sparsity** assumption is usually introduced to reduce the implementation complexity

[1] R. Hadani, S. Radkib, M. Tsatsanis, A. Monk, A. J. Goldsmith, A. F. Molisch and R. Calderbank, "Orthogonal time frequency space modulation," *IEEE WCNC, San Francisco, CA, USA*, Mar. 2017

[2] P. Raviteja, K. T. Phan, Y. Hong, and E. Viterbo, "Interference cancellation and iterative detection for orthogonal time frequency space modulation," *IEEE Trans. Wireless Commun.*, vol. 17, no. 10, pp. 6501-6515, Oct. 2018.

[3] W. Yuan, Z. Wei, J. Yuan, and D. W. K. Ng, "A simple variational Bayes detector for orthogonal time frequency space (OTFS) modulation," *IEEE Trans. Veh. Technol.*, vol. 69, no. 7, pp. 7976-7980, Apr. 2020.

[4] Y. Ge, Q. Deng, P. C. Ching, and Z. Ding, "OTFS signaling for uplink NOMA of heterogeneous mobility users," *IEEE Trans. Commun.*, vol. 69, no. 5, pp. 3147-3161, May. 2021.

# Orthogonal Time Frequency Space (OTFS)



- Probability graph based symbol detection in the DD domain
    - To approximately solve the Maximum A Posteriori (MAP) detection problem using **factor graph**
      - When the **girth** of the factor graph becomes small, the detectors are more sensitive to the error propagation and easy to fall into **weak local optimums**
    - **Damping factor** is introduced to mitigate the error rate degradation caused by loopy graph
      - Smaller damping factor brings a smaller information update step, so as to prevent falling into a weak local optimum, but **more iterations** are need
- $$x^{(t)} \leftarrow (1 - \alpha)x^{(t-1)} + \alpha x^{(t)}$$
- **DD sparsity assumption** is usually introduced to reduce the implementation complexity
    - A Doppler component may spread over **multiple resolvable Doppler bins**
    - Not satisfied in the **Doppler-abundant channels**

# Proposed EC-MRC-Rake detector



- Expectation Cancellation (EC) based MRC-Rake detector

- Based on the MRC-Rake<sup>[1]</sup> detector implemented in the **delay-time (DT) domain**

- The complexity no longer depends on the number of Doppler taps
- DT channels can be well estimated using non-parametric algorithms <sup>[2]</sup>

- Using **soft interference cancellation**, inspired by the factor graph based detectors

- The **error propagations** can be mitigated, thereby the error rate and convergence can be improved

$$\hat{\mathbf{y}}_i = \mathbf{y} - \sum_{j \neq i} \mathbf{H}_j \hat{\mathbf{x}}_j \xrightarrow{\text{Replace With}} \hat{\mathbf{y}}_i = \mathbf{y} - \sum_{j \neq i} \mathbf{H}_j \boldsymbol{\mu}_j$$

- Soft information  $\boldsymbol{\mu}$  is defined using **symbol expectation**

- Symbol expectation is mapped from the bit expectations, i.e., the bit LLRs
- The estimation approach of equalized gain and variance is proposed to calculate the LLRs
- Easy to be combined with the turbo equalization structure <sup>[3]</sup>

$$LLR(b_{i,0}), LLR(b_{i,1}), \dots, LLR(b_{i,Q}) \xrightarrow{\text{Mapper}} \boldsymbol{\mu}_i$$

[1] T. Thaj, and E. Viterbo, "Low complexity iterative rake decision feedback equalizer for zero-padded OTFS systems," *IEEE Trans. Veh. Technol.*, vol. 69, no. 12, pp. 15606-15622, Dec. 2020

[2] H. Zhang, J. Li, T. Zhang, and X. Zhu, "An efficient channel estimation scheme for short frame OTFS using impulse-train pilots," *IEEE GLOBECOM*, Rio de Janeiro, Brazil, Dec. 2022

[3] X. Wang, and H. V. Poor, "Iterative (turbo) soft interference cancellation and decoding for coded CDMA," *IEEE Trans. Commun.*, vol. 47, no. 7, pp. 1046-1061, July. 1999.

# Outline

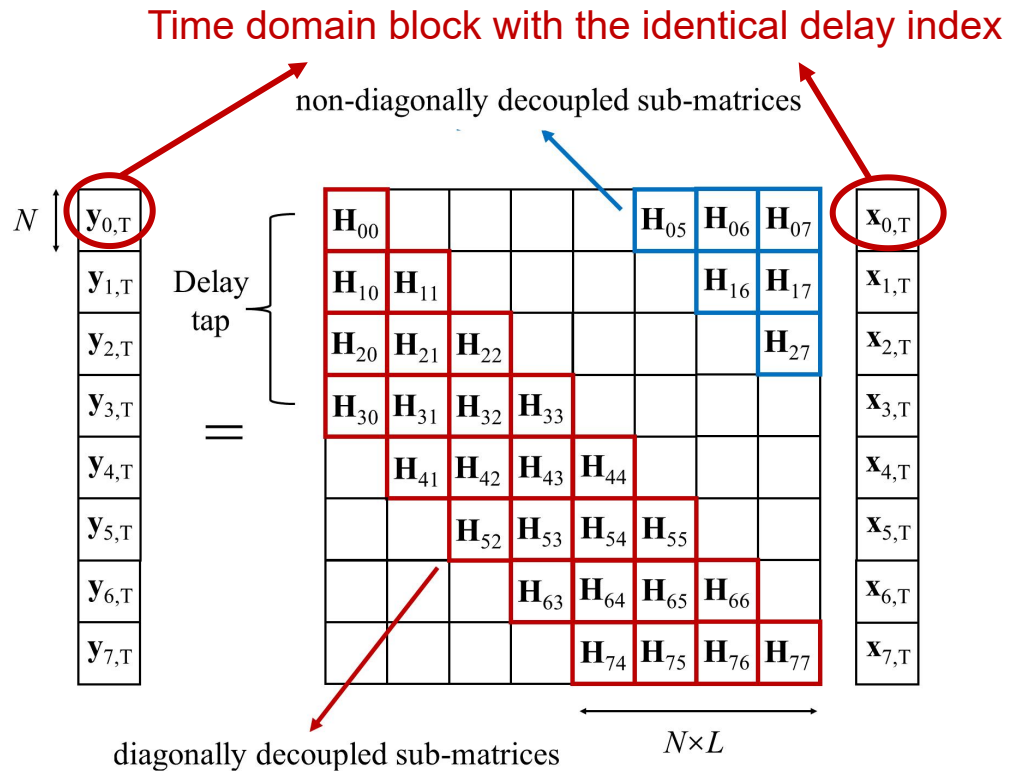


- Motivations
- **EC-MRC-Raker detector**
- Simulations

# Proposed EC-MRC-Rake detector



- Input-output relationship in the DT domain <sup>[1]</sup>



- Block-wise input-output (received block with delay index  $\ell$ ):

$$\mathbf{y}_{\ell,T} = \sum_{\ell' \in \mathcal{L}} \mathbf{H}_{\ell\langle\ell-\ell'\rangle_M} \mathbf{x}_{\langle\ell-\ell'\rangle_{M,T}} + \mathbf{w}_{\ell,T}$$

- Delay domain **block-wise summation**
- Delay domain **block-wise interference cancellation and MRC**

- Inner-block input-output (reduced-CP OTFS):

$$\mathbf{H}_{\ell\langle\ell-\ell'\rangle_M} \mathbf{x}_{\langle\ell-\ell'\rangle_{M,T}} = \begin{cases} \text{diag}\{\mathbf{\Pi}_N^{-1} \mathbf{H}_{\ell\langle\ell-\ell'\rangle_M}\} \odot \mathbf{x}_{\langle\ell-\ell'\rangle_{M,T}}, & \ell' \leq \ell \\ \mathbf{\Pi}_N (\text{diag}\{\mathbf{\Pi}_N^{-1} \mathbf{H}_{\ell\langle\ell-\ell'\rangle_M}\} \odot \mathbf{x}_{\langle\ell-\ell'\rangle_{M,T}}), & \ell' > \ell \end{cases}$$

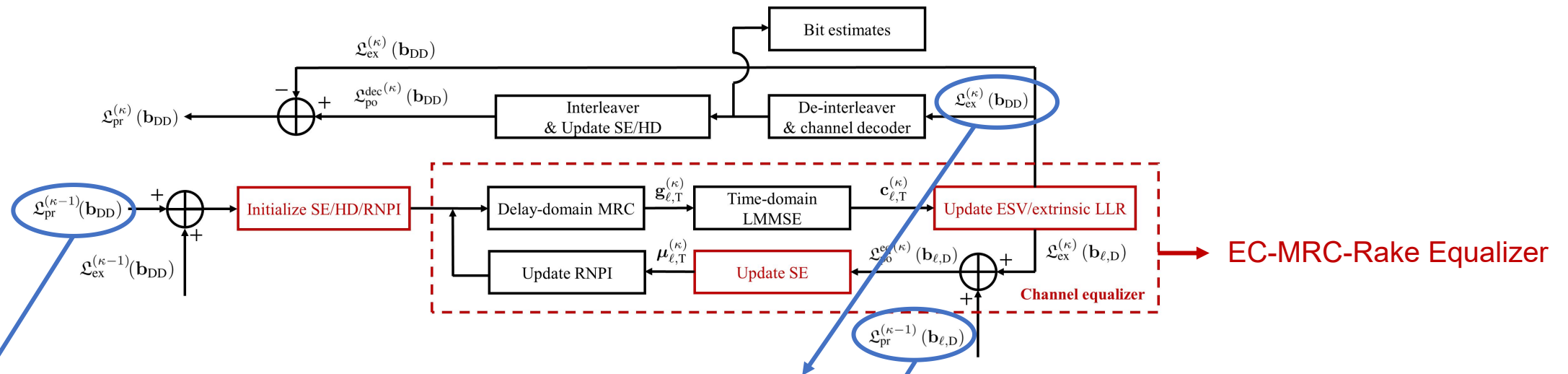
- Time domain **element-wise multiplications**
- Time domain **one-tap equalization**

# Proposed EC-MRC-Rake detector



- Turbo equalization structure

- *Decoding gain is exploited in the iterative channel equalizer*



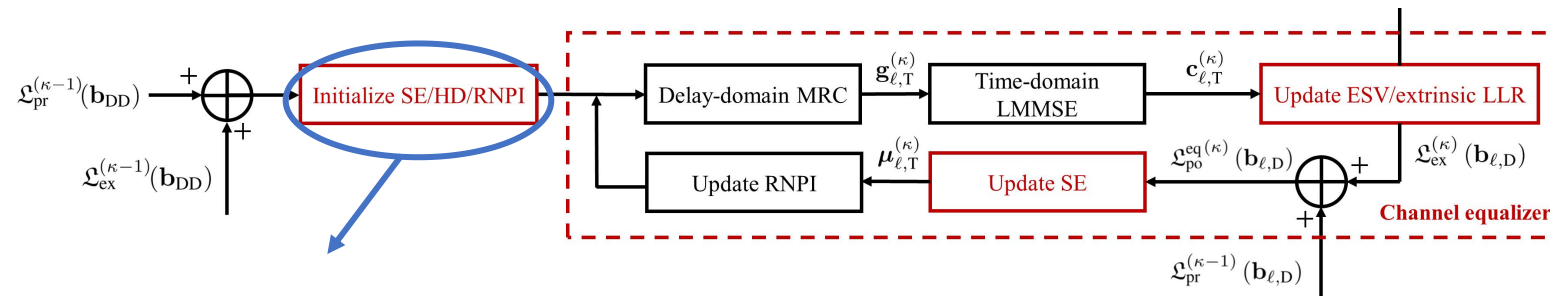
- Channel decoder **corrects** the equalizing incremental information (i.e., extrinsic LLR) in the current iteration
- Channel equalizer **uses** the decoding incremental information as a priori information in the next iteration



# Proposed EC-MRC-Rake detector



- EC-MRC-Rake equalizer



- *SE (symbol expectation) : used in the soft interference cancellation*

$$\begin{aligned}
 \text{Bit LLRs} \quad & LLR(b_{\ell k,0}), \dots, LLR(b_{\ell k,Q}) \xrightarrow{\mathbb{E}\{b_{\ell k,i}\} = \Pr\{b_{\ell k,i}=1\} = \frac{1}{1+e^{LLR(b_{\ell k,i})}}} \text{Bit Expectations} \quad \mathbb{E}\{b_{\ell k,0}\}, \dots, \mathbb{E}\{b_{\ell k,Q}\} \\
 & \xrightarrow{\mathcal{F}(\mathbb{E}\{b_{\ell k,0}\}, \dots, \mathbb{E}\{b_{\ell k,Q}\})} \text{Symbol Expectation (delay-Doppler domain)} \quad \mu_{\ell k}
 \end{aligned}$$

Bit-to-Symbol Mapper in 3GPP 38.211

- *HD (hard decision) : used in the estimation of equalized symbol variance*
- *RNPI (residual noise-plus-interference) : used in the interference cancellation to reduce the complexity*

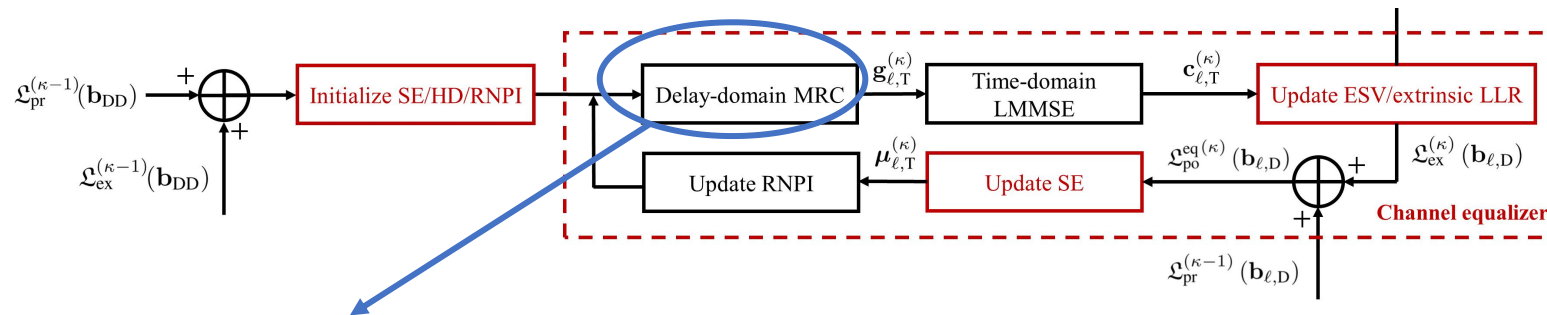
$$\tilde{\mathbf{e}}_{\text{DT}} = \mathbf{y}_{\text{DT}} - \mathbf{H}_{\text{DT}} \boldsymbol{\mu}_{\text{DT}}$$

$\mathbf{y}_{\text{DT}}$	Received signal in the <b>delay-time</b> domain
$\mathbf{H}_{\text{DT}}$	<b>Delay-time</b> channel matrix
$\boldsymbol{\mu}_{\text{DT}}$	Symbol expectation in the <b>delay-time</b> domain

# Proposed EC-MRC-Rake detector



- EC-MRC-Rake equalizer



- Block-wise interference cancellation (**inter-delay-interference cancellation**)

$$\begin{aligned}
 \text{(DT domain)} \quad \Delta \mathbf{y}_{\langle \ell + \ell' \rangle_{M,T}} &= \underbrace{\mathbf{y}_{\langle \ell + \ell' \rangle_{M,T}} - \sum_{\ell'' \in \mathcal{L}, \ell'' \neq \ell'} \mathbf{H}_{\langle \ell + \ell' \rangle_M} \langle \ell + \ell' - \ell'' \rangle_M \boldsymbol{\mu}_{\langle \ell + \ell' - \ell'' \rangle_{M,T}}}_{\text{soft interference reconstruction}} \\
 &= \underbrace{\tilde{\mathbf{e}}_{\langle \ell + \ell' \rangle_{M,T}} + \mathbf{H}_{\langle \ell + \ell' \rangle_M} \boldsymbol{\mu}_{\ell,T}}_{\text{low-complexity realization}}
 \end{aligned}$$

soft interference cancellation

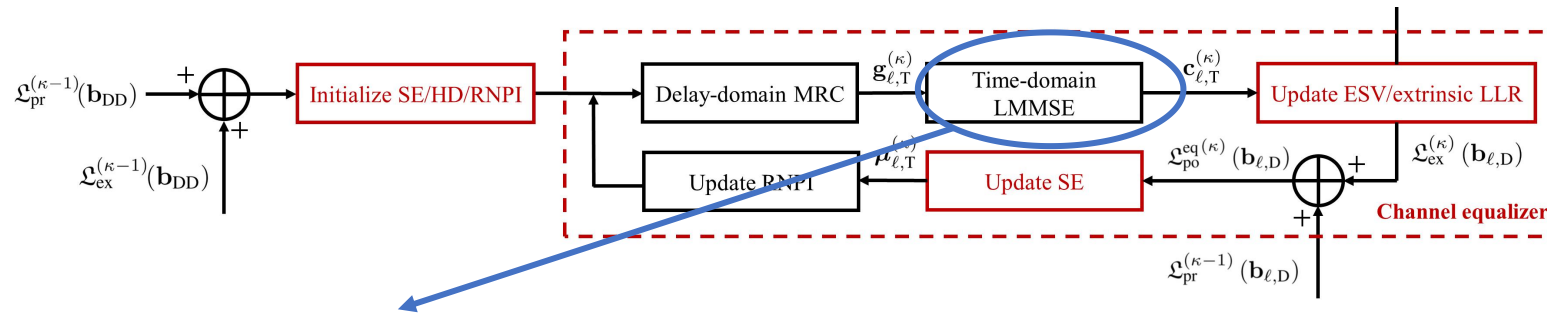
- Block-wise maximal ratio combining

$$\text{(DT domain)} \quad \mathbf{g}_{\ell,T} = \underbrace{\sum_{\ell' \in \mathcal{L}} \mathbf{H}_{\langle \ell + \ell' \rangle_M}^H \Delta \mathbf{y}_{\langle \ell + \ell' \rangle_{M,T}}}_{\text{delay-block combining}} = \left( \sum_{\ell' \in \mathcal{L}} \mathbf{H}_{\langle \ell + \ell' \rangle_M}^H \mathbf{H}_{\langle \ell + \ell' \rangle_M} \right) \boldsymbol{\mu}_{\ell,T} + \sum_{\ell' \in \mathcal{L}} \mathbf{H}_{\langle \ell + \ell' \rangle_M}^H \tilde{\mathbf{e}}_{\langle \ell + \ell' \rangle_{M,T}}$$

# Proposed EC-MRC-Rake detector



- EC-MRC-Rake equalizer



➤ One-tap equalization (*inter-Doppler interference cancellation*)

MRC output :  
(DT domain)

$$\mathbf{g}_{\ell,T} = \underbrace{\left( \sum_{\ell' \in \mathcal{L}} \mathbf{H}_{\langle \ell + \ell' \rangle_M^\ell}^H \mathbf{H}_{\langle \ell + \ell' \rangle_M^\ell} \right)}_{\text{diagonal matrix } \mathbf{K}_{\ell,T}} \boldsymbol{\mu}_{\ell,T} + \underbrace{\sum_{\ell' \in \mathcal{L}} \mathbf{H}_{\langle \ell + \ell' \rangle_M^\ell}^H \tilde{\boldsymbol{\epsilon}}_{\langle \ell + \ell' \rangle_M^\ell}}_{\triangleq \Delta \mathbf{g}_{\ell,T}}$$

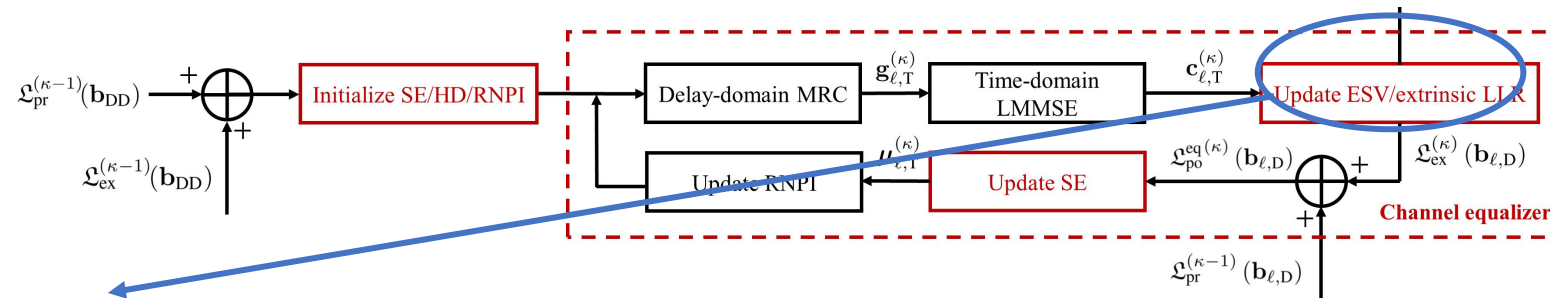
Equalized output :  
(DT domain)

$$\begin{aligned} \mathbf{c}_{\ell,T} &= \underbrace{\mathbf{g}_{\ell,T} \oslash (\text{diag}\{\mathbf{K}_{\ell,T}\} + \sigma_w^2 \mathbf{1}_N)}_{\text{time-domain one-tap equalizer}} \\ &= \underbrace{(\text{diag}\{\mathbf{K}_{\ell,T}\} \oslash (\text{diag}\{\mathbf{K}_{\ell,T}\} + \sigma_w^2 \mathbf{1}_N)) \odot \boldsymbol{\mu}_{\ell,T}}_{\triangleq \boldsymbol{\beta}_{\ell,T}} + \underbrace{(\mathbf{1}_N \oslash (\text{diag}\{\mathbf{K}_{\ell,T}\} + \sigma_w^2 \mathbf{1}_N)) \odot \Delta \mathbf{g}_{\ell,T}}_{\triangleq \mathbf{d}_{\ell,T}} \end{aligned}$$

# Proposed EC-MRC-Rake detector



- EC-MRC-Rake equalizer



- *Equalized output*

(DT domain) 
$$\mathbf{c}_{\ell,T} = \boldsymbol{\beta}_{\ell,T} \odot \mathbf{x}_{\ell,T} + \underbrace{\boldsymbol{\beta}_{\ell,T} \odot (\boldsymbol{\mu}_{\ell,T} - \mathbf{x}_{\ell,T})}_{\mathbf{e}_{\ell,T}} + \mathbf{d}_{\ell,T} \odot \Delta \mathbf{g}_{\ell,T}$$

- *Extrinsic LLR*

(DD domain) 
$$LLR_{ex}(b_{\ell k,i}) = \ln \left( \frac{\sum_{s(i)=0, s \in \mathcal{S}} \exp \left( -\frac{1}{\sigma_{\ell k}^2} |c_{\ell k} - \beta_{\ell k} s|^2 \right)}{\sum_{s(i)=1, s \in \mathcal{S}} \exp \left( -\frac{1}{\sigma_{\ell k}^2} |c_{\ell k} - \beta_{\ell k} s|^2 \right)} \right)$$

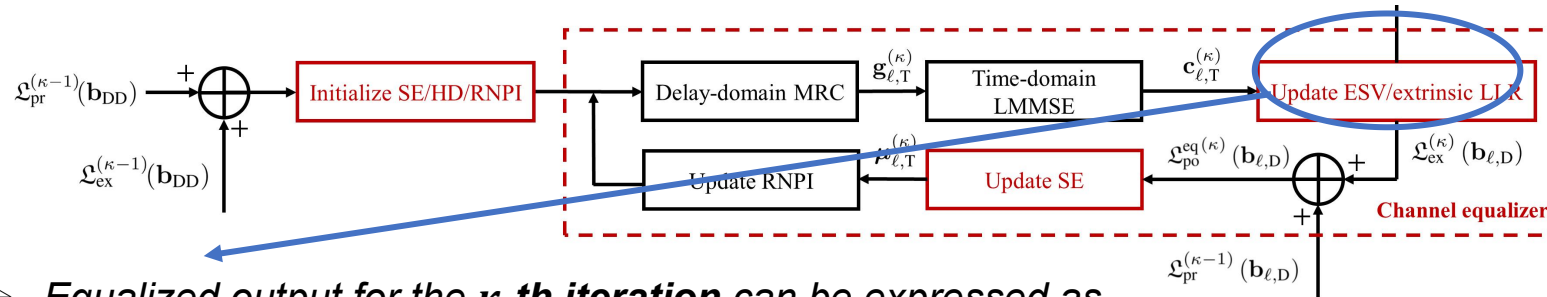
Equalized symbol variance (ESV)

Equalized symbol gain (ESG)

# Proposed EC-MRC-Rake detector



- EC-MRC-Rake equalizer



- *Equalized output for the  $\kappa$ -th iteration can be expressed as*

(DD domain)

$$\mathbf{c}_{\ell,D}^{(\kappa)} = \boldsymbol{\beta}_{\ell,D} \odot \hat{\mathbf{x}}_{\ell,D}^{(\kappa-1)} + \hat{\mathbf{e}}_{\ell,D}^{(\kappa)}$$

- *Equalized symbol gain (ESG): Calculated referring to the **convolution theorem***

(DD domain)

$$\beta_{\ell k} = \frac{1}{N} \sum_{n=0}^{N-1} \beta_{\ell n} \triangleq \beta_{\ell}$$

- *Equalized symbol variance (ESV): Estimated **sequentially***

(DD domain)

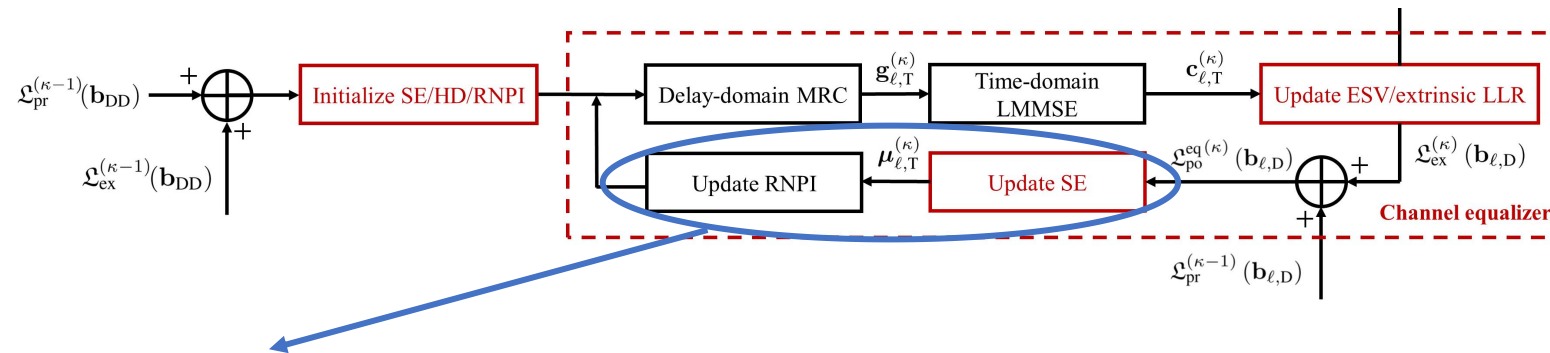
$$\widehat{\sigma}_{\ell k}^2 = \frac{\ell}{\ell + 1} \widehat{\sigma}_{(\ell-1)k}^2 + \frac{1}{(\ell + 1)N} \sum_{k=0}^{N-1} \left| c_{\ell k}^{(\kappa)} - \beta_{\ell} \hat{x}_{\ell k}^{(\kappa-1)} \right|^2$$

Average along the Doppler-axis since the symbols with the identical delay index go through the homologous DD channel

# Proposed EC-MRC-Rake detector



- EC-MRC-Rake equalizer



- *Updating symbol expectations (SE)*
- *Updating residual noise-plus-interference*

⇒ **Equalizing the next signal block  $\mathbf{x}_{\ell+1,T}$  using the latest updated information**

# Proposed EC-MRC-Rake detector



- EC-MRC-Rake turbo detector

TABLE I: Complexity comparisons in each iteration

<b>EP detector [7] (where <math>P \leq D \leq N  \mathcal{L} </math>)</b>	
$\times$	$MN (3D  \mathcal{S}  + 6D + 6  \mathcal{S} )$
$\div$	$MN (6D + \log_2  \mathcal{S}  + 3)$
exp	$MN 2  \mathcal{S} $
ln	$MN \log_2  \mathcal{S} $
<b>MRC-Rake detector [11]</b>	
$\times$	$MN (4  \mathcal{L}  + \frac{3}{2} \log_2 N + 3  \mathcal{S}  + 2 \log_2  \mathcal{S} )$
$\div$	$MN ( \mathcal{S}  + 2)$
exp	$MN  \mathcal{S} $
ln	$MN \log_2  \mathcal{S} $
<b>EC-MRC-Rake detector [proposed]</b>	
$\times$	$MN (3  \mathcal{L}  + \frac{3}{2} \log_2 N + 3  \mathcal{S}  + 4 \log_2  \mathcal{S}  - 1)$
$\div$	$MN ( \mathcal{S}  + \log_2  \mathcal{S}  + 2)$
exp	$MN ( \mathcal{S}  + \log_2  \mathcal{S} )$
ln	$MN \log_2  \mathcal{S} $

- $M$  : number of delay samples
- $N$  : number of Doppler
- $|\mathcal{S}|$  : number of elements in modulation alphabet
- $P$  : number of resolvable path
- $|\mathcal{L}|$  : number of resolvable delay taps
- $D$  : number of non-zero entries in each row of DD matrix

(No approximations for LLR computations here. If the Max-Log approximation is adopted, the computational complexity of both MRC-Rake and EC-MRC-Rake detectors can be reduced)

# Outline



- Motivations
- EC-MRC-Rake detector
- **Simulations**



# Numerical Results



- Simulation conditions

Num. of subcarriers	512
Subcarrier spacing	30KHz
LDPC coder	PEG, 2048, 0.5
Channel model	EVA / ETU
Doppler model	Jakes
Num. of Monte Carlo tests	100000 (stopped until 400 error code blocks for each SNR point)
<b>Num. of Doppler samples</b>	<b>8 / 32</b>
<b>Num. of Doppler taps each EVA delay component</b>	<b>1</b>

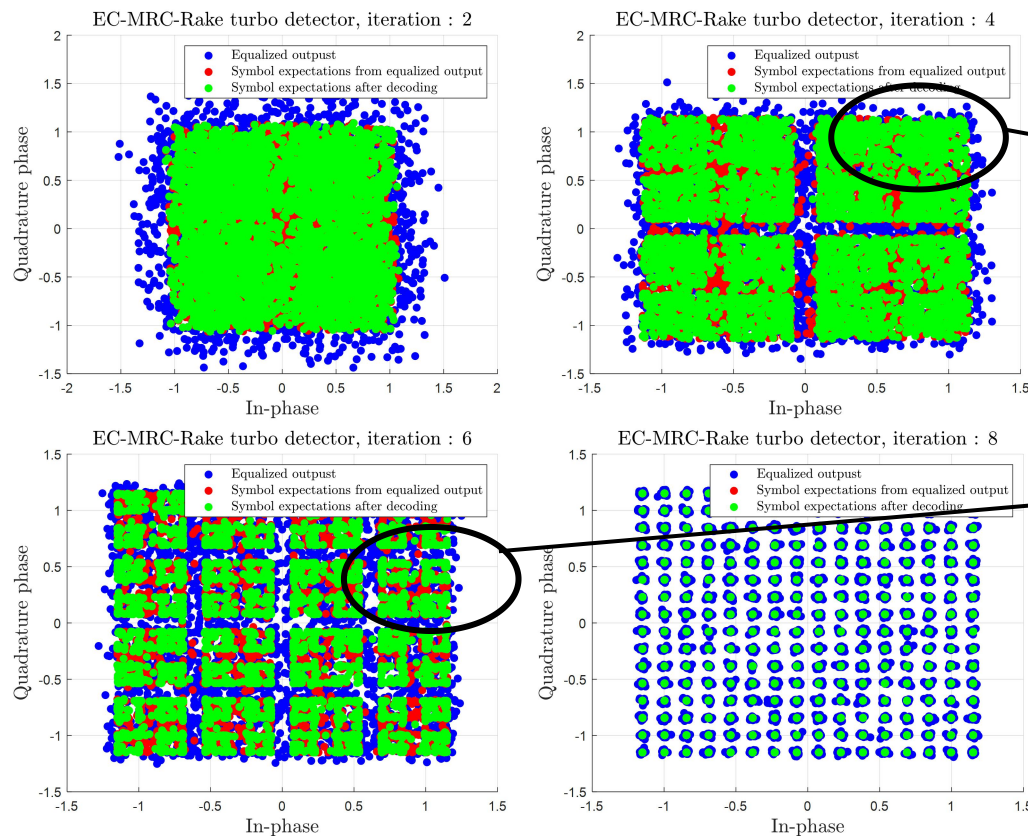
**Fractional** in the Doppler domain

**Short frame** OTFS system towards the low-latency communications

# Numerical Results



- Iterative evolutions of I/Qs using EC-MRC-Rake turbo detector



➤ Comparison between the *Blue* and *Red* points

Symbol expectations (SEs) can be regarded as the filtering results from the equalized output

➤ Comparison between the *Red* and *Green* points

Channel decoding provides additional SNR gain to make SEs closer to the constellation points

$512 \times 8$ , EVA,  $E_s/N_0 = 33\text{dB}$ ,  $k_{p,max} = 0.25N$   
( $v_{max} = 2300\text{km/h}$  for  $\Delta f = 30\text{KHz}$  and  $f_c = 3.5\text{GHz}$ )

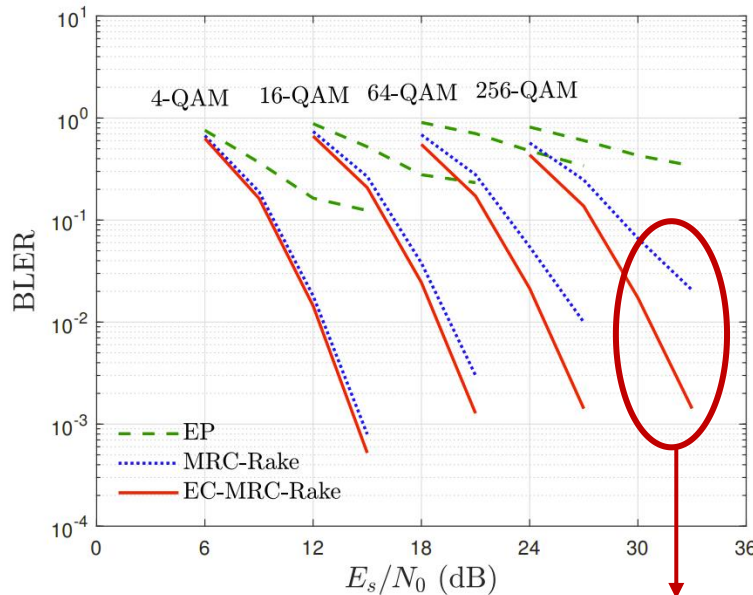
# Numerical Results



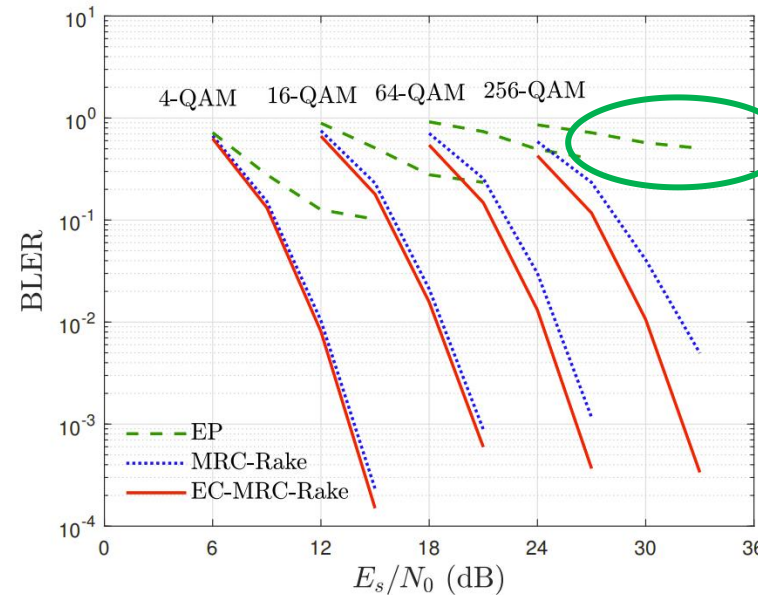
- BLERs vs.  $E_s/N_0$  under different QAM configurations

$512 \times 8$ ,  $k_{p,max} = 0.05N$  ( $v_{max} = 460\text{km/h}$  for  $\Delta f = 30\text{KHz}$  and  $f_c = 3.5\text{GHz}$ )

EVA



ETU



The configured maximum iteration number is not enough to give full play to the EP detector's performance

*Expectation cancellation (EC) shows significant **error rate improvement**, especially when high-order QAMs are adopted*

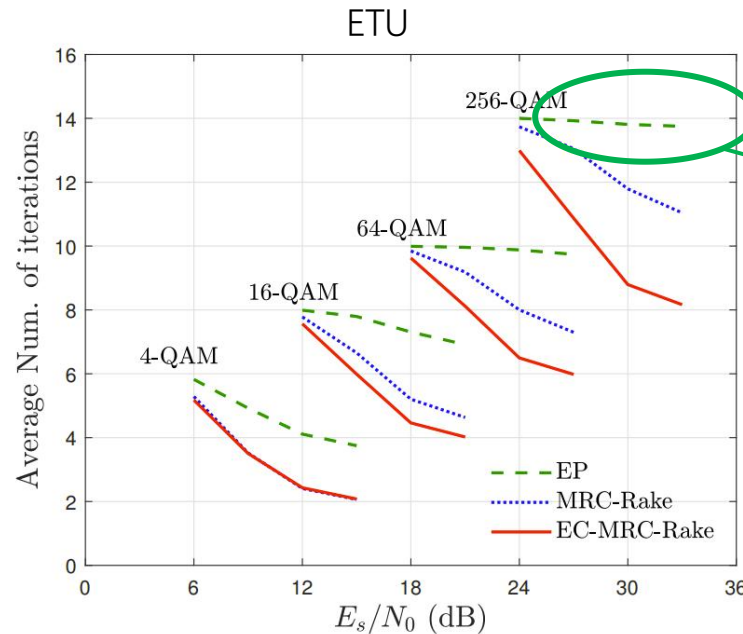
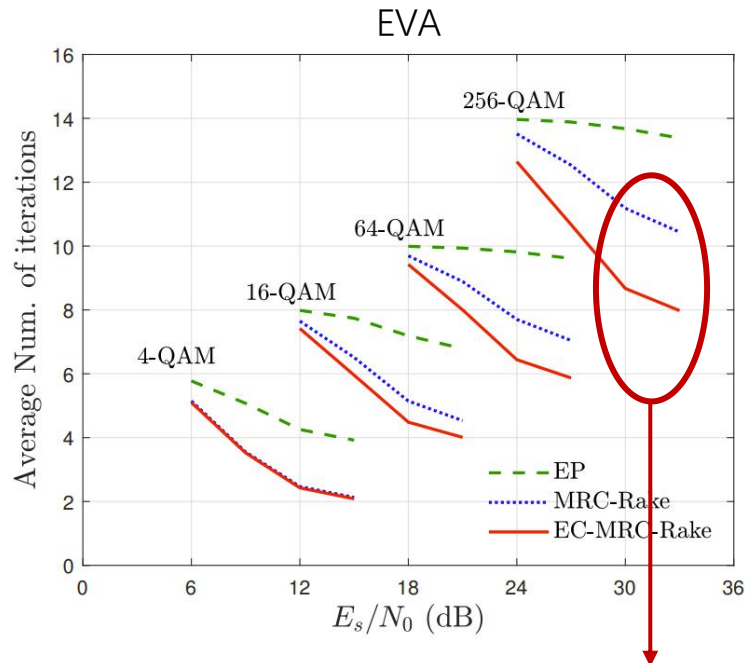
*The higher the QAM order, the smaller the tolerance of error propagations*

# Numerical Results



- Average iteration number vs.  $E_s/N_0$  under different QAM configurations

$512 \times 8$ ,  $k_{p,max} = 0.05N$  ( $v_{max} = 460\text{km/h}$  for  $\Delta f = 30\text{kHz}$  and  $f_c = 3.5\text{GHz}$ )



The configured maximum number of iterations is not enough to give full play to the EP detector's performance

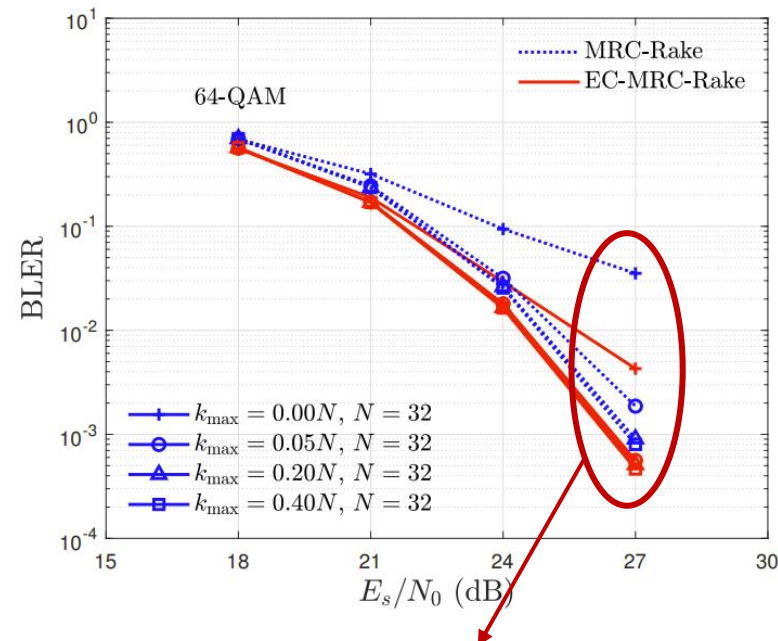
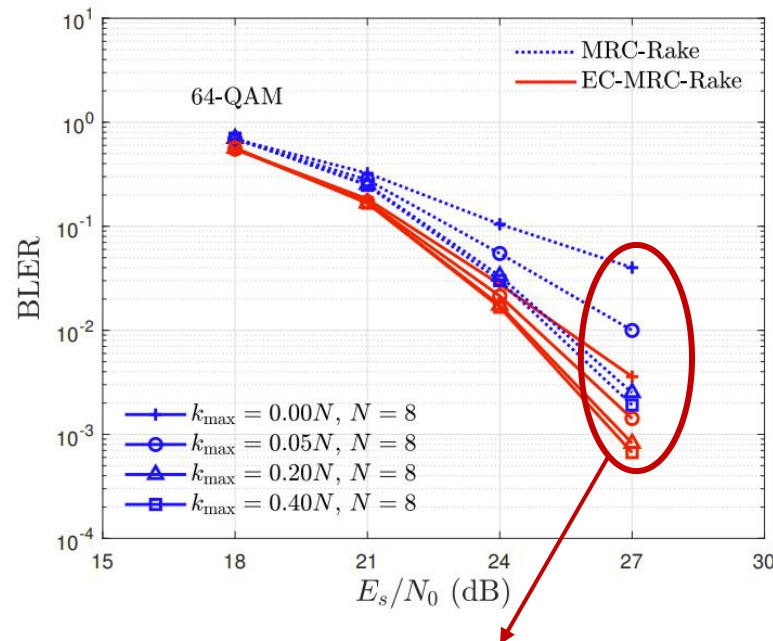
*Expectation cancellation (EC) shows significant convergence improvement, especially when high-order QAMs are adopted*

# Numerical Results



- BLERs vs.  $E_s/N_0$  under different  $k_{p,max}$  and  $N$

512 × 8, EVA ( $k_{p,max} = 0.05N$  corresponds to  $v_{max} = 460\text{km/h}$  for  $\Delta f = 30\text{KHz}$  and  $f_c = 3.5\text{GHz}$ )



*\*Note: In the simulations, the resolvable path at delay 0 contains 2 Doppler components, while the paths at other delays only contain single Doppler component*

Better diversity exploiting after introducing *the expectation cancellation (EC)*

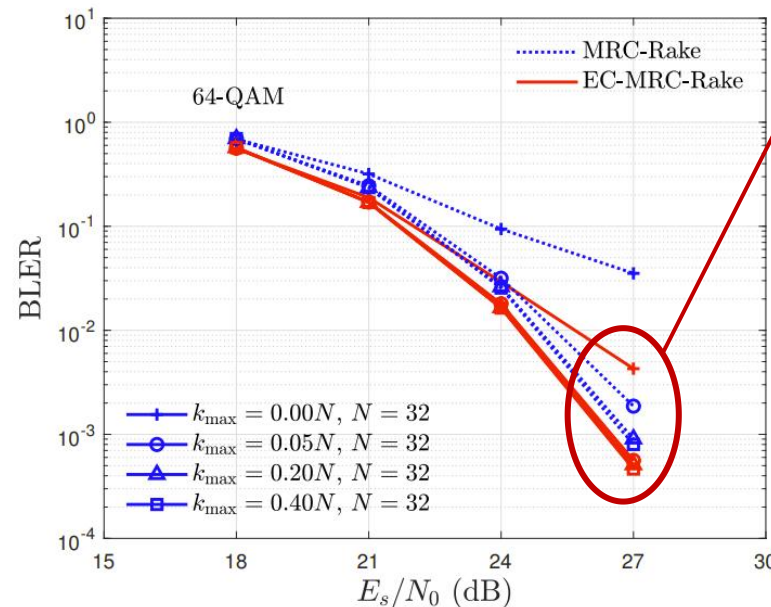
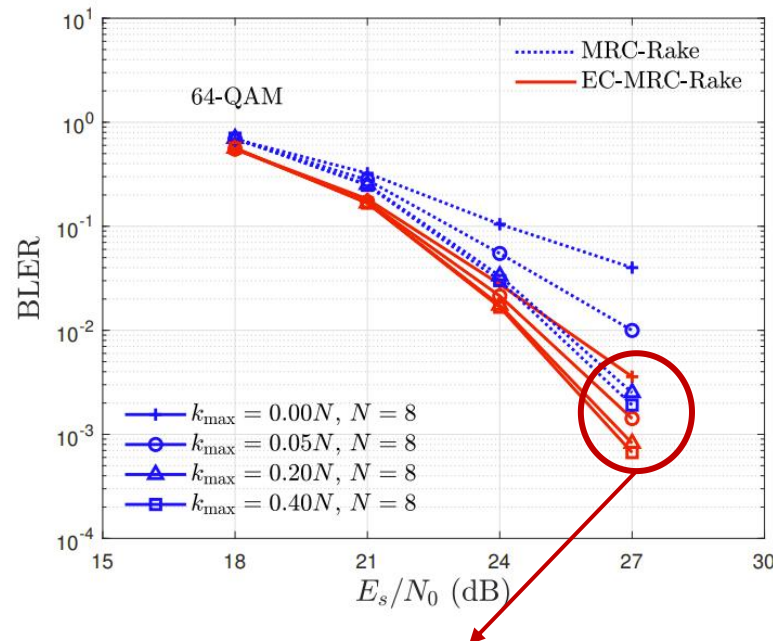


# Numerical Results



- BLERs vs.  $E_s/N_0$  under different  $k_{p,max}$  and  $N$

512 × 8, EVA ( $k_{p,max} = 0.05N$  corresponds to  $v_{max} = 460\text{km/h}$  for  $\Delta f = 30\text{KHz}$  and  $f_c = 3.5\text{GHz}$ )



No higher diversity gain for higher  $k_{p,max}$  under  $N = 32$  since the Doppler components at delay 0 are sufficiently separated in the DD plane under  $N = 32$

*\*Note: In the simulations, the resolvable path at delay 0 contains 2 Doppler components, while the paths at other delays only contain single Doppler component*

Higher diversity gain for higher  $k_{p,max}$  under  $N = 8$  since the Doppler components at delay 0 tend to be more separated in the Doppler domain for higher  $k_{p,max}$

# Thanks for your attention !

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