



Universidade de Brasília

Departamento de Ciência da Computação

Computador RISC-V

Intel DE1-SoC

DE1-SoC

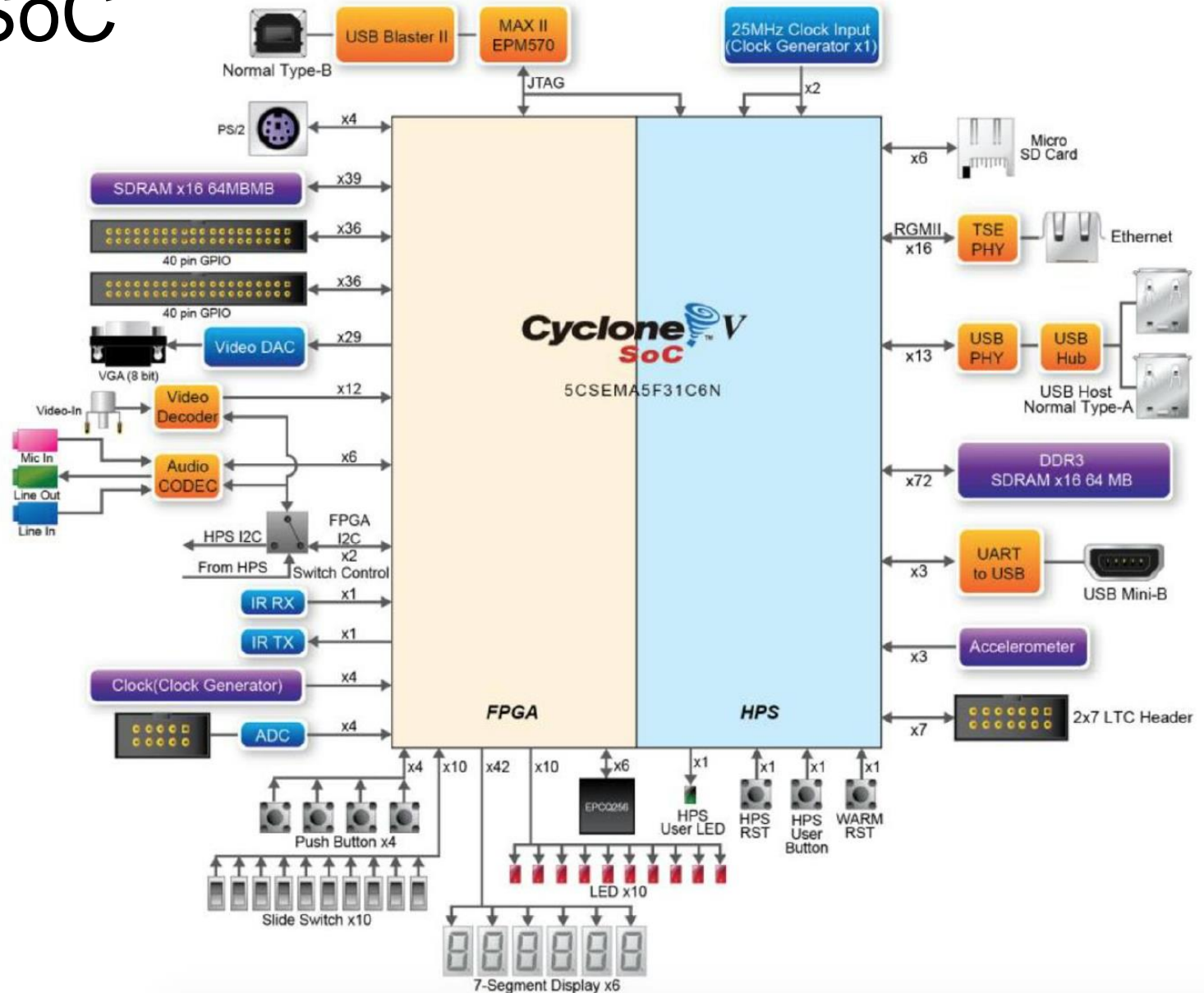
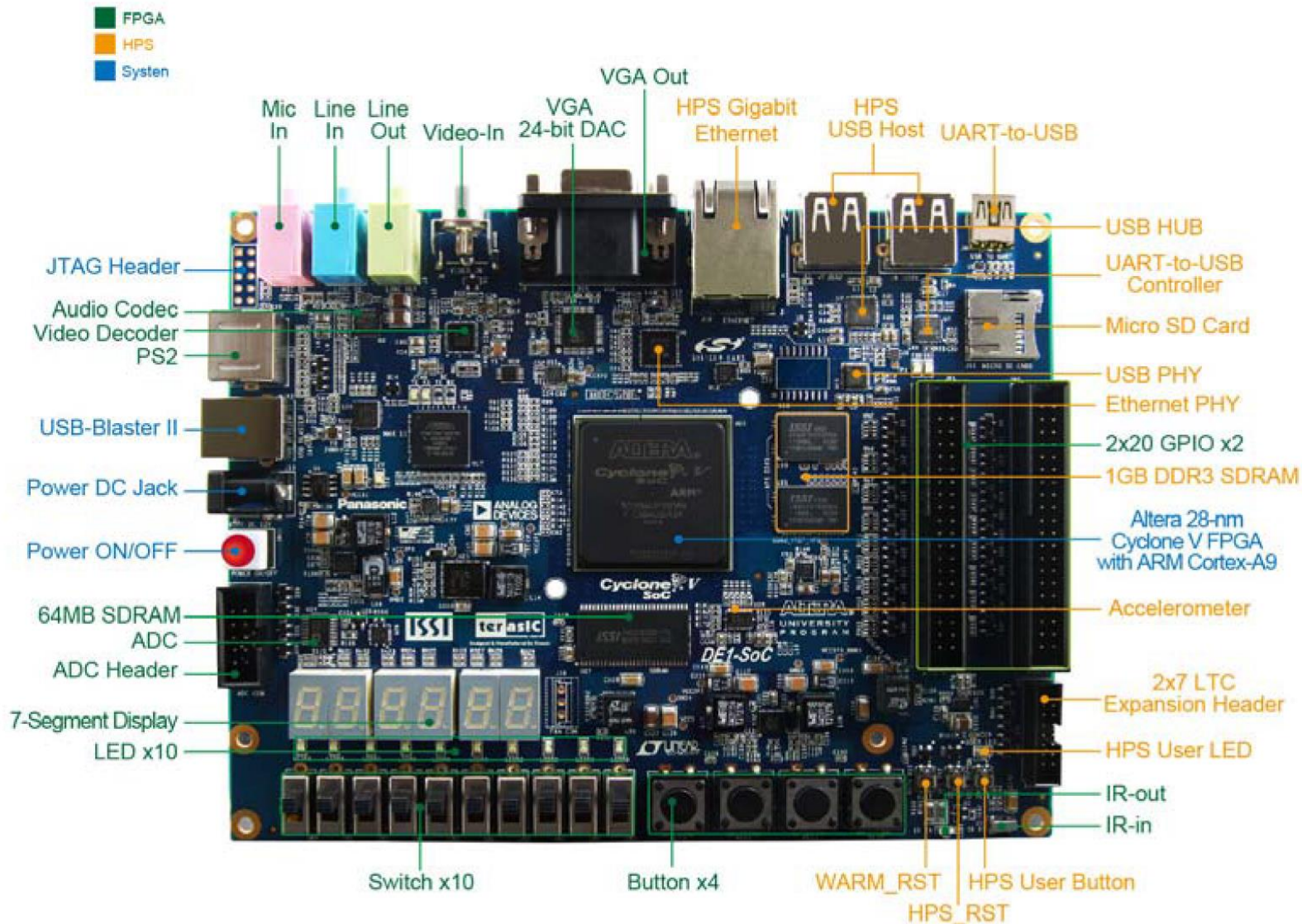


Figure 1: DE1-SoC layout

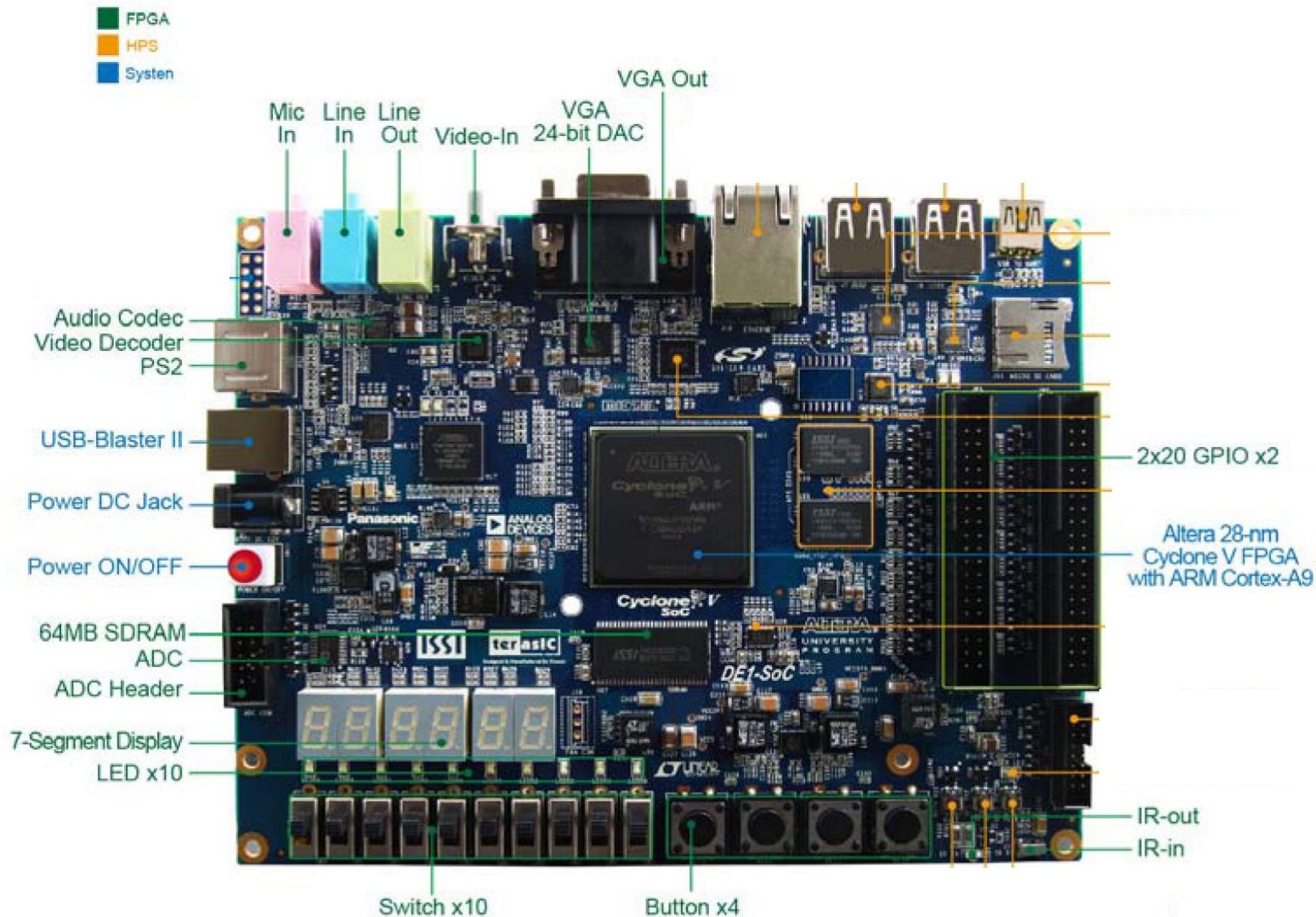


Kit de desenvolvimento DE1-SoC



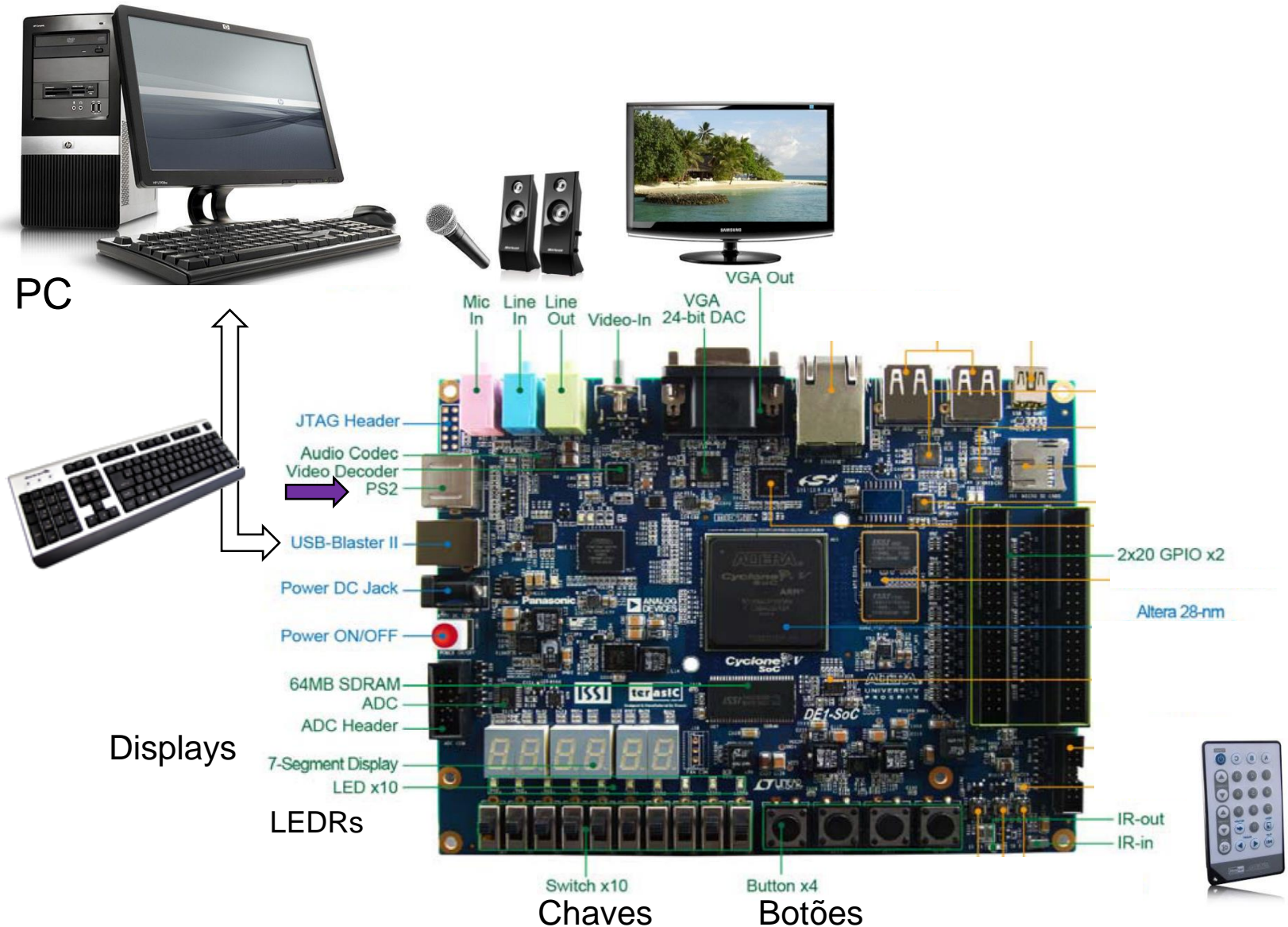


Computador com Processador RISC-V





Computador com Processador RISC-V





FPGA

