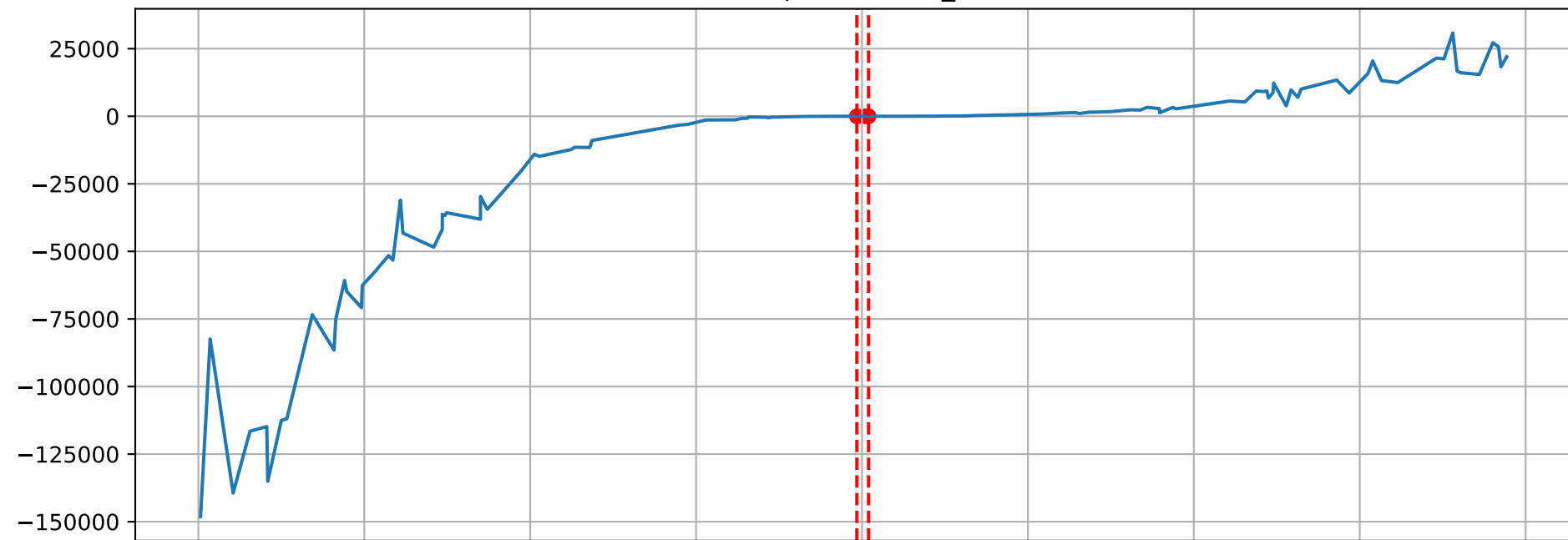


Bias: step, Model: IC\_FDNet



MSE (dB): step, Model: IC\_FDNet

