

Implementation		FF	LUT	Latency	Clock period(ns)
2TI-SIMON-Bit-Serialized	RTL	1335	740	4685	2.193
	HLS	1999	1430	4677	2.892
3TI-SIMON-Bit-Serialized	RTL	1468	684	4624	3.181
	HLS	3135	1436	4624	3.061
2TI-SIMON-Round-Based	RTL	1293	924	393	4.221
	HLS	1620	1436	395	3.333
3TI-SPECK	RTL	855	1642	2238	3.133
	HLS	986	1926	2238	4.722
TI-LED	RTL	623	539	5950	5.687
	HLS	3257	3868	5950	4.169
Range	HLS/RTL	1.15x~5.22	1.17x~7.17x	N/a	0.73x~1.51x