

Certificate of Achievement

This is to certify that

Harald Blab

has completed the Dot Matrix VHDL and FPGA course

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This is a comprehensive VHDL course covering advanced design strategies for FPGA development. The 120+ lectures teach methodologies allowing for code reuse while maintaining the highest verification standards on every hierarchy level.

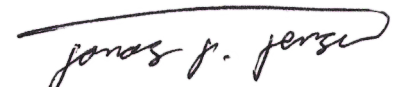
Concepts covered by this course include

- Advanced VHDL constructs
- Verification component
- Self-checking testbench
- Interactive TCL-driven testbench
- Inspection of synthesis logs and RTL schematics
- Assembling and lab testing of physical prototype

The Dot Matrix course website

vhdlwhiz.com/dotmatrix

Please direct requests for verification of the authenticity of this certificate to jonas@vhdlwhiz.com



Jonas Julian Jensen
Course Instructor