

# ENEE 630: Advanced Digital Signal Processing

## Project 01: Designing a $\pi/4$ BPSK TX/RX system

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### Abstract

In this project, we create an efficient TX RX system for  $\pi/4$  BPSK modulation and then further try to optimise the receiver while reducing the errors in time and frequency delay estimations. We get meaningful results and find that our system estimates the frequency and time estimates ideally in a clean channel and the BER curves in the presence of a noisy AWGN channel with doppler and time shifts closely follow the ideal BER curve. We finish the project by using 4 major optimisation techniques to reduce resources and make it suitable for a fixed point implementation. We also analyse our system using a RX MIPS table.

## 1 System Design

### 1.1 Transmitter

#### 1.1.1 Signal Generation

We start with burst formatting and generate the frame we work on. From project specifications, we have been given the sampling rate which is 16000 Hz as well as the duration of each frame which is 50 ms. From this information, we find that a single frame is 800 bits in length. The frame is further subdivided into three parts. The start of the frame is a pilot signal that will be used to correctly estimate the reception of the frame as well as the change in time and frequencies that might occur due to the noisy channel. The pilot channel occupies 128 bits at the start of the frame and we have chosen a 0 tone to be our pilot for convenience purposes. This pilot should give us an impulse when looking at it in the frequency domain on the receiver side. We shall analyse it in detail in the coming sections. The next component of the frame is a key which is a 8 bit sequence of 0's and whose role would be made more clear in the subsequent phases of the project. After this comes 664 bits of binary data that we have randomly generated in MATLAB.

#### 1.1.2 Modulation

From here we move on to the modulator. Our modulation of choice is  $\pi/4$  BPSK. This is one of the simplest PSK modulation schemes and our work to benchmark our floating point system will be focused around it. It is clear from the signal constellation diagrams for a traditional BPSK that the two symbols are phase shifted by  $\pi$  and we are using  $\pi/4$  BPSK which implies that one of our symbols will be in the first complex quadrant and of the type  $1+1j$  and the other will be  $\pi$  radians out of phase with this symbol and of the type  $-1-1j$ . We use this fact to perform the modulation of our randomly generated data. Several approaches can be taken to implement this. A simple formula is

$$X(k) = e^{j\pi k/4}(1 - 2\text{Data}[k])$$

but we have simply used the above mentioned property/definition to modulate the data rather than working in exponentials.

#### 1.1.3 Upsampling and Filtering

The modulated data stream is then passed through the final phase of our transmitter. We first upsample the modulated data frame 16 times. This means that we now have 800 symbols of data that correspond to  $16 \times 800$  samples rather than just 800 samples as before upsampling. This upsampled data stream is then further passed through a low-pass cosine raised filter. The cosine raised filter has a rolloff coefficient of 0.35 and covers  $\pm 3$  symbols with 97 taps. This enables us to define sps and span of the filter to implement it in MATLAB. The sps field is defined as the number of samples per symbol which for our system is 16. Our span is 6 because we truncate our signal to 6 symbols for filter application. The signal stream after the low-pass filter has been defined as our baseband signal.

### 1.2 Channel

We have designed a channel from scratch to test our receiver operation. The channel introduces four types

Table 1: Cost of Receiver Table

Operations	Lowpass Filter	Downsampling	DFT	Time Estimation	Frequency Estimation	Phase Estimation	Demodulation
Multiplication	800 x 97	800 x 16	128 x 128 +128 x 30	80 x 128	130	256	1600
Addition	879 x 97	800 x 15	128 x 127 + 128 x 29	-	1	117	800
Sine	-	-	-	-	-	256	2400
Cosine	-	-	-	-	-	256	2400
Max	-	-	-	81	-	-	-
Division	-	-	-	-	1	2	-
Square Root	-	-	-	-	4	-	-
Arctan	-	-	-	-	-	1	-

of perturbations. First of all, the channel has an additive white gaussian noise component. Further the channel has a doppler shift and time delay effect. This means we create a random unknown frequency delay to the baseband signal represented as  $f_0$  as well as time shift that we express as  $k_0$ . According to the specification sheet, we were told to incorporate a frequency shift of the range  $[-1500, 1500]$  Hertz and a time delay of  $[-2.5, 2.5]$ ms in the baseband signal. We know that the sampling frequency given to us is 16000 Hz. Given that the duration is  $\pm 2.5$  ms, there is thus an error/uncertainty of

$$\pm 16000 * 2.5 * 10^{-3} = \pm 40$$

samples in the baseband signal. After upsampling we can easily analyse that this uncertainty now becomes  $\pm 40$  *symbols*. This means we have to introduce a time shift of exactly  $\pm 40*16$  in our baseband signal.

Further using the SNR of the signal, we can very easily model the WGN to be added to the doppler and time delayed baseband signal. We also incorporate a phase delay in the channel that we have initialised as the variable  $p_0$  in the MATLAB script. This concludes our channel. We now have a data stream that has been shifted by an unknown frequency  $f_0$ , an unknown time delay of  $k_0$  samples and has an additional phase component  $p_0$  along with some WGN noise. The size of the frame has also been increased to  $880*16$ . The channel thus has a behaviour similar to the given input output relation.

$$R(k) = e^{i\phi_0} e^{j2\pi f_0 k/f_s} S(k - k_0) + n(k)$$

## 1.3 Receiver

### 1.3.1 Matched Filter and Energy Filter for downsampling

We start our receiver with a matched filter which is the same raised cosine filter that we designed on the transmitter side. The convolution of the channel passed signal and the raised cosine filter now needs to be downsampled. However this is not very straightforward.

We introduced redundancy in the signal using upsampling to make noise a non factor. Now we need to *downsample* our frame that we got from the channel

and get 880 bits of data back. But the most important question that is still lingering is what is the ideal downsampling time? It needs to be understood that if we start downsampling at the incorrect moment, we will *always get 880 bit frames that have absolutely no data!*. Thus there needs to be some sort of sampling energy filter that identifies what the correct downsampling time is. Note that we are not completely eliminating the time shift using this filter. We will still need to do time estimation on the data frame that we get back.

The sampling filter can be created in multiple ways. We can check the DFT of all the data for each index in the first 16 points. We can then get the index that maximises the energy or correspondingly the DFT values to get the optimal downsampling index. However, similarly we can use the time domain expression of Parseval's relation (*as shown below*) to find the index with the maximum energy in the time domain and this is the algorithm that we have chosen to implement to reduce computational resources and not compute sines and cosines used in a standard DFT block. We will later see, we can in fact do this using a table of DFT coefficients to optimise the algorithm even further since the table call will be based on indexing and no actual computations would be required.

$$\sum_{n=-\infty}^{\infty} |x(n)|^2 = \frac{1}{2\pi} \int_{-\pi}^{\pi} |X(e^{j\omega})|^2 d\omega$$

### 1.3.2 Pilot Estimation

The estimation of a pilot is not a very tough task. We have already applied this technique in the aforementioned sampling time formulation using the energy method. All we need to do to estimate the pilot is to find the exact index where there is a spike corresponding to our zero tone pilot's frequency response. Since we know that the pilot is 128 bits in length, we can shift a window of size 128 bits and calculate the DFT of the window. The index where the spike occurs corresponds to the pilot and the time delay can be finally resolved. We optimised this algorithm further. Firstly we chose to implement a DFT matrix of our own and not rely on the inbuilt *fft()* function. This enabled us to use the created DFT table to extract the coefficients that we required and just refer to the memory where the coefficients are already stored.

### 1.3.3 Time and Frequency estimation

After we have found the location of the pilot, we take the DFT of the pilot. We know that the DFT of the pilot signal has a peak at 2kHz when we transmit it (due to the nature of  $\pi/4$  BPSK modulation). After the channel, this peak has shifted  $\pm 1500$  Hz. Therefore, we expect a peak in the DFT of the pilot signal in the interval of 500Hz to 3500Hz. Resolution of the 128 point DFT is then expressed as:

$$\frac{2\pi}{128} = 125 \text{ Hz}$$

The optimisation of the indices relies on the following calculations:

$$\frac{2000 - 1500}{125} = 4 \quad \text{and} \quad \frac{2000 + 1500}{125} = 28$$

Our range thus corresponds to the 5 to 29th indices of the DFT coefficients. Multiplying with W128trunc gives us these indices without any need to use the `fft()` function. We then look for peaks from the DFT. The index of the maximum value gives us where the peak occurs. However, in order to get a more accurate solution, we also look at the second peak. If there exists some point larger than half of the peak value, we store this index as well.

If there is exactly one peak, 'the index number-13' multiplied with 125 Hz give us the frequency shift (Because when there is no shift, we expect a peak in the 13th index). On the other hand, if there are more than one peak, we apply the *linear interpolation*. Further, the square root operation give us the magnitude. The linear interpolation algorithm is as follows. Consider the magnitudes of peaks are  $m_1$  and  $m_2$ , and indices are  $k_1$  and  $k_2$ , for first and second peak respectively. Then linear interpolation can be written as:

$$k = \frac{m_1 k_1 + m_2 k_2}{m_1 + m_2}$$

After we find, we first decode pilot data. Because we know exactly what is our pilot data is, we can estimate the phase shift from that using a simple inverse tangent operation and then demodulate.

### 1.3.4 Phase Estimation

After we estimate time and frequency shift, we can find phase shift from the pilot signal. First we can reverse the effect of the frequency and time shifts. Then, we can demodulate pilot signal as  $\hat{P}[n]$  by multiplying  $\exp(-\frac{\pi}{4}n)$ . We have observed that, first few terms of  $\hat{P}[n]$  is different from the rest because of the non-ideal lowpass filter. On the other hand, we know that we know that our pilot is contains one bits, and in the constellation diagram it is correspond to complex number  $p = (1 + j0)$ . Because of the phase shift demodulated version of the pilot signal is different from the  $p$ . If we

define  $\hat{p} = \frac{1}{118} \sum_{k=11}^{128} \hat{P}[n]$ , we can estimate the phase shift as

$$\phi = \arctan \left( \frac{\Im\{\hat{p}\}}{\Re\{\hat{p}\}} \right).$$

### 1.3.5 Demodulation

We can now exactly recover the 800 bits of data that we started with. These 800 bits are however modulated. We can now feed our frame into a  $\pi/4$  BPSK demodulator to recover the actual data. This demodulator compares the recovered sample with the decision boundary of the constellation diagram of  $\pi/4$  BPSK to identify whether the bit received is 1 or 0. An easier way of modelling the decision boundary is to map the  $\pi/4$  demodulation to a simple BPSK demodulation and check the sign of the found symbol. We further map the symbol found to 2 and 1 corresponding to positive and negative data respectively.

BER can then be calculated using a simple comparator and over many iterations the FER can be initiated as a count that increases any time there is even one bit of error in a frame. The iterations are repeated according to the details provided in the project specifications.

## 2 Experiment Setup

### 2.1 Major Optimisations

- **DFT BLOCK** We have implemented the DFT algorithm as a table, thereby getting rid of the need to use the `fft()` function.
- **INDEX TRUNCATION** We have only computed the DFT coefficients that lie in our own range of frequencies i.e signal frequency  $\frac{f_s}{8} \pm 1500$  Hz.
- **INTERPOLATION** We have also used linear interpolation between the maximum and the second maximum peak to generalise the system to work for frequencies that are not integer multiples of the resolution i.e 125 Hz.
- **PARSEVAL'S THEOREM** We have exploited Parseval's relation to bypass the need to compute the frequency interpretation for analysing the best possible downsampling time.

### 2.2 Setup 1

Specifications:

- SNR = 100 dB *Clean channel*
- F0 = 0 Hz
- TIME OFFSET = -2.5 ms to 2.5 ms, Step = 0.0625 ms
- NUMBER OF SIMULATIONS = 100 per point

### 2.2.1 Observations

We notice that our system performs ideally under a clean channel. The standard deviation of the estimates is 0 and we get the perfect frequency and time estimates every time.

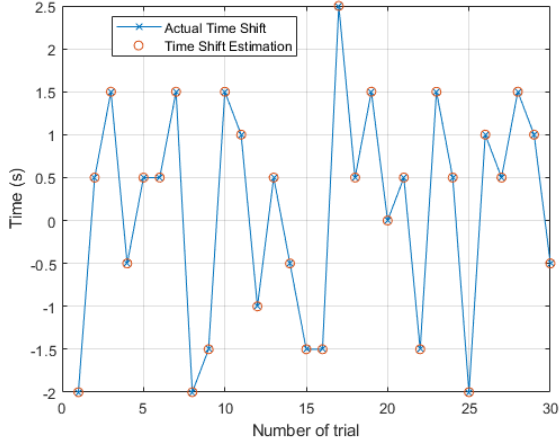


Figure 1: Part 1 Time Estimates and Random time shifts

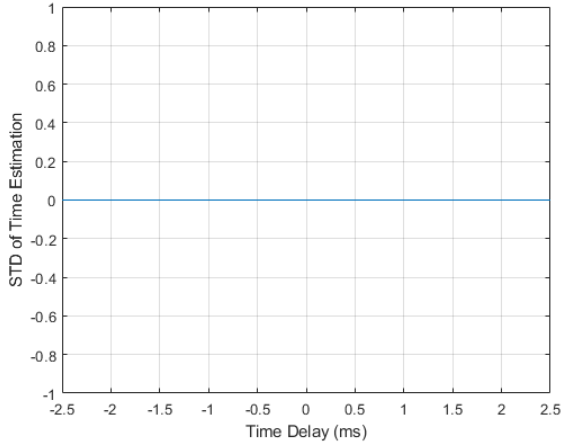


Figure 2: Part 1 Standard Deviation in the time estimate

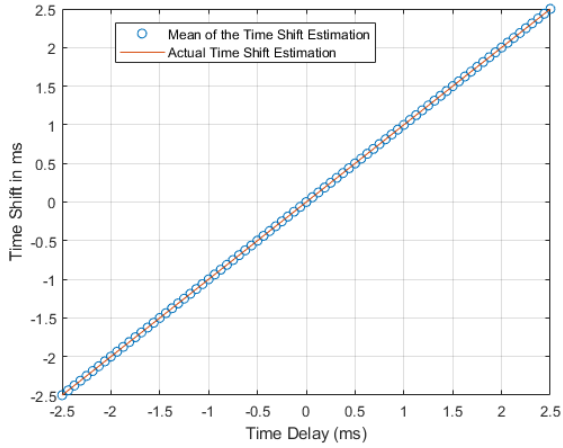


Figure 3: Part 1 Mean in the time estimate

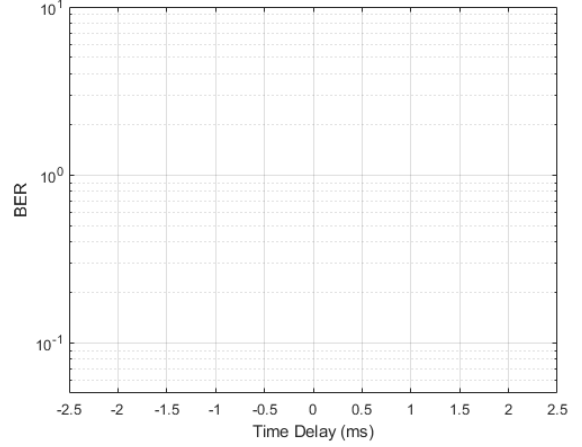


Figure 4: Part 1 BER is 0 for the Clean Channel

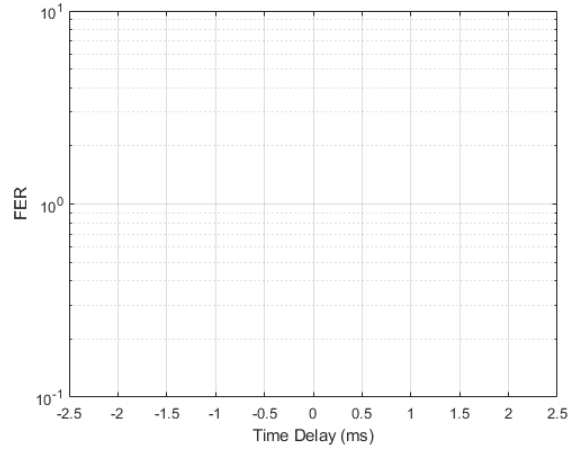


Figure 5: Part 1 FER is 0 for the Clean Channel

## 2.3 Setup 2

Specifications:

- $\text{SNR} = 100 \text{ dB}$  *Clean channel*
- $F_0 = -1500 \text{ Hz to } 1500 \text{ Hz}$ , Step= 125 Hz
- $\text{TIME OFFSET} = 0 \text{ ms}$
- $\text{NUMBER OF SIMULATIONS} = 100 \text{ per point}$

### 2.3.1 Observations

We notice that our system performs ideally under a clean channel. The standard deviation of the estimates is 0 and we get the perfect frequency estimates every time. Also notice that the BER and FER are 0 for every iteration and this is in line with the choice of our channel which has a very high SNR of 100 db.

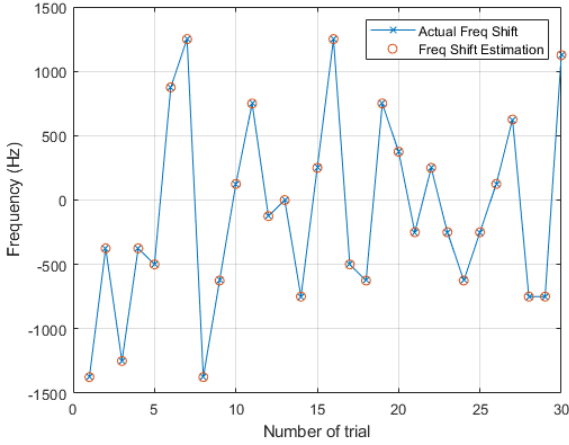


Figure 6: Part 2 Frequency Estimates and Random freq shifts

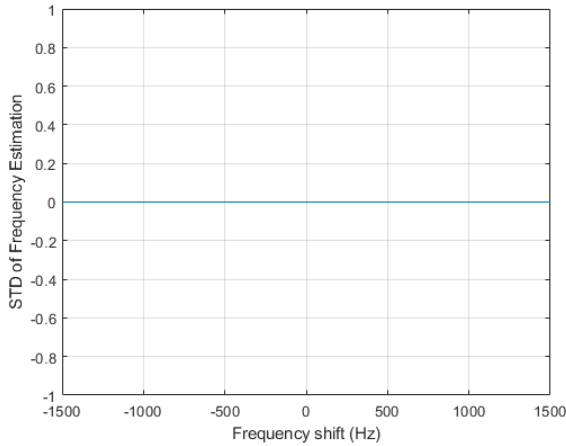


Figure 7: Part 2 Standard Deviation in the frequency estimate

## 2.4 Setup 3

Setup 3 includes 3 major experiments. In the first experiment, we do not time or frequency shift and we

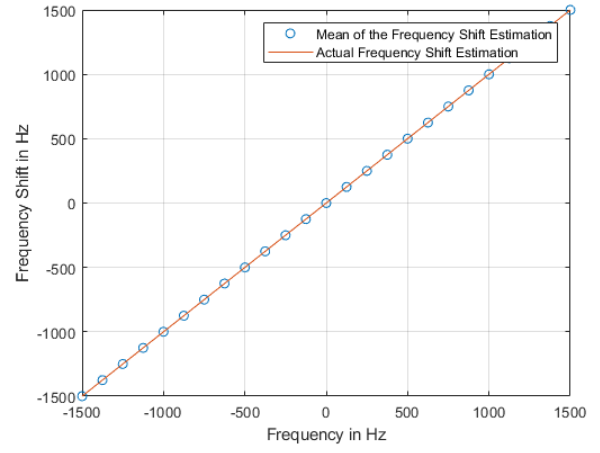


Figure 8: Part 2 Mean in the frequency estimate

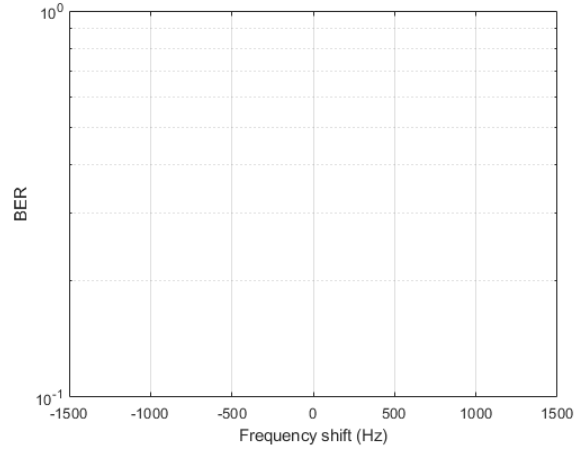


Figure 9: Part 2 BER is 0 for the Clean Channel

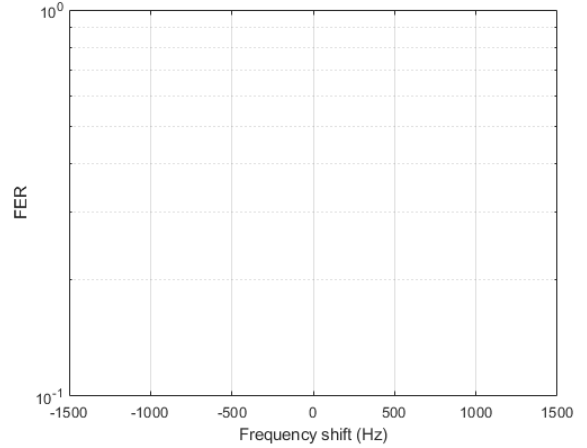


Figure 10: Part 2 FER is 0 Clean Channel: Setup 2

compare our curve to the ideal BPSK curve.

From Figures 16 and 20, we can see that it follows the theoretical bound very closely.

Specifications: (Run our system for given pairs of time and frequency offsets)

- $\text{SNR} = -3 \text{ to } 15 \text{ dB}$ , Step= 0.5 dB

- $F_0 = 0 \text{ Hz}, 625 \text{ Hz}, 62.5 \text{ Hz}$
- $\text{TIME OFFSET} = 0 \text{ ms}, 0.25 \text{ ms}, \frac{2.5}{80} \text{ ms}$
- $\text{NUMBER OF SIMULATIONS} = >1000$
- $\text{FRAME ERROR LIMIT} = >50$

#### 2.4.1 Observations

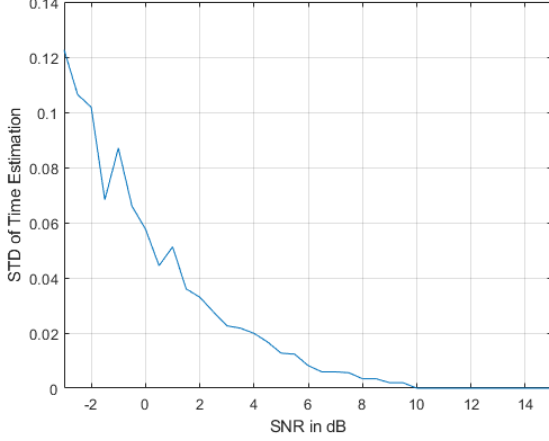


Figure 11: Part 3A Std Deviation of Time estimate

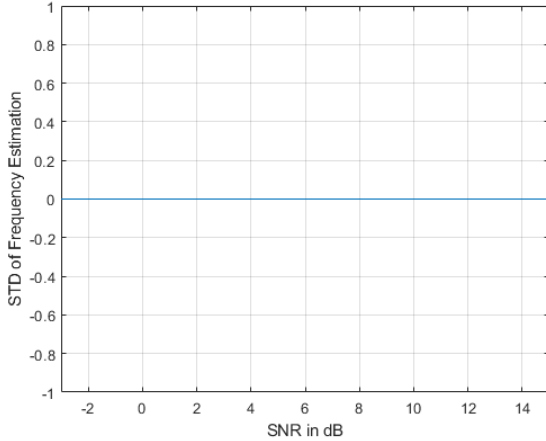


Figure 12: Part 3A Std Deviation of Freq estimate

### 3 Key Conclusions

We can conclude that we have created an efficient floating point benchmarking system that accurately estimates both channel induced frequency and time shifts while also getting near ideal BER to SNR curves. We observe no BER and FER and perfect frequency and time estimates in the case of a clean channel. In the case of a noisy channel, SNR's of around 8 db are enough to completely eliminate the uncertainty in the performance of our design and it behaves similar to the clean channel case and the FER is completely eliminated at SNRs of around 10 dB.

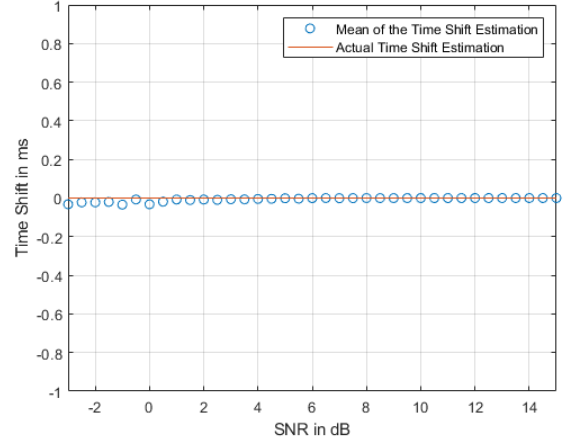


Figure 13: Part 3A Mean Deviation of Time estimate

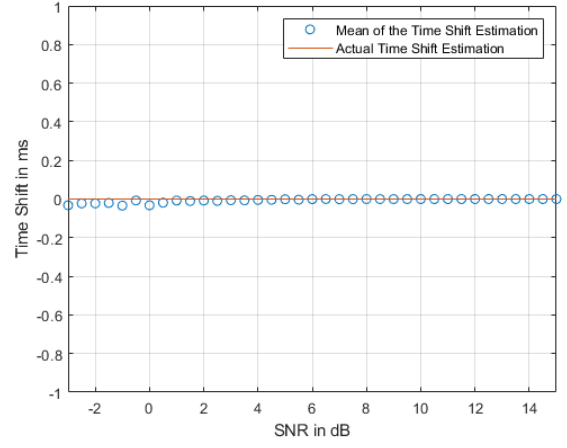


Figure 14: Part 3A Mean Deviation of Freq estimate

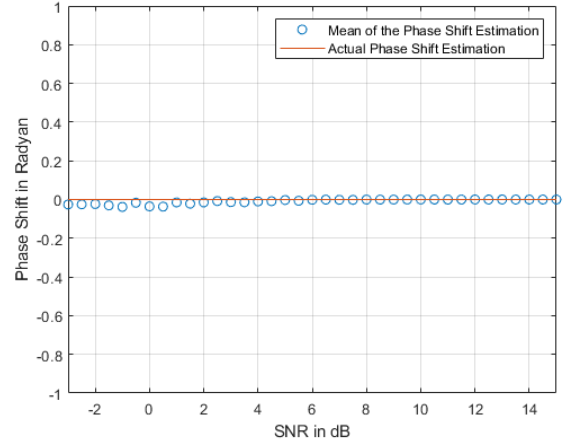


Figure 15: Part 3A Mean Deviation of Phase estimate

The system proposed has also been optimised heavily using both algorithmic knowledge as well as some "tricks" that are common in DSP implementations. The next step is to go a step further and implement a faster real time  $\pi/4$  BPSK system that can be put onto an FPGA to reduce computation times.

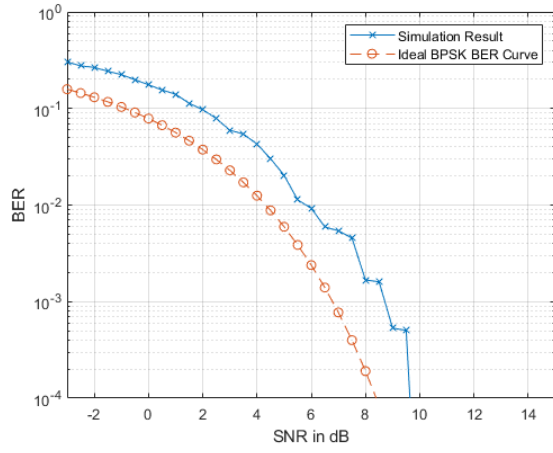


Figure 16: Part 3A BER estimate

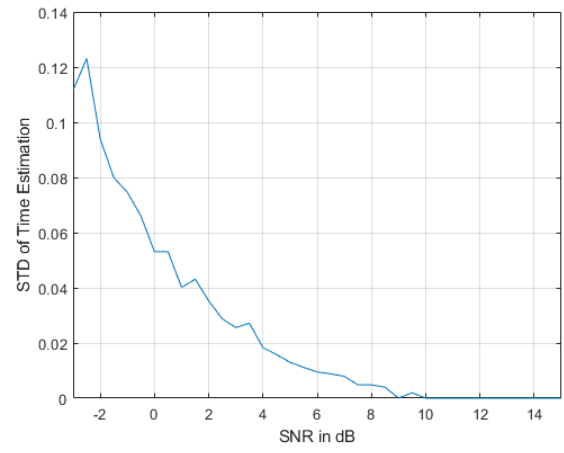


Figure 19: Part 3B Std Deviation of Time estimate

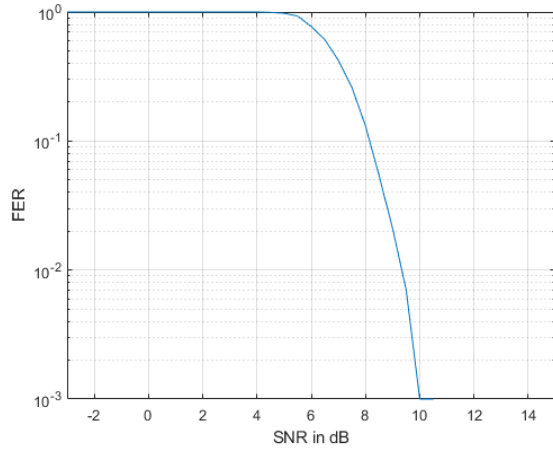


Figure 17: Part 3A FER estimate

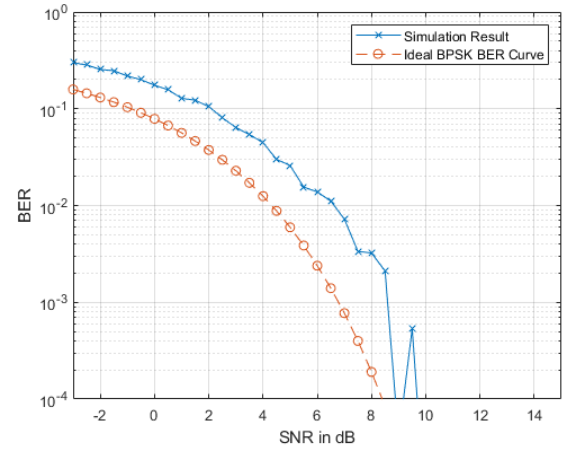


Figure 20: Part 3B BER estimate

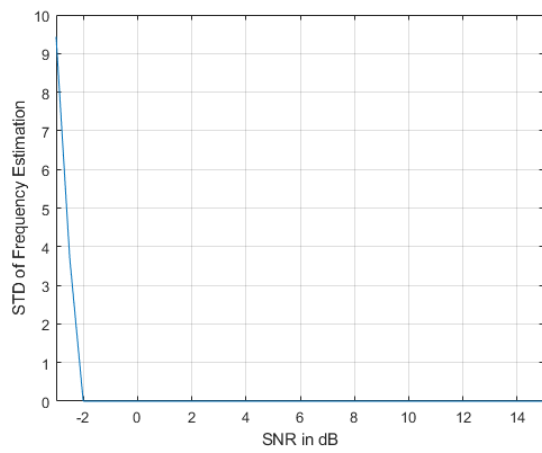


Figure 18: Part 3B Std Deviation of Freq estimate

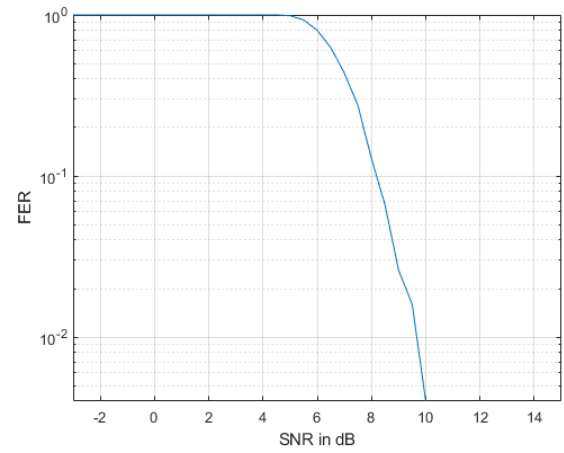


Figure 21: Part 3B FER estimate

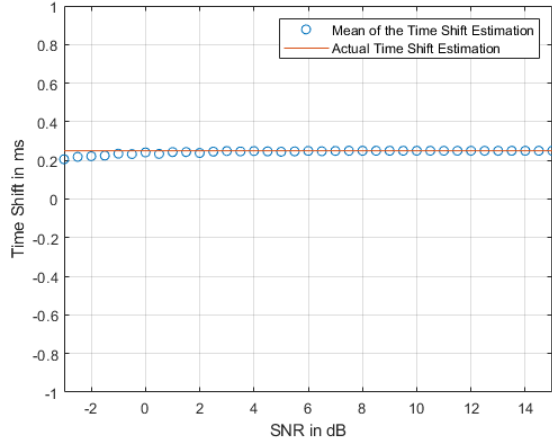


Figure 22: Part 3B Mean Deviation of Time estimate

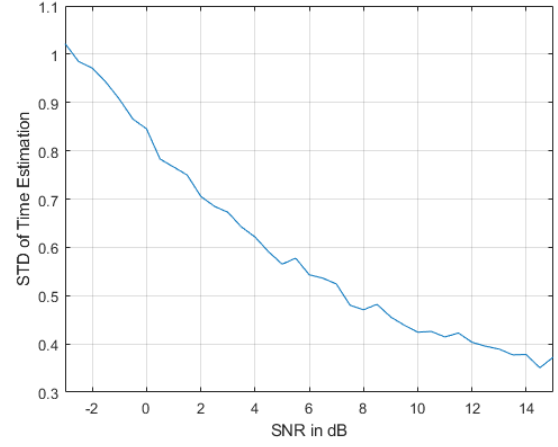


Figure 25: Part 3C Std Deviation of Time estimate

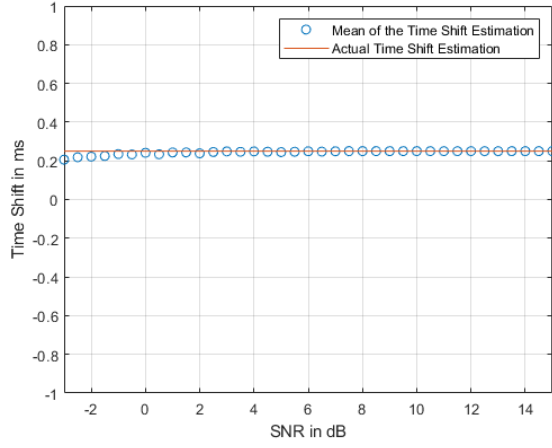


Figure 23: Part 3B Mean Deviation of Freq estimate

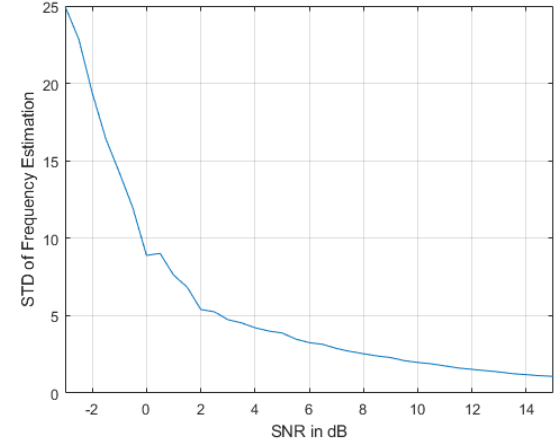


Figure 26: Part 3C Std Deviation of Freq estimate

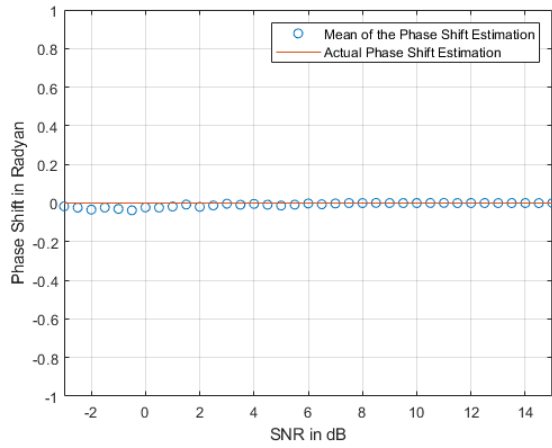


Figure 24: Part 3B Mean Deviation of Phase estimate

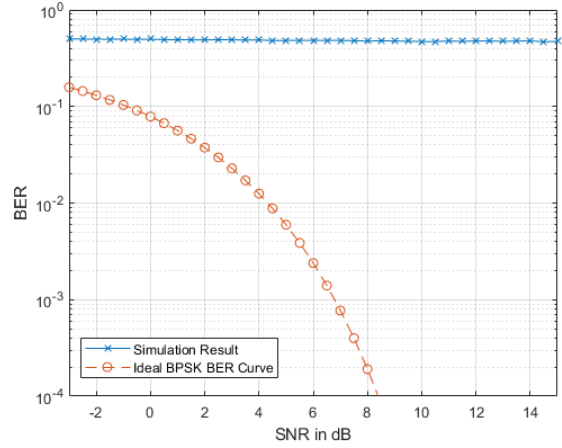


Figure 27: Part 3C BER estimate



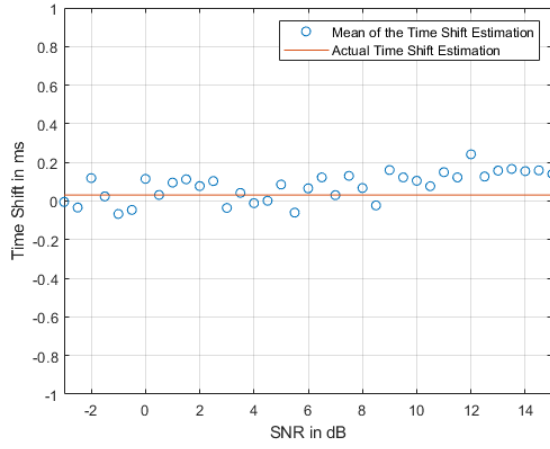


Figure 28: Part 3C Mean Deviation of Time estimate

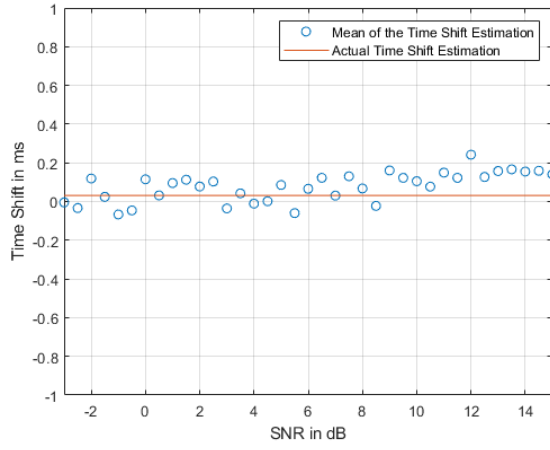


Figure 29: Part 3C Mean Deviation of Freq estimate

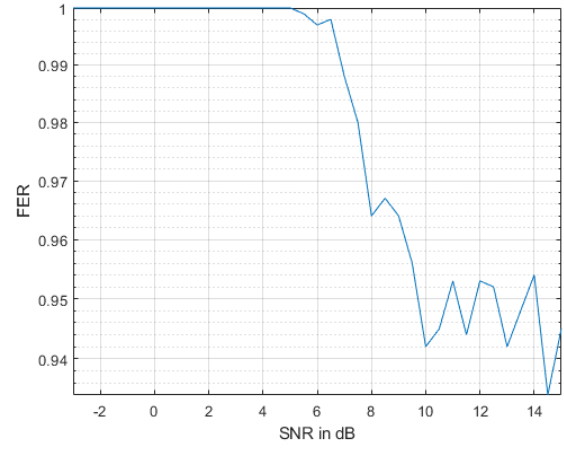


Figure 31: Part 3C FER estimate

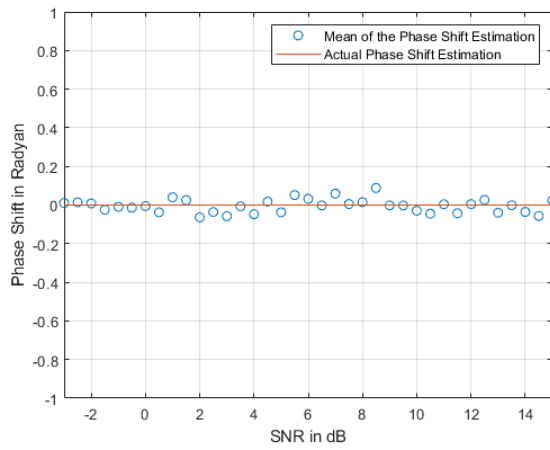


Figure 30: Part 3C Mean Deviation of Phase estimate