

Design Inception

The thinking step
This is where you'll figure out
how something will be
designed

RTL Design

This is where you'll be using
HDL to describe the design you
have thought of

Synthesis

In this step an HDL compiler
will do its magic and convert
your design into registers, look
up tables...

Place and Route

HDL compiler now decides
where to place each
component in limited physical
space using limited resources

} Done by HDL design
toolchain

Verification

Last stage of is verification of
implemented design to ensure
correct working of design

Design complete!