

SUPPLEMENTARY MATERIAL

FREQUENTLY ASKED QUESTIONS

Department of Electronics and Communication Engineering
Indian Institute of Technology, Roorkee

ECN 104

Digital Logic Design

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1 Verilog

1.1 What is an RTL?

RTL (or Register Transfer Level) is a level of abstraction similar to Verilog for describing a circuit.

2 Document History

1. Mon Feb 4 10:42:07 IST 2019: First edition