	0 1 2 3 4 5 6 7 8 9 1 1 1 1 1 1 1 5 5 6 7 8 9 1 1 2 3 4 5 5 6 7 6 7 6 7 7 7 7 7	1 1 1 1 2 2 2 2 3 8 9 0 1 2 3 3 Byte 2 0 1 2 3 4 5 6 7	2 2 2 2 2 3 3 3 4 5 6 7 8 9 0 1 Byte 3 0 1 2 3 4 5 6 7	3 3 3 3 5 Byt		4 4 4 4 4 4 4 6 7 Byte 5 0 1 2 3 4 5 6 7	4 4 5 5 5 5 5 5 5 6 6 6
1	Type 2 Request Class, Dev8 Transport, 34-bit Addressing Packets						
Byte 0 >	ackID S prio tt ftype prio 0 0 0 1 0	destinationID	sourceID	transaction	rdsize	srcTID	Address [0:15]
Byte 8 >	Address [16:28]		CRC			i i	i
	1			i I			
Type 2 Request Class, Dev8 Transport, 50-bit Addressing Packets							
Byte 0 >	ackID S b prio tt ftype 0 0 0 1 0	destinationID	sourceID	transaction	rdsize	srcTID	Extended Address [0:15]
Byte 8 >	Address [0:28]		yd xam sbs	Final CRC			Logic 0 Pad
	Type 2 Request Class, Dev8 Transport, 66-bit Addressing Packets						
Byte 0 >	ackID $\geqslant \stackrel{H}{\Rightarrow} \text{ prio } \begin{array}{c} tt \\ 0 & 0 & 0 & 1 & 0 \end{array}$	destinationID	sourceID	transaction	rdsize	srcTID	Extended Address [0:15]
Byte 8 >	Extended Address [16:31]		28]		td xam	Final CRC	