Bit positions:
```
0 1 2 3 4 5 6 7 | 8 9 10 11 12 13 14 15 | 16 17 18 19 20 21 22 23 | 24 25 26 27 28 29 30 31 | 32 33 34 35 36 37 38 39 | 40 41 42 43 44 45 46 47 | 48 49 50 51 52 53 54 55 | 56 57 58 59 60 61 62 63
Byte 0 | Byte 1 | Byte 2 | Byte 3 | Byte 4 | Byte 5 | Byte 6 | Byte 7
0 1 2 3 4 5 6 7 | 0 1 2 3 4 5 6 7 | 0 1 2 3 4 5 6 7 | 0 1 2 3 4 5 6 7 | 0 1 2 3 4 5 6 7 | 0 1 2 3 4 5 6 7 | 0 1 2 3 4 5 6 7 | 0 1 2 3 4 5 6 7
```

ATOMIC Request Packets

Byte 0 >	ackID	VC	CRF	prio	tt 0 0	ftype 0 1 0 1	destinationID	sourceID	transaction	wrsize	srcTID	Extended Address [0:15]
Byte 8 >	Extended Address [16:31]						Address [0:29]				wdptr	xam sbs	double-word 0 [0:15]
Byte 16 >	double-word 0 [16:63]										Final CRC	

NWRITE, NWRITE_R, CASTOUT, FLUSH with data Request Packets (Where N < 9)

Byte 0 >	ackID	VC	CRF	prio	tt 0 0	ftype 0 1 0 1	destinationID	sourceID	transaction	wrsize	srcTID	Extended Address [0:15]
Byte 8 >	Extended Address [16:31]						Address [0:29]				wdptr	xam sbs	double-word 0 [0:15]
Byte 16 >	double-word 0 [16:63]										double-word 1 [0:15]	
⋮
Byte (16 + (N-2)*8) >	double-word n-1 [16:63]										double-word n [0:15]	
Byte (16 + (N-1)*8) >	double-word n [16:63]										Final CRC	

NWRITE, NWRITE_R, CASTOUT, FLUSH with data Request Packets (Where N = 9)

Byte 0 >	ackID	VC	CRF	prio	tt 0 0	ftype 0 1 0 1	destinationID	sourceID	transaction	wrsize	srcTID	Extended Address [0:15]
Byte 8 >	Extended Address [16:31]						Address [0:29]				wdptr	xam sbs	double-word 0 [0:15]
Byte 16 >	double-word 0 [16:63]										double-word 1 [0:15]	
⋮
Byte 72 >	double-word 7 [16:63]										double-word 8 [0:15]	
Byte 80 >	Early CRC				double-word 8 [16:63]							
Byte 88 >	Final CRC				Logic 0 Pad							

NWRITE, NWRITE_R, CASTOUT, FLUSH with data Request Packets (Where N > 9)

Byte 0 >	ackID	VC	CRF	prio	tt 0 0	ftype 0 1 0 1	destinationID	sourceID	transaction	wrsize	srcTID	Extended Address [0:15]
Byte 8 >	Extended Address [16:31]						Address [0:29]				wdptr	xam sbs	double-word 0 [0:15]
Byte 16 >	double-word 0 [16:63]										double-word 1 [0:15]	
⋮
Byte 72 >	double-word 7 [16:63]										double-word 8 [0:15]	
Byte 80 >	Early CRC				double-word 8 [16:63]							
Byte 88 >	double-word 9 [0:63]											
⋮
Byte (16 + (N-1)*8) >	double-word n [0:63]											
Byte (16 + (N-1)*8) >	Final CRC				Logic 0 Pad							

Notes: N is the number of double-words in the payload.
n = N-1