

Processor

The diagram illustrates a system architecture. At the top, a large square box contains a smaller square box labeled "Processor". A vertical line extends from the bottom of the "Processor" box to the top of a second large square box below it. This second large square box contains a smaller square box labeled "Agent". A vertical line extends from the bottom of the "Agent" box to the text "RapidIO-based System Interconnect". To the right of the vertical line connecting the Processor and Agent boxes, the text "Local Interconnect" is positioned.

Local Interconnect

Agent

RapidIO-based
System Interconnect