# Job Notification Form, IIT Delhi

## Company Overview

**Name:**    Rivos Inc.

**Website:**    https://www.rivosinc.com/

**Company Type:**    Core (Technical)

**Description:**    Rivos is a high performance RISC-V System Startup targeting integrated system solutions for Enterprise. We are a best in class engineering team with strong design, product, manufacturing and financial partnerships. Our team is composed of the brightest minds with diverse cultural and technical backgrounds in silicon, platform and software engineering. We are an open, inclusive and transparent team culture that fosters new ideas and provides lots of opportunities to innovate, learn and grow.

## Job Details

**Designation:**    Silicon Verification - Full-time - (Masters/M.tech)

**Type:**    Core (Technical)

**Place of Posting:**    Bangalore

**Job Details:**    (INDIA) BANGALORE, INDIA /ENGINEERING – SILICON ENGINEERING /FULL-TIME
Positions are open for full-time and Co-op/internship in the areas of CPU and SOC verification from unit level to chip level as well as all aspects of verification such as functional, microarchitecture, performance, and formal.
We are looking for all levels of talent, from entrance to advanced level of experience.
Responsibilities
Work closely with architecture and RTL designers on verifying the functionality correctness of the design
Reviewing Architecture and Design Specifications
Develop test plans and test environments
Develop tests in assembly, C/C++, or vectors according to test plans
Develop coverage monitors and analyze coverage to ensure all the test cases in the plans are covered
Develop checkers in SystemVerilog or C-base transactors to verify the design
Write assertions and apply formal verification to the designImplementing test benches, generating directed/constrained random tests
Debugging failures, running simulations, tracking bugs
Handling schedules and supporting multi-functional engineering effortAssisting in verification flows, automation scripts and regressions
Requirements
In-depth knowledge of digital logic design, CPU/SOC architecture and microarchitecture.
Sophisticated knowledge of SystemVerilog.
Experienced level knowledge C/C++.Relevant knowledge of verification methodologies and tools such as simulators, waveform viewers, build and run automation, coverage collection.

Basic knowledge of formal verification methodology is a plus.
Excellent knowledge of one of the scripting languages such as Python, TCL is a plus.
Excellent skills in problem solving, written and verbal communication, excellent organization skills, and highly self-motivated.
Ability to work well in a team and be productive under aggressive schedules.
Education and Experience
PhD, Master's Degree or Bachelor's Degree in technical subject area.

**Joining By:**     15 June 2023

# Salary Details

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|---|---|
| **CTC:** | 2,100,000 INR Per Annum |
| **Gross:** | 2,100,000 INR Per Annum |
| **Base Salary:** | 2,100,000 INR Per Annum |
| **Joining Bonus:** | 0 INR Per Annum |
| **HRA:** | 0 (min) -0 (max) INR Per Annum |
| **Medical Allowance:** | 0 (min) -0 (max) INR Per Annum |
| **Other cash benefits part of gross:** | 0 (min) -0 (max) INR Per Annum |
| **RSUs:** | 0 INR Per Annum |
| **ESOPs:** | 0 INR Per Annum |
| **Performance/other bonuses:** | 0 (min) -0 (max) INR Per Annum |
| **Other cash benefits part of CTC:** | 0 (min) -0 (max) INR Per Annum |
| **Other Cash Benefits:** | 2L (Retention Stocks) ++ upto 15% performance Bonus ++ ESOPs + + Rivos offers Provident Fund @ 12% on Basic Pay (part of CTC) |

# Selection Process

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|---|---|
| **Resume Shortlist:** | Yes |
| **Written Test:** | No |
| **Online Test:** | No |
| **Group Discussion:** | No |
| **Medical Test:** | No |
| **Personal Interview:** | Yes |

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|---|---|---|
| **No. of Rounds:** | 4 | |
| **No. of Offers:** | 10 | |
| **Minimum CGPA:** | 8 | |

# Eligibility

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|---|---|
| **Recruiting PHDs:** | Yes |
| **Eligible Departments:** | B.Tech in Electrical Engineering, B.Tech in Electrical Engineering (Power and Automation), B.Tech in Engineering Physics, M.Tech in Communications Engineering, M.Tech in Instrument Technology, M.Tech in Integrated Electronics & Circuits, M.Tech in Optoelectronics & Optical Communication, M.Tech in Power Electronics, Electrical Machines & Drives, M.Tech in Power Systems, M.Tech in Radio Frequency Design & Technology, M.Tech in Solid State Materials, M.Tech in VLSI Design Tools & Technology, M.Sc in Physics, M.S.(R) in Electrical Engineering, M.S.(R) in Sensors, Instrumentation and Cyber-physical System Engineering, M.S.(R) in VLSI Design Tools and Technology, B.Tech in Electrical Engineering (Power and Automation) and M.Tech in Computer Science & Engineering |