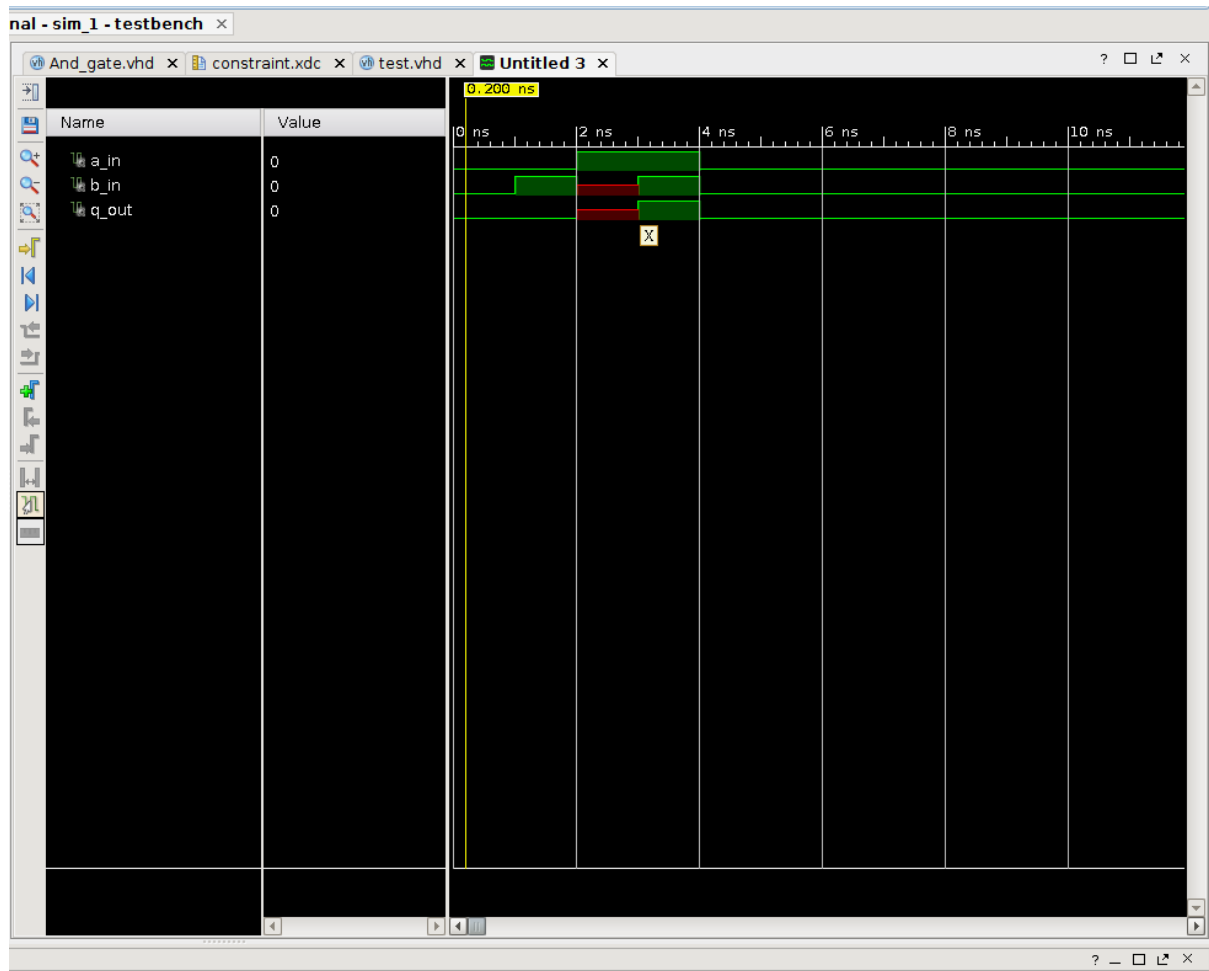


# Assignment-1

## 1) Simulation of AND Gate



## 2) Resource Utilisation

- a) LUT Memory =0
- b) LUT logic = 1
- c) DSP =0
- d) Flip Flops =0
- e) BRAM = 0