

# WORKING OF THE PROJECT

- ❖ First the user inputs 6 random numbers from RAM[0] to RAM[5].

0	7
1	3
2	54
3	6
4	1
5	58

- ❖ Two variables l and i are created at the starting of the program and the values are initialized to 0 and 30 respectively. l is used for iterating from 0 to 5 whereas i is used for iterating from 30

16	0
17	30

- ❖ Any odd number when performed logical AND with 1 will produce an output of 1 and any even number when performed logical AND with 1 will produce an output of 0.

- ❖ Now the emulator takes up values from RAM[0] to RAM[5] and if a value is found out to be odd, it is stored from RAM[30] onwards.

30	7
31	3
32	1

- ❖ Now when the emulator reaches the 6<sup>th</sup> register it will jump to the sorting instructions. i value is changed to 30

16	6
17	30

- ❖ The emulator considers values inside RAM[i] and RAM[i+1]. If RAM[i] is greater than RAM[i+1] the value gets swapped. Upon reaching 0, the emulator checks if the values are sorted, if not it will repeat the sort cycle.

Output after 1<sup>st</sup> cycle

30	3
31	1
32	7

Output after completion

30	1
31	3
32	7

- ❖ If all the values are sorted it will jump to ending infinite loop where the variables l and i are set to 0

16	0
17	0

## PSEUDO CODE

*//TWO VARIABLES i AND l. i IS USED FOR ITERATION FROM 30 . l IS USED FOR ITERATION FROM 0 TO 5 AND FOR STORING VALUES.*

i=30

l=0

go to SAS

*//CHECKING IF THE NUMBER IS ODD OR EVEN*

(SAS)

D=ram(l)

D=D AND 1

IF D==0

JUMP TO LOOP1

IF D==1

JUMP TO LOOP

*//IF EVEN THEN GO TO NEXT REGISTER*

(LOOP1)

D=I

IF D-6 == 0:

GOTO SORT

I=I+1

JUMP TO SAS

*//IF ODD STORE VALUE IN ITH REGISTER TO THE iTH REGISTER*

(LOOP)

A=I

D=RAM[I]

A=i

RAM[i]=D

i=i+1

I=I+1

JUMP TO SAS

*//SORTING BEGINS*

(SORT)

i=30

JUMP TO SORT1

*//SORTING IF RAM[i]>RAM[i+1]*

(SORT1)

D=RAM[i]

if D==0

    jump to check

A=i+1

if M==0

    jump to check

if D-M>0

    JUMP TO SWAP

if D-M<0

    jump to cont

*//SWAPPING VALUES BETWEEN REGISTERS*

(SWAP)

D=RAM[i]

I=RAM[i+1]

A=i+1

M=D

A=i

M=I

i=i+1

jump to SORT1

*//CONTINUING TO THE NEXT REGISTER IF RAM[i]<RAM[i+1]*

(CONT)

i=i+1

jump to SORT1

*//CHECKING IF THE ARRAY IS SORTED*

(CHECK)

```
i=30
GO TO CHECK2
(CHECK2)
D=RAM[i]
A=i+1
if D==0:
    GO TO ENDING
if M==0:
    GO TO ENDING
if D>M
    GO TO SORT
i=i+1
GO TO CHECK2
//ENDING THE PROGRAM WITH INFINITE LOOP
(ENDING)
i=0
l=0
GO TO ENDING
```

## ASM CODE

```
@0
D=A
@l
M=D
```

@30

D=A

@i

M=D

@SAS

(SAS)

@l

A=M

D=M

@1

D=D&A

@LOOP1

D;JEQ

@LOOP

D-1;JEQ

(LOOP)

@l

A=M

D=M

@i

A=M

M=D

@i

M=M+1

@l

M=M+1

```
@SAS
0;JEQ
(LOOP1)
@I
D=M
@6
D=D-A
@SORT
D;JEQ
@I
M=M+1
@SAS
0;JEQ
(SORT)
@30
D=A
@i
M=D
@SORT1
0;JMP
(SORT1)
@i
A=M
D=M
@CHECK
D;JEQ
```

@i

A=M+1

D=M

@CHECK

D;JEQ

@i

A=M

D=M

A=A+1

D=D-M

@SWAP

D;JGT

@i

M=M+1

@SORT1

0;JMP

(SWAP)

@i

D=M+1

@I

A=D

D=M

@I

M=D

@I

M=D



@i

A=M

D=M

@i

A=M+1

M=D

@l

D=M

@i

A=M

M=D

@i

M=M+1

@SORT1

0;JMP

(CHECK)

@30

D=A

@i

M=D

@CHECK2

0;JMP

(CHECK2)

@i

A=M

D=M

@ENDING

D;JEQ

@i

A=M+1

D=M

@ENDING

D;JEQ

@i

A=M

D=M

A=A+1

D=D-M

@SORT

D;JGT

@i

M=M+1

@CHECK2

0;JMP

(ENDING)

@i

M=0

@l

M=0

@ENDING

0;JMP

# OUTPUT

RAM[0] = 7, RAM[1] = 3, RAM[2] = 54, RAM[3] = 6, RAM[4] = 1, RAM[5] = 58

## INPUT SNAPSHOT

CPU Emulator (2.5) - C:\Users\hp\OneDrive\Desktop\academics\EOC\TEST.asm

File View Run Help

Animate: Program flow View: Screen Format: Decimal

ROM Asm

0	@0
1	D=A
2	@16
3	M=D
4	@30
5	D=A
6	@17
7	M=D
8	@9
9	@16
10	A=M
11	D=M
12	@1
13	D=D&A
14	@30
15	D;JEQ
16	@18
17	D-1;JEQ
18	@16
19	A=M
20	D=M
21	@17
22	A=M
23	M=D
24	@17
25	M=M+1
26	@16
27	M=M+1
28	@9

RAM

0	7
1	3
2	54
3	6
4	1
5	58
6	0
7	0
8	0
9	0
10	0
11	0
12	0
13	0
14	0
15	0
16	0
17	0
18	0
19	0
20	0
21	0
22	0
23	0
24	0
25	0
26	0
27	0
28	0

PC 0 A 0

D 0

ALU

D Input : 0

M/A Input : 0

ALU output : 0

Script restarted

## OUTPUT SNAPSHOT

CPU Emulator (2.5) - C:\Users\hp\OneDrive\Desktop\academics\EOC\TEST.asm

File View Run Help

Animate: Program flow View: Screen Format: Decimal

ROM Asm

107	@17
108	A=M
109	D=M
110	A=A+1
111	D=D-M
112	@40
113	D;JGT
114	@17
115	M=M+1
116	@97
117	O;JMP
118	@17
119	M=0
120	@16
121	M=0
122	@118
123	O;JMP
124	
125	
126	
127	
128	
129	
130	
131	
132	
133	
134	
135	

RAM

7	0
8	0
9	0
10	0
11	0
12	0
13	0
14	0
15	0
16	0
17	0
18	0
19	0
20	0
21	0
22	0
23	0
24	0
25	0
26	0
27	0
28	0
29	0
30	1
31	3
32	7
33	0
34	0
35	0

PC 119 A 17

D 0

ALU

D Input : 0

M/A Input : 118

ALU output : 0

## INSIGHTS LEARNED DURING IMPLENTATION

- ❖ The assembly language does not have operators like division, modulo etc , but it is achievable through logical operations like AND NOT etc.
- ❖ Usage of labels while coding in assembly language is a good practise as it helps in the flexibility of the code as well as helps to understand the code.
- ❖ It is a very good practise to write pseudo code before writing the asm file as it helps to reduce the chance of getting errors by a large margin.
- ❖ Identifying logical errors in machine language is a hard and is an almost impossible task. So it is always a good practise to test the code manually by hand before running the code on the emulator.
- ❖ Another advantage that we learned from assembly language is that since it directly works with the hardware we can store specific inputs in the registers according to our choice
- ❖ It also gives us a good picture of how the computer performs the complex tasks such as sorting etc using the ALU

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