## EE2019-Analog Systems and Lab: Tutorial 5

## Nagendra Krishnapura, Shanthi Pavan

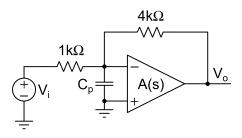


Figure 1: Circuit for problem 1

- 1. Fig. 1 shows an inverting amplifier. The opamp has a gain  $A(s)=A_0/(1+s/p_1)(1+s/p_2)$  where  $A_0=20000,\ p_1=1$  krad/s,  $p_2=10$  Mrad/s.  $C_p$  is a parasitic capacitor.
  - What is the phase margin of the system with  $C_p = 0$ ?
  - What is the closed loop bandwidth of the system? (Calculate this from (a) Unity loop gain frequency, (b) Natural frequency of the second order system, and (c) Exact calculation-computing the frequency at which the gain magnitude drops to 1/√2 times the dc gain.; Compare the estimates so obtained)
  - What is the value of  $C_p$  for which the circuit becomes unstable?
  - With  $C_p$  being the value calculated in the previous part, can you change the circuit so that the phase margin is  $60^{\circ}$  without changing the opamp or the closed-loop dc gain  $V_o/V_i$ ?
- 2. Fig. 2 shows a transimpedance amplifier. The opamp has a frequency independent gain  $A_0$ . The feedback resistor R has a parasitic capacitor C. C is distributed across the length of the resistor and should be modeled as shown in Fig. 2(b) where the infinite number of infinitesimal  $\Delta R$  and  $\Delta C$  sum up to R

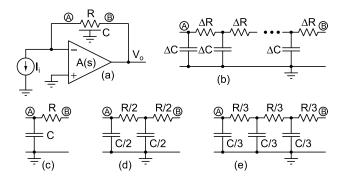


Figure 2: Circuit for problem 2

and C respectively. This cannot be analyzed easily, so we model it as shown in Fig. 2(c), (d), or (e). Analyze each case and comment on the effect of  $A_0$  on stability or damping. (In addition to stability, this problem also tells you something about oversimplified models).

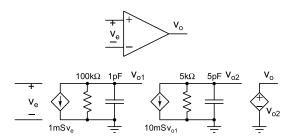


Figure 3: Circuit for problem 3

- 3. Fig. 3 shows the internal schematic of an opamp. This opamp is used to realize a unity gain, non-inverting amplifier.
  - What is the phase margin?
  - Connect a capacitor across one of the existing capacitors inside the opamp so that the phase margin is 60°.

Repeat the above if the opamp is used to realize an inverting amplifier of gain -4.

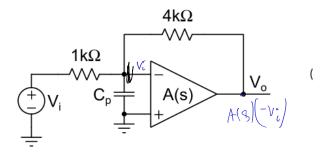


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$$=$$
  $\frac{-4000}{(1+8)(1+8)}$ 

$$\left(1 + \omega^{2}\right) \left(1 + \omega^{2}\right) \left(1 + \omega^{2}\right)$$

lu- 4x 10 reals

What is the closed loop bandwidth of the system? (Calculate this from (a) Unity loop gain frequency, (b) Natural frequency of the second order system, and (c) Exact calculation-computing the frequency at which the gain magnitude drops to 1/√2 times the dc gain.; Compare the estimates so obtained)

$$\left( \frac{1+1}{p_1} \right) \left( \frac{1+1}{p_2} \right) 5 + A_0 = 0$$

$$S = -5 \times 10^{6} \pm 3.873 \times 10^{6}$$

$$chs(s) = 6.35 \times 10^{6}$$

J Exact

$$\frac{V_{0} - A(8)(-V_{1})}{V_{0}^{2} - V_{0}} = \frac{V_{0}^{2} - V_{0}}{L_{0}}$$

$$\frac{4V_{0}^{2} - 4V_{1}}{V_{0}^{2} - V_{0}} = \frac{V_{0}^{2} - V_{0}}{L_{0}^{2}}$$

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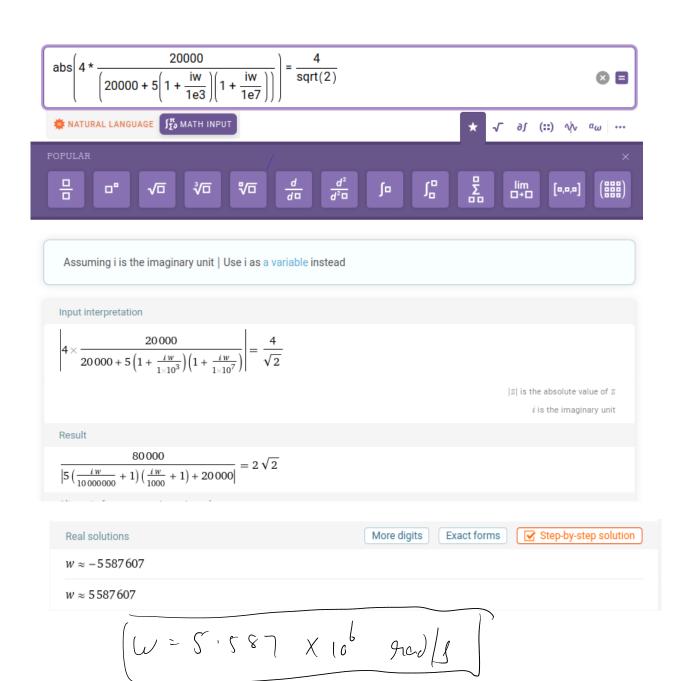
$$\frac{4V_{0}^{2} - 4V_{1}}{L_{0}^{2} - V_{0}} = \frac{V_{0}^{2} - V_{0}}{L_{0}^{2}}$$

$$V_{i} = -V_{0} - V_{0} s$$

$$V_{i} = -(-15) \qquad V_{0} = -\frac{4}{\sqrt{168}}$$

$$V_{i}(s) = -\frac{4}{\sqrt{168}}$$

$$V_{i}$$



 What is the value of C<sub>p</sub> for which the circuit becomes unstable?

$$\frac{4k\Omega}{V_{0}} = \frac{1}{\sqrt{k}} \frac{1$$

occomes unstable:

• With  $C_p$  being the value calculated in the previous part, can you change the circuit so that the phase margin is  $60^{\circ}$  without changing the opamp or the closed-loop dc gain  $V_o/V_i$ ?

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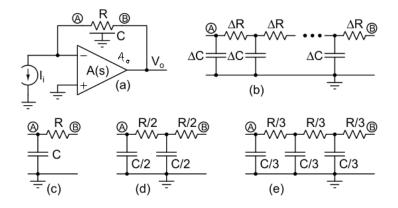
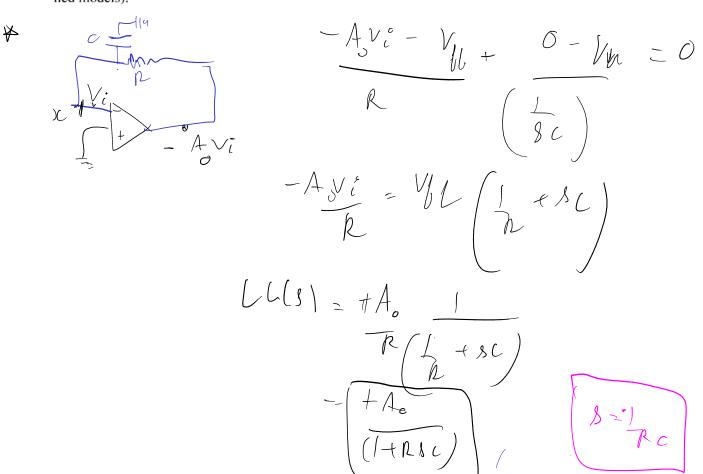


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$$R_{1} = \frac{1}{1} (1) \qquad \qquad \frac{1}{2} (1) \qquad \qquad \frac{1}$$

2RC = - (2+) 16h-4(16) -6 + 255  $\frac{8 - 6 \pm 255}{RC} \simeq -1.527 - 10.4$  $A \frac{y-x}{k_{18}} + \frac{y-x}{k_{18}} + \frac{0-x}{3} = 0$  $\frac{0-y}{318c} + \frac{x-y}{R_{13}} + \frac{0-y}{3+3} = 0$ 2 = h g / 2 t 3 r t 1 R t 2 c |  $-Av = y \left( \frac{3}{2} - \frac{1}{3} \left( \frac{b}{2} + \frac{1}{3} \right) \left( \frac{3}{3} + \frac{1}{3} + \frac{3}{3} \right) \right)$ b+1c)(2c22 + 1 (x13 + 3(8c)

$$\frac{1}{2} = \frac{2}{3} \left( \frac{1}{12} + \frac{86}{3} \right) \left( \frac{86}{3} + \frac{3}{3} + \frac{25}{6464} \right) \left( \frac{9}{4} + \frac{1}{12} \right)$$

$$-\frac{A}{2} \left( \frac{1}{12} + \frac{1}{12} \right) \left( \frac{1}{12} + \frac{1}{12} + \frac{1}{12} + \frac{1}{12} + \frac{1}{12} + \frac{1}{12} + \frac{1}{12} \right)$$

$$= \frac{A}{3} \left( \frac{1}{3} + \frac{1}{3} \right) \left( \frac{1}{12} + \frac{1}{12}$$

become larger of larger.

Dl gain increases by large value, but DC ga!nix lesser, so P.M.-L

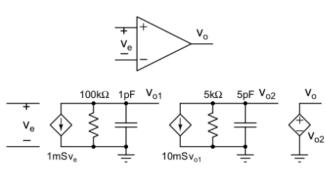


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  This opamp is used to realize a unity gain, noninverting amplifier.
  - · What is the phase margin?
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$$V_0 = V_{02}$$

$$= (lons V_{01})$$

$$= (8C_2 + 1)$$

$$R_2$$

$$\left(\frac{8(1+1)(8(1+1))(8(1+1))}{8(1+1)(8(1+1))(8(1+1))}\right)$$

$$\frac{5000}{\left(07\right)\left(1+\frac{8}{4\times10^{7}}\right)}$$

$$5000 = \frac{2}{614} + 1$$
 $\frac{16\times10^{14}}{16\times10^{14}}$ 

$$25 \times 6^{6} = \left(\frac{1}{100} + 1\right) \left(\frac{1}{100} + 1\right) \left(\frac{1}{100} + 1\right)$$

$$|A(1)| = \frac{56}{200}$$

$$= \frac{1}{200} = \frac{1}$$

 Connect a capacitor across one of the existing capacitors inside the opamp so that the phase margin is 60°.

Let us coment acres frist capacitor

Let C be new concitance

1 = 50

THIS PLANTING

2, d (4) P. 7)7P2 50 at  $\omega_{3} = \sigma_{\times 10^{3} \times 10^{3}}$   $\sigma_{3} = \sigma_{\times 10^{3} \times 10^{3}}$ 60 = 180 - tan ( wg) - tan ( m FC - S3 X10 J = 173021F C1217.32nF

Repeat the above if the opamp is used to realize an inverting amplifier of gain 
$$-4$$
.

 $PR = 60^{\circ} = \frac{1}{100} \left( \frac{1}{100} \right)^{-1} \left( \frac$